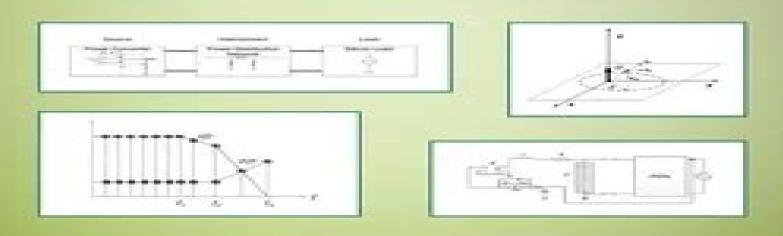
Fundamentals of Power Integrity for Computer Platforms and Systems



J. TED DIBENE II

<u>Fundamentals Of Power Integrity For Computer</u> <u>Platforms And Systems</u>

G Thomas

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Fundamentals of Power Integrity for Computer Platforms and Systems Joseph T. DiBene, II,2014-05-16 An all encompassing text that focuses on the fundamentals of power integrity Power integrity is the study of power distribution from the source to the load and the system level issues that can occur across it For computer systems these issues can range from inside the silicon to across the board and may egress into other parts of the platform including thermal EMI and mechanical With a focus on computer systems and silicon level power delivery this book sheds light on the fundamentals of power integrity utilizing the author's extensive background in the power integrity industry and unique experience in silicon power architecture design and development Aimed at engineers interested in learning the essential and advanced topics of the field this book offers important chapter coverage of fundamentals in power distribution power integrity analysis basics system level power integrity considerations power conversion in computer systems chip level power and more Fundamentals of Power Integrity for Computer Platforms and Systems Introduces readers to both the field of power integrity and to platform power conversion Provides a unique focus on computer systems and silicon level power delivery unavailable elsewhere Offers detailed analysis of common problems in the industry Reviews electromagnetic field and circuit representation Includes a detailed bibliography of references at the end of each chapter Works out multiple example problems within each chapter Including additional appendixes of tables and formulas Fundamentals of Power Integrity for Computer Platforms and Systems is an ideal introductory text for engineers of power integrity as well as those in the chip design industry specifically physical design and packaging Power Integrity for Electrical and Computer Engineers J. Ted Dibene, II, David Hockanson, 2019-09-24 A professional guide to the fundamentals of power integrity analysis with an emphasis on silicon level power integrity Power Integrity for Electrical and Computer Engineers embraces the most recent changes in the field offers a comprehensive introduction to the discipline of power integrity and provides an overview of the fundamental principles Written by noted experts on the topic the book goes beyond most other resources to focus on the detailed aspects of silicon and optimization techniques in order to broaden the field of study This important book offers coverage of a wide range of topics including signal analysis EM concepts for PI frequency domain analysis for PI numerical methods overview for PI and silicon device PI modeling Power Integrity for Electrical and Computer Engineers examine platform technologies system considerations power conversion system level modeling and optimization methodologies To reinforce the material presented the authors include example problems This important book Includes coverage on convergence accuracy and error analysis and explains how these can be used to analyze power integrity problems Contains information for modeling the power converter from the PDN to the load in a full system level model Explores areas of device level modeling of silicon as related to power integrity Contains example word problems that are related to an individual chapter's subject Written for electrical and computer engineers and academics Power Integrity for Electrical and Computer

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