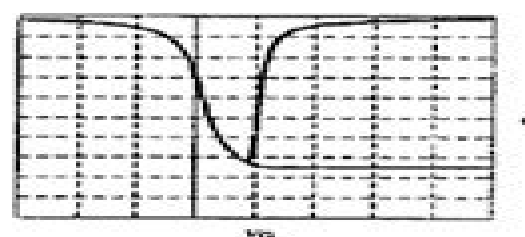
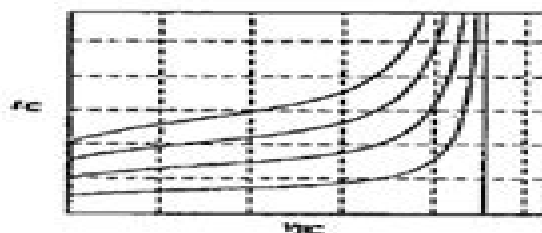
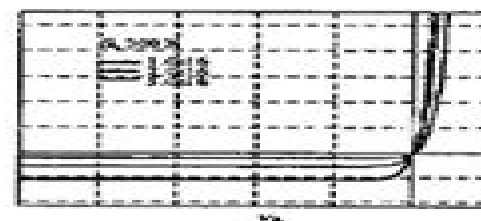
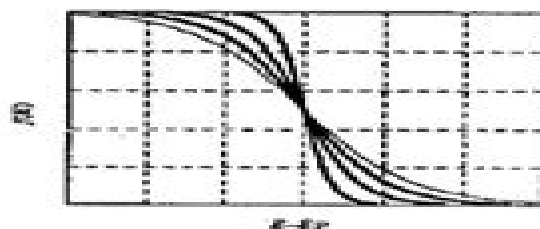


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Analysis and Design of MOSFETs Juin Jei Liou, Adelmo Ortiz-Conde, Francisco Garcia-Sanchez, 2012-12-06 Analysis and Design of MOSFETs Modeling Simulation and Parameter Extraction is the first book devoted entirely to a broad spectrum of analysis and design issues related to the semiconductor device called metal oxide semiconductor field effect transistor MOSFET These issues include MOSFET device physics modeling numerical simulation and parameter extraction The discussion of the application of device simulation to the extraction of MOSFET parameters such as the threshold voltage effective channel lengths and series resistances is of particular interest to all readers and provides a valuable learning and reference tool for students researchers and engineers Analysis and Design of MOSFETs Modeling Simulation and Parameter Extraction extensively referenced and containing more than 180 illustrations is an innovative and integral new book on MOSFETs design technology *Lithium-ion Battery Materials and Engineering* Malgorzata K. Gulbinska, 2014-09-06 Gaining

public attention due in part to their potential application as energy storage devices in cars Lithium ion batteries have encountered widespread demand however the understanding of lithium ion technology has often lagged behind production This book defines the most commonly encountered challenges from the perspective of a high end lithium ion manufacturer with two decades of experience with lithium ion batteries and over six decades of experience with batteries of other chemistries Authors with years of experience in the applied science and engineering of lithium ion batteries gather to share their view on where lithium ion technology stands now what are the main challenges and their possible solutions The book contains real life examples of how a subtle change in cell components can have a considerable effect on cell s performance Examples are supported with approachable basic science commentaries Providing a unique combination of practical know how with an in depth perspective this book will appeal to graduate students young faculty members or others interested in the current research and development trends in lithium ion technology **Thermal Design** H. S. Lee, 2010-11-23 Thermal Design Heat Sinks Thermoelectrics Heat Pipes Compact Heat Exchangers and Solar Cells Second Edition is a significantly updated new edition which now includes a chapter on thermoelectrics It covers thermal devices such as heat sinks

thermoelectric generators and coolers heat pipes and heat exchangers as design components in larger systems These devices are becoming increasingly important and fundamental in thermal design across such diverse areas as microelectronic cooling green or thermal energy conversion and thermal control and management in space The underlying concepts in this book cover the understanding of the physical mechanisms of the thermal devices with the essential formulas and detailed derivations and also the design of the thermal devices in conjunction with mathematical modeling graphical optimization and occasionally computational fluid dynamic CFD simulation This new edition includes more examples problems and tutorials and a solutions manual is available on a companion website

Multi-Threshold CMOS Digital Circuits Mohab

Anis, Mohamed Elmasry, 2012-12-06 Multi Threshold CMOS Digital Circuits Managing Leakage Power discusses the Multi threshold voltage CMOS MTCMOS technology that has emerged as an increasingly popular technique to control the escalating leakage power while maintaining high performance The book addresses the leakage problem in a number of designs for combinational sequential dynamic and current steering logic Moreover computer aided design methodologies for designing low leakage integrated circuits are presented The book give an excellent survey of state of the art techniques presented in the literature as well as proposed designs that minimize leakage power while achieving high performance Multi Threshold CMOS Digital Circuits Managing Leakage Power is written for students of VLSI design as well as practicing circuit designers system designers CAD tool developers and researchers It assumes a basic knowledge of digital circuit design and device operation and covers a broad range of circuit design techniques

Analysis of Piezoelectric Semiconductor

Structures Jiashi Yang, 2020-06-19 This book presents the mechanics of piezoelectric semiconductor structures where the main electromechanical coupling of interest is the interaction between mechanical fields and semiconduction This volume stands as the first full book treatment of this multi physical subject from the mechanics angle The analysis of piezoelectric semiconductor structures and devices is an emerging and rapidly growing interdisciplinary area involving materials electronics and solid mechanics It has direct applications in the new area of piezotronics and piezo phototronics The book is theoretical beginning with a phenomenological framework and progressing to include solutions to problems fundamental to the theory and application Dr Yang illustrates how in piezoelectric semiconductors mechanical fields interact with semiconduction through the piezoelectrically produced electric fields by mechanical loads This provides the foundation of piezotronic and piezo phototronic devices in which semiconduction is induced affected manipulated or controlled by mechanical fields Also discussing composite structures of piezoelectric dielectrics and nonpiezoelectric semiconductors as well as thermal effects the book is an ideal basic reference on the topic for researchers

Single-chip Bluetooth

Solutions Sudeepto Chakraborty, 2001 **Charge Transport in Low Dimensional Semiconductor Structures** Vito Dario Camiola, Giovanni Mascali, Vittorio Romano, 2020-03-02 This book offers from both a theoretical and a computational perspective an analysis of macroscopic mathematical models for description of charge transport in electronic devices in

particular in the presence of confining effects such as in the double gate MOSFET. The models are derived from the semiclassical Boltzmann equation by means of the moment method and are closed by resorting to the maximum entropy principle. In the case of confinement, electrons are treated as waves in the confining direction by solving a one-dimensional Schrödinger equation, obtaining subbands, while the longitudinal transport of subband electrons is described semiclassically. Limiting energy transport and drift-diffusion models are also obtained by using suitable scaling procedures. An entire chapter in the book is dedicated to a promising new material like graphene. The models appear to be sound and sufficiently accurate for systematic use in computer-aided design simulators for complex electron devices. The book is addressed to applied mathematicians, physicists, and electronic engineers. It is written for graduate or PhD readers, but the opening chapter contains a modicum of semiconductor physics, making it self-consistent and useful also for undergraduate students.

Semiconductor fundamentals Robert F. Pierret, 1983 *Scaled Planar Floating-gate NAND Flash Memory*

Technology Shyam Sunder Raghunathan, 2010. NAND flash memories are ubiquitous in their use as portable storage media in cellphones, cameras, music players, and other portable electronic devices. The NAND flash memory device, consisting of a floating-gate transistor cell, is the most aggressively scaled electronic device as evidenced by ever-increasing memory capacities. In this work, we will examine possible problems arising from continued scaling of these structures and discuss novel solutions to overcome them. Firstly, we investigate scaling of the conventional poly-silicon floating-gate aimed at reducing cell-to-cell interference. We experimentally delineate a new reliability concern for the first time with programming current through ultra-thin poly-silicon floating-gates becoming increasingly ballistic. We also experimentally demonstrate doping-related issues in the poly-silicon floating-gate. We then apply a novel metal-based floating-gate cell for the first time designed to overcome the problems discussed above. We explore factors that influence the choice of metal and demonstrate excellent functionality in ultra-thin metal floating-gate cells scaled down to 3 nm TiN floating-gate thickness, thus greatly reducing cell-to-cell interference. Finally, in order to facilitate continued scaling of the control dielectric, we explore replacement of the conventional silicon oxide nitride dielectric with high- k dielectric materials. We integrate poly-silicon and metal floating-gate cells with Al₂O₃ high- k control dielectric. Further, we establish that a deeper work-function control gate is helpful in reducing gate injection. Combining ultra-thin metal floating-gate, high- k control dielectric, and deep work-function control gate, we enable the planar floating-gate cell as a scalable candidate.

Silicon-Plasmonic Photodetectors for High-Speed Signal Processing Mühlbrandt, Sascha Johannes, 2025-04-01. This book introduces and experimentally demonstrates a novel approach to exploit internal photoemission (IPE) for photodetection in silicon. The resulting device represents the first IPE-based detector that operates at technically viable performance figures. It combines a compact footprint and a high bandwidth with monolithic cointegration in silicon photonic circuits. The new concept opens an attractive route for optoelectronic generation and detection of waveforms in the sub-THz and THz range.

Future Trends in

Microelectronics Serge Luryi, Jimmy Xu, Alexander Zaslavsky, 2010-08-03 In the summer of 2009 leading professionals from industry government and academia gathered for a free spirited debate on the future trends of microelectronics This volume represents the summary of their valuable contributions Providing a cohesive exploration and holistic vision of semiconductor microelectronics this text answers such questions as What is the future beyond shrinking silicon devices and the field effect transistor principle Are there green pastures beyond the traditional semiconductor technologies This resource also identifies the direction the field is taking enabling microelectronics professionals and students to conduct research in an informed profitable and forward looking fashion Proceedings of the 2003 ASME Summer Heat Transfer Conference ,2003

Journal of Heat Transfer ,2005 **Semiconductor Fundamentals** Robert F. Pierret,1983 *Semiconductor Fundamentals* Robert F. Pierret,1988-01-01 **Fundamentals of Semiconductor Devices** Edward S. Yang,1978

Oxygen Ion and Mixed Conductors and Their Technological Applications H.L. Tuller,Johannes Schoonman,Ilan Riess,2000-10-31 Progress in the development of oxygen ion and mixed conductors is responsible for innovations in gas sensors fuel cells oxygen permeation membranes oxygen pumps and electrolyzers Commercialization has been impeded by material stability and compatibility issues high fabrication costs and an inadequate understanding of the interfacial phenomena controlling the operation of the devices Here a group of experts cover all the key topical areas ranging from fundamentals relating to a defects electrochemical and interfacial processes b catalysis electrocatalysis and gas reforming to design and fabrication including c advanced electroceramic processing methods d materials selection and optimization e and applications including scale up commercialization and competitive technologies Readership Materials scientists chemists physicists and chemical and electrical engineers either first entering the field or active within it *Extreme Environment Electronics* John D. Cressler,H. Alan Mantooth,2017-12-19 Unfriendly to conventional electronic devices circuits and systems extreme environments represent a serious challenge to designers and mission architects The first truly comprehensive guide to this specialized field *Extreme Environment Electronics* explains the essential aspects of designing and using devices circuits and electronic systems intended to operate in extreme environments including across wide temperature ranges and in radiation intense scenarios such as space The *Definitive Guide to Extreme Environment Electronics* Featuring contributions by some of the world s foremost experts in extreme environment electronics the book provides in depth information on a wide array of topics It begins by describing the extreme conditions and then delves into a description of suitable semiconductor technologies and the modeling of devices within those technologies It also discusses reliability issues and failure mechanisms that readers need to be aware of as well as best practices for the design of these electronics Continuing beyond just the paper design of building blocks the book rounds out coverage of the design realization process with verification techniques and chapters on electronic packaging for extreme environments The final set of chapters describes actual chip level designs for applications in energy and space exploration Requiring only a basic background in

electronics the book combines theoretical and practical aspects in each self contained chapter Appendices supply additional background material With its broad coverage and depth and the expertise of the contributing authors this is an invaluable reference for engineers scientists and technical managers as well as researchers and graduate students A hands on resource it explores what is required to successfully operate electronics in the most demanding conditions

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