

Single-chip Type with Built-in FET Switching Regulator Series



Step-up and inverted 2-channel DC/DC converter with Built-in Power MOSFET

RD62140DRL

Description

This semiconductor is designed with integrated step-up and step-down regulators with integrated internal high-side MOSFETs. When using input voltage range of 0.5~5.5V, it is suitable for applications of portable device. In addition, this small package design is ideal for miniaturizing the power supply.

Features

- Wide input voltage range of 0.5V to 5.5V
- High frequency operation: 1 MHz
- Independent Non-FET or MOSFET output
- Independent built-in MOSFET or MOSFET/BJT output
- Independent MOSFET output (Built-in MOSFET) for step-up output
- Small package (QFN24) with 1.0mm pitch, 4 x 4 mm, 0.4mm height
- On-chip protection (OVP,SCP,OTP,OTP)

Important Specifications

■ Input voltage range	0.5V~5.5V
■ Output voltage range	Fixed voltage mode: 1.8V~5.0V
■ Output current	0.5V~5.0V
■ Maximum current	1.0A
■ Load regulation	1.0% (typical)
■ Full FET output mode	1.0A (typical)
■ Standby current	1.0A (typical)

Package

QFN24 (4 x 4 mm)
1.0mm pitch (0.4mm height)

Applications

- IoT devices
- Wearable devices
- Portable devices that are represented by medical systems and others

Typical Application: Fixed 1.8V to 5.0V output voltage (1.8V output)



Fig. 1 Typical application

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F. F. Mazda



Single Chip Built In Fet Type Switching Regulator Series:

Sputtering Materials for VLSI and Thin Film Devices Jaydeep Sarkar, 2010-12-13 An important resource for students engineers and researchers working in the area of thin film deposition using physical vapor deposition e g sputtering for semiconductor liquid crystal displays high density recording media and photovoltaic device e g thin film solar cell manufacturing This book also reviews microelectronics industry topics such as history of inventions and technology trends recent developments in sputtering technologies manufacturing steps that require sputtering of thin films the properties of thin films and the role of sputtering target performance on overall productivity of various processes Two unique chapters of this book deal with productivity and troubleshooting issues The content of the book has been divided into two sections a the first section Chapter 1 to Chapter 3 has been prepared for the readers from a range of disciplines e g electrical chemical chemistry physics trying to get an insight into use of sputtered films in various devices e g semiconductor display photovoltaic data storage basic of sputtering and performance of sputtering target in relation to productivity and b the second section Chapter 4 to Chapter 8 has been prepared for readers who already have background knowledge of sputter deposition of thin films materials science principles and interested in the details of sputtering target manufacturing methods sputtering behavior and thin film properties specific to semiconductor liquid crystal display photovoltaic and magnetic data storage applications In Chapters 5 to 8 a general structure has been used i e a description of the applications of sputtered thin films sputtering target manufacturing methods including flow charts sputtering behavior of targets e g current voltage relationship deposition rate and thin film properties e g microstructure stresses electrical properties in film particles While discussing these topics attempts have been made to include examples from the actual commercial processes to highlight the increased complexity of the commercial processes with the growth of advanced technologies In addition to personnel working in industry setting university researchers with advanced knowledge of sputtering would also find discussion of such topics e g attributes of target design chamber design target microstructure sputter surface characteristics various troubleshooting issues useful Unique coverage of sputtering target manufacturing methods in the light of semiconductor displays data storage and photovoltaic industry requirements Practical information on technology trends role of sputtering and major OEMs Discussion on properties of a wide variety of thin films which include silicides conductors diffusion barriers transparent conducting oxides magnetic films etc Practical case studies on target performance and troubleshooting Essential technological information for students engineers and scientists working in the semiconductor display data storage and photovoltaic industry *Electronics Engineer's Reference Book* F. F. Mazda, 2013-10-22 Electronics Engineer s Reference Book Sixth Edition is a five part book that begins with a synopsis of mathematical and electrical techniques used in the analysis of electronic systems Part II covers physical phenomena such as electricity light and radiation often met with in electronic systems Part III contains chapters on basic electronic components and materials the building blocks of any

electronic design Part IV highlights electronic circuit design and instrumentation The last part shows the application areas of electronics such as radar and computers **Japanese Technical Abstracts** ,1987 **Japan Electronics Almanac** ,1988

NBS Technical Note ,1973 **Electronics World + Wireless World** ,1994 *Electronic Design* ,2002 **Complex Digital Hardware Design** Istvan Nagy,2024-05-09 This book is about how to design the most complex types of digital circuit boards used inside servers routers and other equipment from high level system architecture down to the low level signal integrity concepts It explains common structures and subsystems that can be expanded into new designs in different markets The book is targeted at all levels of hardware engineers There are shorter lower level introductions to every topic while the book also takes the reader all they way to the most complex and most advanced topics of digital circuit design layout design analysis and hardware architecture NASA Tech Briefs ,1990 Government Reports Announcements & Index ,1987-12

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