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Progress in Cryptology -- INDOCRYPT 2014 Willi Meier, Debdeep Mukhopadhyay, 2014-10-24 This book constitutes the refereed proceedings of the 15th International Conference on Cryptology in India INDOCRYPT 2014 held in New Delhi India in December 2014 The 25 revised full papers presented together with 4 invited papers were carefully reviewed and selected from 101 submissions The papers are organized in topical sections on side channel analysis theory block ciphers

cryptanalysis efficient hardware design protected hardware design elliptic curves **MIPS Pipeline Cryptoprocessor** Kirat Pal Singh, 2012-11-01 The design and implementation of a crypto processor based on Cryptographic algorithms can be used in wide range of electronic devices include PCs PDAs hardware security modules web servers etc The growing problem of breaches in information security in recent years has created a demand for earnest efforts towards ensuring security in electronic processors The successful deployment of these electronic processors for ecommerce Internet banking government online services VPNs mobile commerce etc are dependent on the effectiveness of the security solutions These security concerns are further compounded when resource constrained environments and real time speed requirements have to be considered in next generation applications Consequently these IT and Network security issues have been a subject of intensive research in areas of computing networking and cryptography these last few years Computational methodologies computer arithmetic and encryption algorithms need deep investigation and research to obtain efficient integrations of crypto processors with desirable improvements and optimizations Approaches on silicon achieve high values of speed and bandwidth **Design based Research** Kirat Pal Singh, Author Impact

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