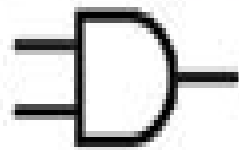
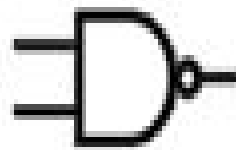


Basic Digital Logic Gates

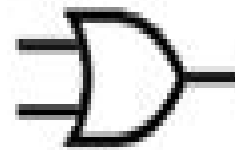
INPUT		OUTPUT
A	B	
0	0	0
1	0	0
0	1	0
1	1	1



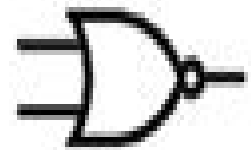
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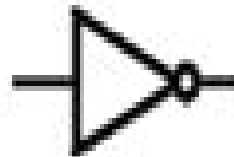
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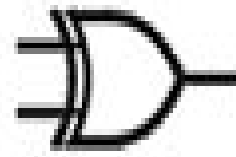
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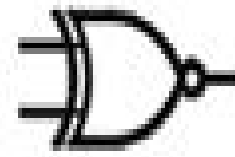
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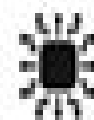
XOR



XNOR



A AND B	$A \cdot B$
A OR B	$A + B$
NOT A	$\overline{A}$
A XOR B	$A \oplus B$



Digital Logic Circuits

Atul P. Godse, Dr. Deepali A. Godse



Digital Logic Circuits:

Digital Electronics 1 Tertulien Ndjountche, 2016-06-20 The omnipresence of electronic devices in our everyday lives has been accompanied by the downscaling of chip feature sizes and the ever increasing complexity of digital circuits This book is devoted to the analysis and design of digital circuits where the signal can assume only two possible logic levels It deals with the basic principles and concepts of digital electronics It addresses all aspects of combinational logic and provides a detailed understanding of logic gates that are the basic components in the implementation of circuits used to perform functions and operations of Boolean algebra Combinational logic circuits are characterized by outputs that depend only on the actual input values Efficient techniques to derive logic equations are proposed together with methods of analysis and synthesis of combinational logic circuits Each chapter is well structured and is supplemented by a selection of solved exercises covering logic design practices

Digital Logic Circuits using VHDL Atul P. Godse, Dr. Deepali A. Godse, 2021-01-01 The book is written for an undergraduate course on digital electronics The book provides basic concepts procedures and several relevant examples to help the readers to understand the analysis and design of various digital circuits It also introduces hardware description language VHDL The book teaches you the logic gates logic families Boolean algebra simplification of logic functions analysis and design of combinational circuits using SSI and MSI circuits and analysis and design of the sequential circuits This book provides in depth information about multiplexers de multiplexers decoders encoders circuits for arithmetic operations various types of flip flops counters and registers It also covers asynchronous sequential circuits memories and programmable logic devices

Digital Logic Circuits (As Per Anna University) A.K. Singh, 2007 **Fundamentals and Applications of Digital Logic Circuits** Sol Libes, 1975 **Digital Electronics Demystified** Myke Predko, 2005-01-11 The field of teaching digital electronics has not changed significantly in the past 20 years Many of the same books that first became available in the late 1970s and early 1980s are still being used as basic texts In the 20 years since these were written the basic rules have not changed but they do not provide strong links to modern electronics including CMOS logic Programmable Logic Devices and microprocessor microcontroller interfacing Courses teaching introductory digital electronics will fill in the missing areas of information for students but neither the instructors nor students have resources to explain modern technology and interfaces One assumption made by all the standard texts is that experimenting with digital electronics cannot be done easily in the proposed book digital guru Myke Predko will show how readers can set up their own apparatus for experimenting with digital electronics for less than 10

Fundamentals of Digital Logic with VHDL Design with CD-ROM Stephen Brown, Zvonko Vranesic, 2008-04-14 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then

a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera s Quartus II CAD software is included free with every text *Digital Logic Techniques, 3rd Edition* John Stonham,1996-05-19 The third edition of Digital Logic Techniques provides a clear and comprehensive treatment of the representation of data operations on data combinational logic design sequential logic computer architecture and practical digital circuits A wealth of exercises and worked examples in each chapter give students valuable experience in applying the concepts and techniques discussed Beginning with an objective comparison between analogue and digital representation of data the author presents the Boolean algebra framework for digital electronics develops combinational logic design from first principles and presents cellular logic as an alternative structure more relevant than canonical forms to VLSI implementation He then addresses sequential logic design and develops a strategy for designing finite state machines giving students a solid foundation for more advanced studies in automata theory The second half of the book focuses on the digital system as an entity Here the author examines the implementation of logic systems in programmable hardware outlines the specification of a system explores arithmetic processors and elucidates fault diagnosis The final chapter examines the electrical properties of logic components compares the different logic families and highlights the problems that can arise in constructing practical hardware systems **Digital Electronics 2** Tertulien Ndjountche,2016-08-16 As electronic devices become increasingly prevalent in everyday life digital circuits are becoming even more complex and smaller in size This book presents the basic principles of digital electronics in an accessible manner allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits Providing a hands on approach this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits Each chapter is supplemented with practical examples and well designed exercises with worked solutions This second of three volumes focuses on sequential and arithmetic logic circuits It covers various aspects related to the following topics latch and flip flop binary counters shift registers arithmetic and logic circuits digital integrated circuit technology semiconductor memory programmable logic circuits Along with the two accompanying volumes this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers **Digital Logic Circuit Analysis and Design** Victor Peter Nelson,1995 For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore or junior level Many recent texts place instructors in the difficult position of choosing between authoritative state of the art coverage and an approach that is highly supportive of

student learning This carefully developed text was widely praised by reviewers for both its great clarity and its rigor The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language and has abundant coverage of current topics of interest such as programmable devices computer aided design and testability An unusually large number of illustrations examples and problems help students gain a solid sense of how theory underlies practice *Digital Logic Techniques* T. J. Stonham,1987 **Fundamentals of Digital Logic with Verilog Design**

Stephen Brown,Zvonko Vranesic,2013-03-15 *Fundamentals of Digital Logic With Verilog Design* teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples Use of CAD software is well integrated into the book A CD ROM that contains Altera s Quartus CAD software comes free with every copy of the text The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials *A Systematic Approach to Digital Logic Design* Frederic J. Mowle,1976 Number systems Base R arithmetic Boolean algebra Special boolean functions and basic logic conventions Minimization procedures for boolean function Binary arithmetic units Decimal arithmetic Introduction to sequential circuit design Practical flip flop circuits Binary counters Register design techniques Advanced arithmetic units **Digital Logic and Computer Design** M. Morris Mano,1979

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Vranesic,2001-12 CD ROM contains Altera MAX plus II CAD software VHDL examples presented in text **Introduction to Logic Circuits & Logic Design with Verilog** Brock J. LaMeres,2017-04-17 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to

match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning Goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome

An Introduction to Logic Circuit Testing Parag K. Lala, 2009 An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits systems The material covered in the book should be sufficient for a course or part of a course in digital circuit testing for senior level undergraduate and first year graduate students in Electrical Engineering and Computer Science The book will also be a valuable resource for engineers working in the industry This book has four chapters Chapter 1 deals with various types of faults that may occur in very large scale integration VLSI based digital circuits Chapter 2 introduces the major concepts of all test generation techniques such as redundancy fault coverage sensitization and backtracking Chapter 3 introduces the key concepts of testability followed by some ad hoc design for testability rules that can be used to enhance testability of combinational circuits Chapter 4 deals with test generation and response evaluation techniques used in BIST built in self test schemes for VLSI chips Table of Contents Introduction Fault Detection in Logic Circuits Design for Testability Built in Self Test References

Digital Principles and Logic Design Arijit Saha, Nilotpal Manna, 2009-01-28 This text and reference provides students and practicing engineers with an introduction to the classical methods of designing electrical circuits but incorporates modern logic design techniques used in the latest microprocessors microcontrollers microcomputers and various LSI components The book provides a review of the classical methods e g the basic concepts of Boolean algebra combinational logic and sequential logic procedures before engaging in the practical design approach and the use of computer aided tools The book is enriched with numerous examples and their solutions over 500 illustrations and includes a CD ROM with simulations additional figures and third party software to illustrate the concepts discussed in the book

Digital Logic Fred Hilsenrath, Bill Pierce, 1988-01-01 **Introduction to Logic Circuits & Logic Design with VHDL** Brock J. LaMeres, 2019-03-19 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a

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