Digital Electronics

Answer Sheet 4

- 1 $A + B = \overline{A \cdot B}$
- 2 (A,B).(C.D)
- 3 a) $= \overline{((A+B)+C)} = \overline{(A+B+C)}$
 - b) = $A.B + A.C + C + \overline{A}.B.\overline{C} = A.B + C + \overline{A}.B = B.(A + \overline{A}) + C = B + C$
 - c) = $A \cdot \overline{A} \cdot B + \overline{B} \cdot \overline{A} \cdot B = 0$
 - d) = $A.(\overline{B}.C + \overline{C}) + B.C = A.(\overline{B} + \overline{C}) + B.C = A.(\overline{B}.\overline{C}) + B.C = A + B.C$
- 4.

$$\overline{AC} + A\overline{C} + BC = \overline{C}(A + \overline{A}) + BC$$

 $= \overline{C} + BC$
 $= \overline{C} \cdot \overline{B} + \overline{C}B + BC$
 $= \overline{C} \cdot \overline{B} + \overline{C}B + \overline{C}B + BC$
 $= \overline{C}(\overline{B} + B) + B(\overline{C} + C)$
 $= \overline{C} + B$

of you might be able to see that $\overline{C} + BC = \overline{C} + B$ directly or from a truth table.

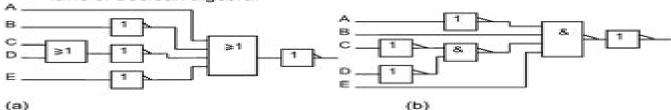
 To show this, we need to simplify the logic, using Boolean Algebra, so as to write the expression in the form XYZ where X, Y and Z are each one of the variables or its complement.

$$A(\overline{B} + A\overline{C}) = A.B.\overline{A.C}$$

$$= AB(\overline{A} + C)$$

$$= AB\overline{A} + ABC$$
for any A.
$$= ABC$$

The first thing to notice is that the two AND gates can be merged to form a four input AND gate. The solutions are then obtained by shuffling INVERTORS so that pairs of INVERTORS cancel and by applying (in a schematical sense) the laws of Boolean algebra.



Digital Electronics Exam Questions With Answers

Jessica J Manson

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FCS Electronic Control & Digital Electronics L3 Julia Pilbeam, 2008 **Experimental Physics Walter Fox** Smith, 2020-03-18 This textbook provides the knowledge and skills needed for thorough understanding of the most important methods and ways of thinking in experimental physics. The reader learns to design assemble and debug apparatus to use it to take meaningful data and to think carefully about the story told by the data Key Features Efficiently helps students grow into independent experimentalists through a combination of structured yet thought provoking and challenging exercises student designed experiments and guided but open ended exploration Provides solid coverage of fundamental background information explained clearly for undergraduates such as ground loops optical alignment techniques scientific communication and data acquisition using LabVIEW Python or Arduino Features carefully designed lab experiences to teach fundamentals including analog electronics and low noise measurements digital electronics microcontrollers FPGAs computer interfacing optics vacuum techniques and particle detection methods Offers a broad range of advanced experiments for each major area of physics from condensed matter to particle physics Also provides clear guidance for student development of projects not included here Provides a detailed Instructor's Manual for every lab so that the instructor can confidently teach labs outside their own research area Digital Electronics Through Project Analysis Ronald A. Reis, 1991 An introductory text to digital circuits for beginning electronics students which provides coverage of basic digital concepts and includes 46 actual digital projects that illustrate concrete applications Coverage encompasses digital combinational and sequential logic General Knowledge & English NDA/CDS Col Ajit Singh, 2021-08-15 This book deals with the whole gamut of circuits General Knowledge and English that an aspirant requires to prepare for NDA CDS and any other Graduate and above level exam held by UPSC As it contains detailed notes on Indian History Geography and Indian Polity followed by MCQs that have appeared in various competitive exams it would prove to be very useful for other competitive exams as well Besides notes on each topic it has over 7000 Multiple Choice Questions MCQs **Digital Electronics** Robert D. Thompson, 2001 Approaching the task of learning digital electronics operation from a developmental approach rather than relying on antiquated rote memorization this user friendly quide emphasizes the use of developmental techniques to derive the knowledge necessary to understand operational and design concepts Employs many innovative ideas to simplify understanding of digital concepts enlightening readers with wisdom gained from over thirty years of author's electronics experience in government academia and industry Takes a developmental approach to show how logic gates operate

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