

Handbook of Semiconductor Manufacturing Technology

Second Edition

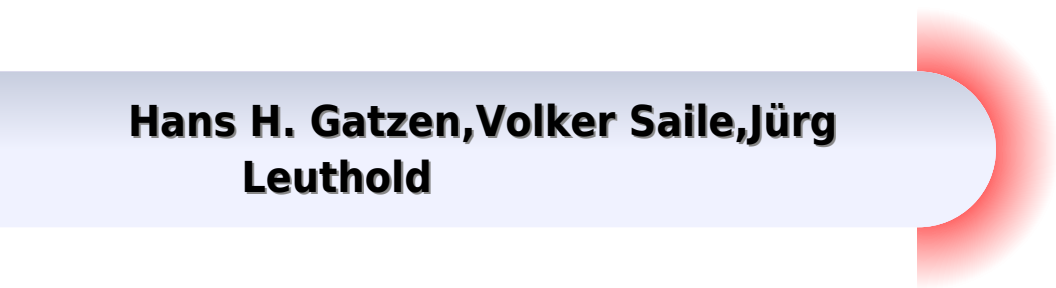


Edited by
Robert E. Hoerling
Yoshio Nishii

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Handbook Of Semiconductor Manufacturing Technology Second Edition

**Hans H. Gatzert, Volker Saile, Jürg
Leuthold**



Handbook Of Semiconductor Manufacturing Technology Second Edition:

Handbook of Semiconductor Manufacturing Technology Yoshio Nishi, Robert Doering, 2017-12-19 Retaining the comprehensive and in depth approach that cemented the bestselling first edition's place as a standard reference in the field the Handbook of Semiconductor Manufacturing Technology Second Edition features new and updated material that keeps it at the vanguard of today's most dynamic and rapidly growing field Iconic experts Robert Doering and Yoshio Nishi have again assembled a team of the world's leading specialists in every area of semiconductor manufacturing to provide the most reliable authoritative and industry leading information available Stay Current with the Latest Technologies In addition to updates to nearly every existing chapter this edition features five entirely new contributions on Silicon on insulator SOI materials and devices Supercritical CO₂ in semiconductor cleaning Low dielectrics Atomic layer deposition Damascene copper electroplating Effects of terrestrial radiation on integrated circuits ICs Reflecting rapid progress in many areas several chapters were heavily revised and updated and in some cases rewritten to reflect rapid advances in such areas as interconnect technologies gate dielectrics photomask fabrication IC packaging and 300 mm wafer fabrication While no book can be up to the minute with the advances in the semiconductor field the Handbook of Semiconductor Manufacturing Technology keeps the most important data methods tools and techniques close at hand

Handbook of Semiconductor Manufacturing Technology Robert Doering, Yoshio Nishi, 2008 *Semiconductor Manufacturing Handbook* Hwaiyu Geng, 2005-05-18 WORLD CLASS SEMICONDUCTOR MANUFACTURING EXPERTISE AT YOUR FINGERTIPS This is a comprehensive reference to the semiconductor manufacturing process and ancillary facilities from raw material preparation to packaging and testing applying basics to emerging technologies Readers charged with optimizing the design and performance of manufacturing processes will find all the information necessary to produce the highest quality chips at the lowest price in the shortest time possible The Semiconductor Manufacturing Handbook provides leading edge information on semiconductor wafer processes MEMS nanotechnology and FPD plus the latest manufacturing and automation technologies including Yield Management Automated Material Handling System Fab and Cleanroom Design and Operation Gas Abatement and Waste Treatment Management And much more Written by 60 international experts and peer reviewed by a seasoned advisory board this handbook covers the fundamentals of relevant technology and its real life application and operational considerations for planning implementing and controlling manufacturing processes It includes hundreds of detailed illustrations and a list of relevant books technical papers and websites for further research This inclusive wide ranging coverage makes the Semiconductor Manufacturing Handbook the most comprehensive single volume reference ever published in the field STATE OF THE ART SEMICONDUCTOR TECHNOLOGIES AND MANUFACTURING PROCESSES SEMICONDUCTOR FUNDAMENTALS How Chips Are Designed and Made Substrates Copper and Low k Dielectrics Silicide Formation Plasma Vacuum Photomask WAFER PROCESSING TECHNOLOGIES Microlithography Ion Implantation Etch PVD

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circuits Epitaxial anneals RTP and oxidation Microlithography etching and ion implantations Physical chemical electrochemical and atomic layer vapor deposition Chemical mechanical planarization Atomic force metrology Packaging bonding and interconnects Flexible hybrid electronics Flat panel flexible display electronics and photovoltaics Gas distribution systems Ultrapure water and filtration Process chemicals handling and abatement Chemical and slurry handling systems Yield management CIM and factory automation Manufacturing execution systems Advanced process control Airborne molecular contamination ESD controls in clean room environments Vacuum systems and RF plasma systems IC manufacturing parts cleaning technology Vibration and noise design And much more *Handbook for Cleaning for Semiconductor*

Manufacturing Karen A. Reinhardt, Richard F. Reidy, 2011-04-12 Provides an in depth discussion of surface conditioning for semiconductor applications The Handbook of Cleaning for Semiconductor Manufacturing Fundamentals and Applications provides an in depth discussion of surface conditioning for semiconductor applications The fundamental physics and chemistry associated with wet processing is reviewed as well as surface and colloidal aspects of cleaning and etching Topics covered in this new reference include Front end line FEOL and back end of line BEOL cleaning applications such as high k metal gate post etch cleaning and pore sealing high dose implant stripping and cleaning and germanium and silicon passivation Formulation development practices methodology and a new directions are presented including chemicals used for preventing corrosion of copper lines cleaning aluminium lines reclaiming wafers and water bonding as well as the filtering and recirculating of chemicals including reuse and recycling Wetting cleaning and drying of features such as high aspect ratio features and hydrophilic surface states especially how to dry without watermarks the abilities to wet hydrophobic surfaces and to remove liquid from deep features The chemical reactions and mechanisms of silicon dioxide etching with hydrofluoric acid particle removal with ammonium hydroxide hydrogen peroxide mixture and metal removal with hydrochloric acid The Handbook of Cleaning for Semiconductor Manufacturing Fundamentals and Applications is a valuable resource for any engineer or manager associated with using or supplying cleaning and contamination free technologies for semiconductor manufacturing Engineers working for semiconductor manufacturing capital equipment chemicals or other industries that assures cleanliness of chemicals material and equipment in the manufacturing area will also find this handbook an indispensable reference Developments in Surface Contamination and Cleaning - Vol 5 Rajiv Kohli, Kashmiri L. Mittal, 2012-12-31 In this series Rajiv Kohli and Kash Mittal have brought together the work of experts from different industry sectors and backgrounds to provide a state of the art survey and best practice guidance for scientists and engineers engaged in surface cleaning or dealing with the consequences of surface contamination This volume complements Volumes 3 and 4 of this series which focused largely on particulate contaminants The expert contributions in this volume cover methods for removal of non particulate contaminants such as metallic and non metallic thin films hydrocarbons toxic and hazardous chemicals and microbiological substances as well as contamination monitoring in pharmaceutical

manufacturing and an innovative method for characterization at the nanoscale Comprehensive coverage of innovations in surface contamination and cleaning Written by established experts in the contamination and cleaning field Each chapter is a comprehensive review of the state of the art Case studies included **Data Center Handbook** Hwaiyu Geng,2021-04-27 DATA CENTER HANDBOOK Written by 59 experts and reviewed by a seasoned technical advisory board the Data Center Handbook is a thoroughly revised one stop resource that clearly explains the fundamentals advanced technologies and best practices used in planning designing building and operating a mission critical energy efficient sustainable data center This handbook in its second edition covers anatomy ecosystem and taxonomy of data centers that enable the Internet of Things and artificial intelligent ecosystems and encompass the following SECTION 1 DATA CENTER OVERVIEW AND STRATEGIC PLANNING Megatrends the IoT artificial intelligence 5G network cloud and edge computing Strategic planning forces location plan and capacity planning Green design construction guidelines and best practices Energy demand conservation and sustainability strategies Data center financial analysis risk management SECTION 2 DATA CENTER TECHNOLOGIES Software defined environment Computing storage network resource management Wireless sensor networks in data centers ASHRAE data center guidelines Data center telecommunication cabling BICSI and TIA 942 Rack level and server level cooling Corrosion and contamination control Energy saving technologies and server design Microgrid and data centers SECTION 3 DATA CENTER DESIGN CONSTRUCTION Data center site selection Architecture design rack floor plan and facility layout Mechanical design and cooling technologies Electrical design and UPS Fire protection Structural design Reliability engineering Computational fluid dynamics Project management SECTION 4 DATA CENTER OPERATIONS TECHNOLOGIES Benchmarking metrics and assessment Data center infrastructure management Data center air management Disaster recovery and business continuity management The Data Center Handbook Plan Design Build and Operations of a Smart Data Center belongs on the bookshelves of any professionals who work in with or around a data center

Handbook of Thin Film Deposition Krishna Seshan,2012-06-27 Resumen The 2nd edition contains new chapters on contamination and contamination control that describe the basics and the issues Another new chapter on meteorology explains the growth of sophisticated automatic tools capable of measuring thickness and spacing of sub micron dimensions The book also covers PVD laser and e beam assisted deposition MBE and ion beam methods to bring together physical vapor deposition techniques Two entirely new areas are focused on chemical mechanical polishing which helps attain the flatness that is required by modern lithography methods and new materials used for interconnect dielectric materials specifically organic polyimide materials [The Electrical Engineering Handbook,Second Edition](#) Richard C. Dorf,1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his

expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the questions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics electromagnetics electrical effects and devices and energy and the emerging trends in the fields of communications digital devices computer engineering systems and biomedical engineering A compendium of physical chemical material and mathematical data completes this comprehensive resource Every major topic is thoroughly covered and every important concept is defined described and illustrated Conceptually challenging but carefully explained articles are equally valuable to the practicing engineer researchers and students A distinguished advisory board and contributors including many of the leading authors professors and researchers in the field today assist noted author and professor Richard Dorf in offering complete coverage of this rapidly expanding field No other single volume available today offers this combination of broad coverage and depth of exploration of the topics The Electrical Engineering Handbook will be an invaluable resource for electrical engineers for years to come

Advancing Silicon Carbide Electronics Technology I Konstantinos Zekentes, 2018-09-25 The rapidly advancing Silicon Carbide technology has a great potential in high temperature and high frequency electronics High thermal stability and outstanding chemical inertness make SiC an excellent material for high power low loss semiconductor devices The present volume presents the state of the art of SiC device fabrication and characterization Topics covered include SiC surface cleaning and etching techniques electrical characterization methods and processing of ohmic contacts to silicon carbide analysis of contact resistivity dependence on material properties limitations and accuracy of contact resistivity measurements ohmic contact fabrication and test structure design overview of different metallization schemes and processing technologies thermal stability of ohmic contacts to SiC their protection and compatibility with device processing Schottky contacts to SiC Schottky barrier formation Schottky barrier inhomogeneity in SiC materials technology and design of 4H SiC Schottky and Junction Barrier Schottky diodes Si SiC heterojunction diodes applications of SiC Schottky diodes in power electronics and temperature light sensors high power SiC unipolar and bipolar switching devices different types of SiC devices including material and technology constraints on device performance applications in the area of metal contacts to silicon carbide status and prospects of SiC power devices

Handbook of Silicon Semiconductor Metrology Alain C. Diebold, 2001-06-29 Containing more than 300 equations and nearly 500 drawings photographs and micrographs this reference surveys key areas such as optical measurements and in line calibration methods It describes cleanroom based measurement technology used during the manufacture of silicon integrated circuits and covers model based critical dimension overlay

Introduction to Semiconductor Manufacturing Technology (International Edition) Hong Xiao, Hong Xiao Staff, 2000-12-01

Nanomanufacturing Handbook Ahmed Busnaina, 2017-12-19 Breakthroughs in nanotechnology have been coming at a rapid pace over the past few years This was fueled by significant

worldwide investments by governments and industry But if these promising young technologies cannot begin to show commercial viability soon that funding is in danger of disappearing as investors lose their appetites and the economic and scientific promise of nanotechnology may not be realized Scrutinizing the barriers to commercial scale up of nanotechnologies the Nanomanufacturing Handbook presents a broad survey of the research being done to bring nanotechnology out of the laboratory and into the factory Current research into nanotechnology focuses on the underlying science but as this forward looking handbook points out the immediate need is for research into scale up process robustness and system integration issues Taking that message to heart this book collects cutting edge research from top experts who examine such topics as surface programmed assembly fabrication and applications of single walled carbon nanotubes SWNTs including nanoelectronics manufacturing nanoelectrical contacts room temperature nanoimprint and nanocontact technologies nanocontacts and switch reliability defects and surface preparation and other innovative application driven initiatives In addition to these technical issues the author provides a survey of the current state of nanomanufacturing in the United States the first of its kind and coverage also reaches into patenting nanotechnologies as well as regulatory and societal issues With timely authoritative coverage accompanied by numerous illustrations the Nanomanufacturing Handbook clarifies the current challenges facing industrial scale nanotechnologies and outlines advanced tools and strategies that will help overcome them

Abrasive Technology Anna Rudawska, 2018-10-24 The subject matter of this book is the information on the abrasive technology methods the characteristics of the methods for example the technological parameters tools and machines innovative methods characteristics of surface structure and surface properties after this type of mechanical process and application in various industrial branches and other technical and technological domains Abrasive technology is very important for example in precision component manufacturing and nano technology devices The aim of this book is to present information on the characteristics and applications of abrasive technology abrasive tools tests and also the innovative methods of this technology This information enables scientists engineers and designers to ensure the soundness and integrity of the fabricated components and to develop new techniques effectively

Micro and Nano Fabrication Hans H. Gatzen, Volker Saile, Jürg Leuthold, 2015-01-02 For Microelectromechanical Systems MEMS and Nanoelectromechanical Systems NEMS production each product requires a unique process technology This book provides a comprehensive insight into the tools necessary for fabricating MEMS NEMS and the process technologies applied Besides it describes enabling technologies which are necessary for a successful production i e wafer planarization and bonding as well as contamination control

Manufacturing Engineering Handbook, Second Edition Hwaiyu Geng, 2015-10-22 The new edition of this professional resource reveals how to optimize all aspects of the global manufacturing process to build the highest quality goods at the lowest price in the shortest possible time How can one apply technical and business knowledge to develop a strategic plan that delivers increased productivity quality sustainability reliability agility resilience and best practices with

rapid time to production and value The answers are found in the fully updated new edition of Manufacturing Engineering Handbook The goal of this second edition is to provide the essential knowledge needed to build products with the highest quality at the lowest cost in the least amount of time by optimizing all aspects of the manufacturing process design development tools processes quality speed output safety and sustainability You will gain access to information on conventional and modern technologies manufacturing processes and operations management that will assist you in achieving these goals The book is written by a team of more than 100 internationally renowned manufacturing engineering experts and pared down from its original 1200 pages The new and vastly improved second edition is specifically designed to concisely and succinctly cover traditional manufacturing processes and advanced technologies as well as newer manufacturing software and systems to integrate them into the modern global manufacturing world Brand new chapters on eco design and sustainability nano materials and nano manufacturing facilities planning operations research New sections on plastics composites and moldmaking global manufacturing and supply chain management Increased coverage of Design for Six Sigma and adaptive manufacturing Affiliated web site with color illustrations graphs charts discussions on future trends additional technical papers and suggestions for further reading [Handbook of Industrial and Systems Engineering, Second Edition](#)

Adedeji B. Badiru, 2013-10-11 A new edition of a bestselling industrial and systems engineering reference Handbook of Industrial and Systems Engineering Second Edition provides students researchers and practitioners with easy access to a wide range of industrial engineering tools and techniques in a concise format This edition expands the breadth and depth of coverage emphasizing new systems engineering tools techniques and models See What's New in the Second Edition Section covering safety reliability and quality Section on operations research queuing logistics and scheduling Expanded appendix to include conversion factors and engineering systems and statistical formulae Topics such as control charts engineering economy health operational efficiency healthcare systems human systems integration Lean systems logistics transportation manufacturing systems material handling systems process view of work and Six Sigma techniques The premise of the handbook remains to expand the breadth and depth of coverage beyond the traditional handbooks on industrial engineering The book begins with a general introduction with specific reference to the origin of industrial engineering and the ties to the Industrial Revolution It covers the fundamentals of industrial engineering and the fundamentals of systems engineering Building on this foundation it presents chapters on manufacturing production systems and ergonomics then goes on to discuss economic and financial analysis management information engineering and decision making Two new sections examine safety reliability quality operations research queuing logistics and scheduling The book provides an updated collation of the body of knowledge of industrial and systems engineering The handbook has been substantively expanded from the 36 seminal chapters in the first edition to 56 landmark chapters in the second edition In addition to the 20 new chapters 11 of the chapters in the first edition have been updated with new materials Filling the gap that exists between the

traditional and modern practice of industrial and systems engineering the handbook provides a one stop resource for teaching research and practice **Microelectronics Manufacturing Diagnostics Handbook** Abraham

Landzberg,2012-12-06 The world of microelectronics is filled with cusses measurement systems manufacturing many success stories From the use of semi control techniques test diagnostics and fail ure analysis It discusses methods for modeling conductors for powerful desktop computers to their use in maintaining optimum engine per and reducing defects and for preventing de formance in modem automobiles they have fects in the first place The approach described clearly improved our daily lives The broad while geared to the microelectronics world has useability of the technology is enabled how applicability to any manufacturing process of similar complexity The authors comprise some ever only by the progress made in reducing their cost and improving their reliability De of the best scientific minds in the world and fect reduction receives a significant focus in our are practitioners of the art The information modem manufacturing world and high quality captured here is world class I know you will diagnostics is the key step in that process find the material to be an excellent reference in of product failures enables step func Analysis your application tion improvements in yield and reliability which works to reduce cost and open up new Dr Paul R Low applications and technologies IBM Vice President and This book describes the process of defect re of Technology Products General Manager duction in the microelectronics world **Makers of the Microchip** Christophe Lecuyer,David C. Brock,2010-09-03 The first years of the company that developed the microchip and created the model for a successful Silicon Valley start up In the first three and a half years of its existence Fairchild Semiconductor developed produced and marketed the device that would become the fundamental building block of the digital world the microchip Founded in 1957 by eight former employees of the Schockley Semiconductor Laboratory Fairchild created the model for a successful Silicon Valley start up intense activity with a common goal close collaboration and a quick path to the market Fairchild s first device hit the market just ten months after the company s founding Fairchild Semiconductor was one of the first companies financed by venture capital and its success inspired the establishment of venture capital firms in the San Francisco Bay area These firms would finance the explosive growth of Silicon Valley over the next several decades This history of the early years of Fairchild Semiconductor examines the technological business and social dynamics behind its innovative products The centerpiece of the book is a collection of documents reproduced in facsimile including the company s first prospectus ideas sketches and plans for the company s products and a notebook kept by cofounder Jay Last that records problems schedules and tasks discussed at weekly meetings A historical overview interpretive essays and an introduction to semiconductor technology in the period accompany these primary documents

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