

LVDS SERDES Transmitter/Receiver IP Cores User Guide

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The low-voltage differential signaling serializer or deserializer (LVDS SERDES) IP cores (ALTLVDS_TX and ALTLVDS_RX) implement the LVDS SERDES interfaces to transmit and receive high-speed differential data. You can configure the features of these IP cores using the IP Catalog and parameter editor.

Related Information

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Provides a list of user guides for previous versions of the ALTLVDS_TX and ALTLVDS_RX IP cores.

Features

Table 1: ALTLVDS_TX and ALTLVDS_RX Features

This table lists the features of the ALTLVDS_TX and ALTLVDS_RX IP cores.

Note: The ALTLVDS_TX and ALTLVDS_RX IP cores are not available for the Stratix® 10 and Arria® 10 device families. For Stratix 10 and Arria 10 devices, use the Altera LVDS SERDES IP core.

IP Core	Features	Supported devices
ALTLVDS_TX and ALTLVDS_RX	Parameterizable data channel widths	All Stratix, Arria, and Cyclone® series devices.
	Parameterizable serializer/deserializer (SERDES) factors	
	Registered input and output ports	
	Support for external phase-locked loops (PLL)	
	PLLs sharing between transmitters and receivers	
	PLL control signals	
ALTLVDS_RX Only	Dynamic phase alignment (DPA) mode support ⁽¹⁾	All Stratix and Arria series devices.
	Soft clock data recovery (CDR) mode support ⁽²⁾	
	DPA PLL calibration support ⁽¹⁾	All Stratix series devices.

⁽¹⁾ DPA is available starting from Stratix GX onwards. The first generation Stratix device family does not support DPA.

⁽²⁾ CDR is not available in the first generation Stratix device family and the Stratix II device family. However, soft-CDR is available in all other Stratix series including Stratix GX and Stratix II GX.

Lvds Serdes Transmitter Receiver Ip Cores User Guide

Wenbin Ji



Lvds Serdes Transmitter Receiver Ip Cores User Guide:

The book delves into Lvds Serdes Transmitter Receiver Ip Cores User Guide. Lvds Serdes Transmitter Receiver Ip Cores User Guide is an essential topic that needs to be grasped by everyone, ranging from students and scholars to the general public. This book will furnish comprehensive and in-depth insights into Lvds Serdes Transmitter Receiver Ip Cores User Guide, encompassing both the fundamentals and more intricate discussions.

1. This book is structured into several chapters, namely:
 - Chapter 1: Introduction to Lvds Serdes Transmitter Receiver Ip Cores User Guide
 - Chapter 2: Essential Elements of Lvds Serdes Transmitter Receiver Ip Cores User Guide
 - Chapter 3: Lvds Serdes Transmitter Receiver Ip Cores User Guide in Everyday Life
 - Chapter 4: Lvds Serdes Transmitter Receiver Ip Cores User Guide in Specific Contexts
 - Chapter 5: Conclusion
 2. In chapter 1, the author will provide an overview of Lvds Serdes Transmitter Receiver Ip Cores User Guide. The first chapter will explore what Lvds Serdes Transmitter Receiver Ip Cores User Guide is, why Lvds Serdes Transmitter Receiver Ip Cores User Guide is vital, and how to effectively learn about Lvds Serdes Transmitter Receiver Ip Cores User Guide.
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- This book is crafted in an easy-to-understand language and is complemented by engaging illustrations. This book is highly recommended for anyone seeking to gain a comprehensive understanding of Lvds Serdes Transmitter Receiver Ip Cores User Guide.

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