

This document contains complete and detailed description of all modules included in the Atmel® AVR® XMEGA® C microcontroller family. The Atmel AVR XMEGA C is a family of low-power, high-performance, and peripheral-rich CMOS 8/16-bit microcontrollers based on the AVR enhanced RISC architecture. The available Atmel AVR XMEGA C modules described in this manual are:

- Atmel AVR CPU
- Memories
- DMAC - Direct memory access controller
- Event system
- System clock and clock options
- Power management and sleep modes
- System control and reset
- WDT - Watchdog timer
- Interrupts and programmable multilevel interrupt controller
- PORT - I/O ports
- TC - 16-bit timer/counters
- AWeX - Advanced waveform extension
- Hi-Res - High resolution extension
- RTC - Real-time counter
- USB - Universal serial bus interface
- TWI - Two-wire serial interface
- SPI - Serial peripheral interface
- USART - Universal synchronous and asynchronous serial receiver and transmitter
- I2C - Infrared communication module
- AES cryptographic engine
- CRC - Cyclic redundancy check
- ADC - Analog-to-digital converter
- AC - Analog comparator
- PDI - Program and debug interface
- Memory programming
- Peripheral address map
- Register summary
- Interrupt vector summary
- Instruction set summary

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