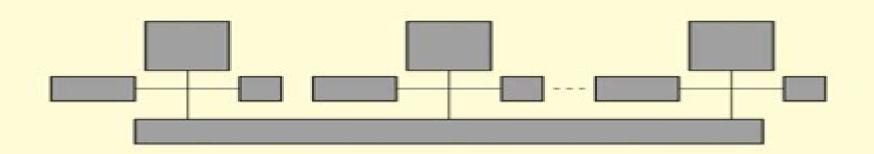


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<u>Microcontroller & Applications</u> D.A.Godse A.P.Godse,2007 Microprocessors and Microcontrollers Microprocessors and microcontrollers A microprocessors survey Development systems for microcontrollers RISC CISC CPU architectures Harvard Von Neumann CPU architecture The 8051 Architecture 8051 microntroller hardware Input output pins Ports and circuits

External memory Counter and timers Serial data input output Interrupts 8051 Addressing Modes and Moving DataAddressing modes External data moves Code memory Read only data moves Indexed addressing mode PUSH and POP opcodes Data exchanges Example programs Logical Operations Arithmetic Operations Jump OperationsLogical operations Byte level logical operations Bit level logical operations Rotate and Swap operations Example programs Arithmetic operations Flags Incrementing and decrementing Addition Subtraction Multiplication and Division Decimal arithmetic Example programs Jump operations The JUMP and CALL program range Jump calls and subroutines Interrupts and returns More detail on interrupts Example problems Counter Timer Programming in 8051 Programming 8051 timers Counter programming 8051 Serial CommunicationBasics of serial communication 8051 connections to RS 232 8051 Serial communication programming Interrupts Programming8051 Interrupts Programming timer interrupts Programming external hardware interrupts Interrupt priority in the 8051 8051 Interfacing and ApplicationsInterfacing 8051 to LCD ADC Temperature sensor DAC Stepper motor Keyboard 8255 **Digital Design using Verilog HDL** Atul P. Godse, Dr. Deepali A. Godse, 2020-12-01 This book begins with an introduction to Verilog HDL It describes basic concepts in Verilog HDL language constructs and conventions and modeling styles gate level modeling data flow level modeling behavioral modeling and switch level modeling It also describes sequential models basic memory components functional register static machine coding and sequential synthesis The last section of the book focuses on component testing and verification It includes combinational circuits testing sequential circuit testing test bench techniques design verification and assertion verification

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