

LOW-POWER CMOS VLSI CIRCUIT DESIGN

KAUSHIK ROY
SHARAT C. PRASAD

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Low Power Cmos Vlsi Circuit Design 1st Edition

Rohit Dhiman



Low Power Cmos Vlsi Circuit Design 1st Edition:

Low-Power Cmos Vlsi Circuit Design Kaushik Roy, Sharat C. Prasad, 2009-02-02 This is the first book devoted to low power circuit design and its authors have been among the first to publish papers in this area Low Power CMOS VLSI Design Physics of Power Dissipation in CMOS FET Devices Power Estimation Synthesis for Low Power Design and Test of Low Voltage CMOS Circuits Low Power Static Ram Architectures Low Energy Computing Using Energy Recovery Techniques Software Design for Low Power

Proceedings of First International Conference on Smart System, Innovations and Computing Arun K. Somani, Sumit Srivastava, Ankit Mundra, Sanyog Rawat, 2018-01-08 The edited volume contains original papers contributed to 1st International Conference on Smart System Innovations and Computing SSIC 2017 by researchers from different countries The contributions focuses on two main areas i e Smart Systems Innovations which includes applications for smart cities smart grid social computing and privacy challenges with their theory specification design performance and system building And second Computing of Complex Solutions which includes algorithms security solutions communication and networking approaches The volume provides a snapshot of current progress in related areas and a glimpse of future possibilities This volume is useful for researchers Ph D students and professionals working in the core areas of smart systems innovations and computing

Nanoelectronics for Next-Generation Integrated Circuits Rohit Dhiman, 2022-11-23 The incessant scaling of complementary metal oxide semiconductor CMOS technology has resulted in significant performance improvements in very large scale integration VLSI design techniques and system architectures This trend is expected to continue in the future but this requires breakthroughs in the design of nano CMOS and post CMOS technologies Nanoelectronics refers to the possible future technologies beyond conventional CMOS scaling limits This volume addresses the current state of the art nanoelectronic technologies and presents potential options for next generation integrated circuits Nanoelectronics for Next generation Integrated Circuits is a useful reference guide for researchers engineers and advanced students working on the frontier of the design and modeling of nanoelectronic devices and their integration aspects with future CMOS circuits This comprehensive volume eloquently presents the design methodologies for spintronics memories quantum dot cellular automata and post CMOS FETs including applications in emerging integrated circuit technologies

Design and Modeling of Low Power VLSI Systems Sharma, Manoj, Gautam, Ruchi, Khan, Mohammad Ayoub, 2016-06-06 Very Large Scale Integration VLSI Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip Emerging research in this area has the potential to uncover further applications for VLSI technologies in addition to system advancements Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization Through a research based discussion of the technicalities involved in the VLSI hardware development process cycle this book is a useful resource for researchers

engineers and graduate level students in computer science and engineering

MOSFET Technologies for Double-Pole Four-Throw Radio-Frequency Switch Viranjay M. Srivastava, Ghanshyam Singh, 2013-10-07 This book provides analysis and discusses the design of various MOSFET technologies which are used for the design of Double Pole Four Throw DP4T RF switches for next generation communication systems The authors discuss the design of the DP4T RF switch by using the Double Gate DG MOSFET as well as the Cylindrical Surrounding double gate CSDG MOSFET The effect of HfO₂ high dielectric material in the design of DG MOSFET and CSDG MOSFET is also explored Coverage includes comparison of Single gate MOSFET and Double gate MOSFET switching parameters as well as testing of MOSFETs parameters using image acquisition Integrated Circuit Design. Power and Timing Modeling, Optimization and Simulation Bertrand Hochet, Antonio J. Acosta, Manuel J. Bellido, 2003-08-02 The International Workshop on Power and Timing Modeling Optimization and Simulation PATMOS 2002 was the 12th in a series of international workshops 1 previously held in several places in Europe PATMOS has over the years evolved into a well established and outstanding series of open European events on power and timing aspects of integrated circuit design The increased interest especially in low power design has added further momentum to the interest in this workshop Despite its growth the workshop can still be considered as a very useful conference featuring high level scientific presentations together with open discussions in a free and easy environment This year the workshop has been opened to both regular papers and poster presentations The increasing number of worldwide high quality submissions is a measure of the global interest of the international scientific community in the topics covered by PATMOS The objective of this workshop is to provide a forum to discuss and investigate the emerging problems in the design methodologies and CAD tools for the new generation of IC technologies A major emphasis of the technical program is on speed and low power aspects with particular regard to modeling characterization design and architectures The technical program of PATMOS 2002 included nine sessions dedicated to most important and current topics on power and timing modeling optimization and simulation The three invited talks try to give a global overview of the issues in low power and or high performance circuit design

Dual Mode Logic Itamar Levi, Alexander Fish, 2020-12-15 This book presents Dual Mode Logic DML a new design paradigm for digital integrated circuits DML logic gates can operate in two modes each optimized for a different metric Its on the fly switching between these operational modes at the gate block and system levels provide maximal E D optimization flexibility Each highly detailed chapter has multiple illustrations showing how the DML paradigm seamlessly implements digital circuits that dissipate less energy while simultaneously improving performance and reducing area without a significant compromise in reliability All the facets of the DML methodology are covered starting from basic concepts through single gate optimization general module optimization design trade offs and new ways DML can be integrated into standard design flows using standard EDA tools DML logic is compatible with numerous applications but is particularly advantageous for ultra low power reliable high performance systems and advanced scaled technologies Written

in language accessible to students and design engineers each topic is oriented toward immediate application by all those interested in an alternative to CMOS logic Describes a novel promising alternative to conventional CMOS logic known as Dual Mode Logic DML with which a single gate can be operated selectively in two modes each optimized for a different metric e g energy consumption performance size Demonstrates several techniques at the architectural level which can result in high energy savings and improved system performance Focuses on the tradeoffs between power area and speed including optimizations at the transistor and gate level including alternatives to DML basic cells Illustrates DML efficiency for a variety of VLSI applications

High Performance Architecture and Grid Computing Archana Mantri,Suman Nandi,Gaurav Kumar,Sandeep Kumar,2011-07-05 This book constitutes the refereeds proceedings of the International Conference on High Performance Architecture and Grid Computing HPAGC 2011 held in Chandigarh India in July 2011 The 87 revised full papers presented were carefully reviewed and selected from 240 submissions The papers are organized in topical sections on grid and cloud computing high performance architecture information management and network security

Major Applications of Carbon Nanotube Field-Effect Transistors (CNTFET) Raj, Balwinder,Khosla, Mamta,Singh, Amandeep,2019-12-06 With recent advancements in electronics specifically nanoscale devices new technologies are being implemented to improve the properties of automated systems However conventional materials are failing due to limited mobility high leakage currents and power dissipation To mitigate these challenges alternative resources are required to advance electronics further into the nanoscale domain Carbon nanotube field effect transistors are a potential solution yet lack the information and research to be properly utilized Major Applications of Carbon Nanotube Field Effect Transistors CNTFET is a collection of innovative research on the methods and applications of converting semiconductor devices from micron technology to nanotechnology The book provides readers with an updated status on existing CNTs CNTFETs and their applications and examines practical applications to minimize short channel effects and power dissipation in nanoscale devices and circuits While highlighting topics including interconnects digital circuits and single wall CNTs this book is ideally designed for electrical engineers electronics engineers students researchers academicians industry professionals and practitioners working in nanoscience nanotechnology applied physics and electrical and electronics engineering

Intelligent Computing in Control and Communication G.T. Chandra Sekhar,H. S. Behera,Janmenjoy Nayak,Bighnaraj Naik,Danilo Pelusi,2021-01-04 This book consists of peer reviewed papers presented at the First International Conference on Intelligent Computing in Control and Communication ICC3 2020 It comprises interesting topics in the field of applications of control engineering communication and computing technology As the current world is witnessing the use of various intelligent techniques for their independent problem solving so this book may have a wide importance for all range of researchers and scholars The book serves as a reference for researchers professionals and students from across electrical electronic and computer engineering disciplines

The Electrical Engineering Handbook Wai Kai Chen,2004-11-16 The Electrical Engineer s Handbook is an invaluable

reference source for all practicing electrical engineers and students Encompassing 79 chapters this book is intended to enlighten and refresh knowledge of the practicing engineer or to help educate engineering students This text will most likely be the engineer s first choice in looking for a solution extensive complete references to other sources are provided throughout No other book has the breadth and depth of coverage available here This is a must have for all practitioners and students The Electrical Engineer s Handbook provides the most up to date information in Circuits and Networks Electric Power Systems Electronics Computer Aided Design and Optimization VLSI Systems Signal Processing Digital Systems and Computer Engineering Digital Communication and Communication Networks Electromagnetics and Control and Systems About the Editor in Chief Wai Kai Chen is Professor and Head Emeritus of the Department of Electrical Engineering and Computer Science at the University of Illinois at Chicago He has extensive experience in education and industry and is very active professionally in the fields of circuits and systems He was Editor in Chief of the IEEE Transactions on Circuits and Systems Series I and II President of the IEEE Circuits and Systems Society and is the Founding Editor and Editor in Chief of the Journal of Circuits Systems and Computers He is the recipient of the Golden Jubilee Medal the Education Award and the Meritorious Service Award from the IEEE Circuits and Systems Society and the Third Millennium Medal from the IEEE Professor Chen is a fellow of the IEEE and the American Association for the Advancement of Science 77 chapters encompass the entire field of electrical engineering THOUSANDS of valuable figures tables formulas and definitions Extensive bibliographic references

Proceedings of the Fifth International Conference on Trends in Computational and Cognitive Engineering M. Shamim Kaiser,Raghvendra Singh,Anirban Bandyopadhyay,Mufti Mahmud,Kanad Ray,2025-07-04 This book presents various computational and cognitive modeling approaches in the areas of health education finance environment engineering commerce and industry It is a collection of selected conference papers presented at the 5th International Conference on Trends in Cognitive Computation Engineering TCCE 2023 organized by Pranveer Singh Institute of Technology Kanpur Uttar Pradesh India in collaboration with IIOIR Shimla Himachal Pradesh India during 24 25 November 2023 The book is divided into two volumes and it shares cutting edge insights and ideas from mathematicians engineers scientists and researchers and discusses fresh perspectives on problem solving in a range of research areas

Analog VLSI Design Automation Sina Balkir,2003-06-27 The explosive growth and development of the integrated circuit market over the last few years have been mostly limited to the digital VLSI domain The difficulty of automating the design process in the analog domain the fact that a general analog design methodology remained undefined and the poor performance of earlier tools have left the analog

Energy Efficient Microprocessor Design Thomas D. Burd,Robert W. Brodersen,2002 This volume starts with a description of the metrics and benchmarks used to design energy efficient microprocessor systems followed by energy efficient methodologies for the architecture and circuit design DC DC conversion energy efficient software and system integration

Advances in Optical Science and Engineering Vasudevan

Lakshminarayanan, Indrani Bhattacharya, 2015-06-02 The Proceedings of First International Conference on Opto Electronics and Applied Optics 2014 IEM OPTRONIX 2014 presents the research contributions presented in the conference by researchers from both India and abroad Contributions from established scientists as well as students are included The book is organized to enable easy access to various topics of interest The first part includes the Keynote addresses by Phillip Russell Max Planck Institute of the Light Sciences Erlangen Germany and Lorenzo Pavesi University of Trento Italy The second part focuses on the Plenary Talks given by eminent scientists namely Azizur Rahman City University London London Bishnu Pal President The Optical Society of India Kamakhya Ghatak National Institute of Technology Agartala Kehar Singh Former Professor India Institute of Technology Delhi Mourad Zghal SUPCOM University of Carthage Tunisia Partha Roy Chaudhuri IIT Kharagpur S K Bhadra CSIR Central Glass and Ceramic Research Institute Kolkata Sanjib Chatterjee Raja Ramanna Centre for Advanced Technology Indore Takeo Sasaki Tokyo University Japan Lakshminarayan Hazra Emeritus Professor University of Calcutta Kolkata Shyam Akashe ITM University Gwalior and Vasudevan Lakshminarayanan University of Waterloo Canada The subsequent parts focus on topic wise contributory papers in Application of Solar Energy Diffraction Tomography E M Radiation Theory and Antenna Fibre Optics and Devices Photonics for Space Applications Micro Electronics and VLSI Nano Photonics Bio Photonics and Bio Medical Optics Non linear Phenomena and Chaos Optical and Digital Data and Image Processing Optical Communications and Networks Optical Design Opto Electronic Devices Opto Electronic Materials and Quantum Optics and Information Processing Advances in AI for Biomedical Instrumentation, Electronics and Computing Vibhav Sachan, Shahid Malik, Ruchita Gautam, Parvin Kumar, 2024-06-13 This book contains the proceedings of 5th International Conference on Advances in AI for Biomedical Instrumentation Electronics and Computing ICABEC 2023 which provided an international forum for the exchange of ideas among researchers students academicians and practitioners It presents original research papers on subjects of AI Biomedical Communications Computing Systems Some interesting topics it covers are enhancing air quality prediction using machine learning optimization of leakage power consumption using hybrid techniques multi robot path planning in complex industrial dynamic environment enhancing prediction accuracy of earthquake using machine learning algorithms and advanced machine learning models for accurate cancer diagnostics Containing work presented by a diverse range of researchers this book will be of interest to students and researchers in the fields of Electronics and Communication Engineering Computer Science Engineering Information Technology Electrical Engineering Electronics and Instrumentation Engineering Computer applications and all interdisciplinary streams of Engineering Sciences Proceedings of the 2025 2nd International Conference on Electrical Engineering and Intelligent Control (EEIC 2025) Ata Jahangir Moshayedi, 2025-11-20 This is an open access book The organizing committee is honored to extend a heartfelt welcome to the 2nd International Conference on Electrical Engineering and Intelligent Control EEIC 2025 Building on the resounding success of its inaugural edition in Singapore 2024 as part of the Conference on Intelligent

Collaboration CIC series this year's conference will convene in Auckland New Zealand reaffirming its role as a global nexus uniting leading researchers scholars and industry pioneers in electrical engineering and intelligent control Auckland's dynamic fusion of world class academia a thriving innovation ecosystem and a steadfast commitment to sustainability mirrors EEIC's vision for Collaborative Intelligence Ethical Innovation and Resilient Systems By hosting EEIC 2025 here we aim to harness the city's unique energy to address emerging challenges and drive technological progress with ethical foresight Under this guiding theme EEIC 2025 will catalyze cutting edge dialogue showcase transformative advancements and inspire collaborative solutions to shape the future of intelligent systems Through fostering synergy across disciplines we aspire to accelerate breakthroughs in both theoretical frameworks and real world applications ensuring resilient and equitable progress in these critical fields

Neuromorphic Circuits for Nanoscale Devices Pinaki Mazumder, Yalcin Yilmaz, Idongesit Ebong, 2022-09-01 Nanoscale devices attracted significant research effort from the industry and academia due to their operation principals being based on different physical properties which provide advantages in the design of certain classes of circuits over conventional CMOS transistors Neuromorphic Circuits for Nanoscale Devices contains recent research papers presented in various international conferences and journals to provide insight into how the operational principles of the nanoscale devices can be utilized for the design of neuromorphic circuits for various applications of non volatile memory neural network training learning and image processing The topics discussed in the book include Nanoscale Crossbar Memory Design Q Learning and Value Iteration using Nanoscale Devices Image Processing and Computer Vision Applications for Nanoscale Devices Nanoscale Devices based Cellular Nonlinear Neural Networks

Current Advancements in Stereo Vision Asim Bhatti, 2012-07-11 The book is a new edition of stereo vision book series of INTECH Open Access Publisher and it presents diverse range of ideas and applications highlighting current research technology trends and advances in the field of stereo vision The topics covered in this book include fundamental theoretical aspects of robust stereo correspondence estimation novel and robust algorithms hardware implementation for fast execution and applications in wide range of disciplines Particularly interesting approaches include neuromorphic engineering probabilistic analysis and anisotropic reaction diffusion addressing the problem of stereo correspondence and the applications in mobile robotics for autonomous terrain mapping and navigation SterCentre for Intelligent Systems Research CISR Institute of Technology Research and Innovation ITRI eo algorithm with anisotropic reaction diffusion systems utilizing biologically motivated reaction diffusion systems with anisotropic diffusion coefficients makes it an interesting addition to the book

Advances in Signal Processing and Communication Engineering Pradip Kumar Jain, Yatindra Nath Singh, Ravi Paul Gollapalli, S. P. Singh, 2022-12-01 This book comprises select proceedings of the International Conference on Advances in Signal Processing and Communication Engineering ICASPACE 2021 The book covers several theoretical and mathematical approaches addressing day to day challenges in signal image and speech processing and advanced communication systems It primarily focuses on effective

mathematical methods algorithms and models that enhance the performance of existing systems The topics covered in the book are advances in signal processing radar and biomedical image processing speech processing technical and environmental challenges in 5G technology and strategies for optimal utilization of resources to improve the efficacy of the communication systems in terms of bandwidth and radiating power etc The works published in the book will remarkably be helpful to prospective scholars academicians and students seeking knowledge in signal processing and communication engineering

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