

Low-Level Programming

C, Assembly, and Program Execution on Intel® 64 Architecture

Igor Zhirkov

Low Level Programming C Assembly And Program Execution On

Raymond Nutting

Low Level Programming C Assembly And Program Execution On:

Low-Level Programming Igor Zhirkov,2017-06-04 This book teaches programmers and programming students how to use x64 assembly to write low level code in C for performance critical programs and how to compile and execute it inside the Intel 64 hardware and OS framework Low Level Programming presents Intel 64 architecture as a development of von Neumann architecture featuring protection mechanisms and performance amplifiers such as caches and branch predicting It proceeds to investigate the compilation cycle and ELF object files Elucidating a structured approach to C with code examples exercises and a companion annex of source code the book models best coding practices for implementing language abstractions on top of assembly The author examines the optimization capabilities and limits of modern compilers and he demonstrates the use of various performance gain techniques such as specialized instructions and prefetching What Readers Will LearnLow Level Programming teaches programmers how to use assembly language and C to write code for Intel 64 platforms and to look under the hood for various purposes including the following Making code more performant on the assembly level Debugging compiler and optimizer errors in native code Fixing executables by disassembly in the absence of source code Diagnosing malware Who This Book Is ForIntermediate to advanced programmers and programming students

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AVR line The knowledge you gain on the ATmega16 can be easily translated to every other microcontroller in the AVR line We cover the main subsystems aboard the ATmega16 providing a short theory section followed by a description of the related microcontroller subsystem with accompanying hardware and software to exercise the subsystem In all examples we use the C programming language We conclude with a detailed chapter describing how to interface the microcontroller to a wide variety of input and output devices Table of Contents Atmel AVR Architecture Overview Serial Communication Subsystem Analog to Digital Conversion Interrupt Subsystem Timing Subsystem Atmel AVR Operating Parameters and Interfacing ATmega16 Register Set ATmega16 Header File

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