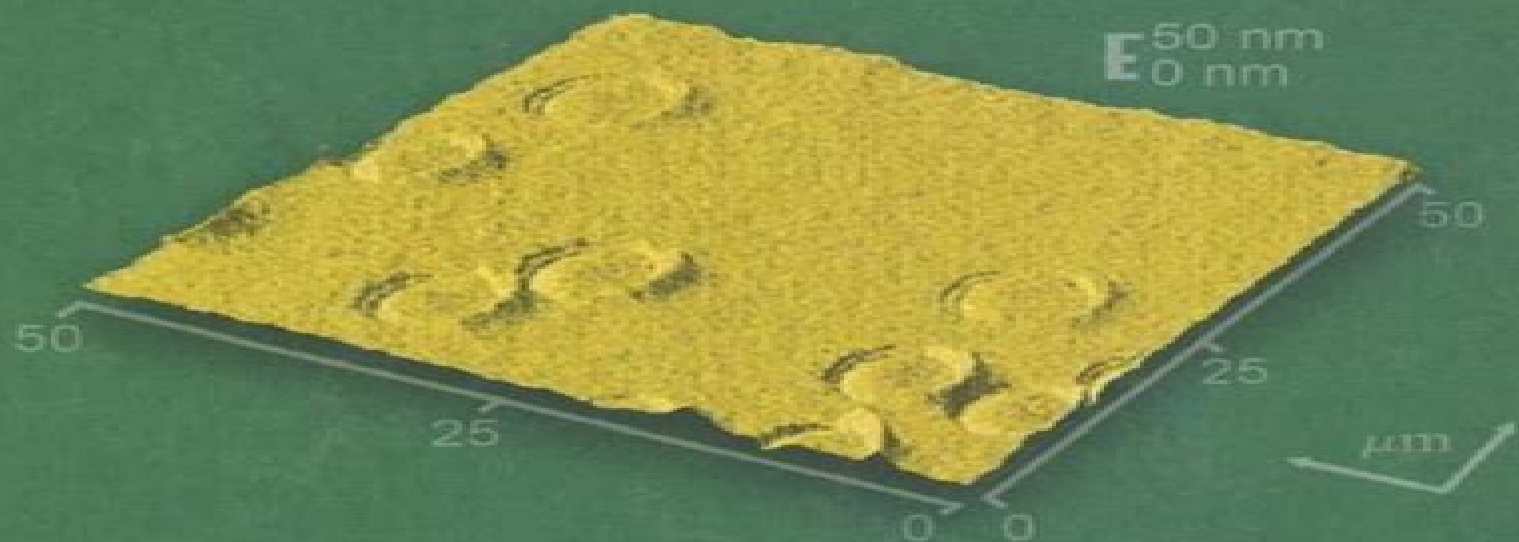


Handbook of SILICON WAFER CLEANING TECHNOLOGY

Second Edition

Materials Science & Process Technology Series



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Handbook Of Silicon Wafer Cleaning Technology 2nd Edition Second Edition Materials Science And Process Technology

Hwaiyu Geng



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Handbook of Silicon Wafer Cleaning Technology, 2nd Edition Karen Reinhardt, Werner Kern, 2008 The second Edition of the Handbook of Silicon Wafer Cleaning Technology is intended to provide knowledge of wet plasma and other surface conditioning techniques used to manufacture integrated circuits The integration of the clean processes into the device manufacturing flow will be presented with respect to other manufacturing steps such as thermal implant etching and photolithography processes The Handbook discusses both wet and plasma based cleaning technologies that are used for removing contamination particles residue and photoresist from wafer surfaces Both the process and the equipment are covered A review of the current cleaning technologies is included Also advanced cleaning technologies that are under investigation for next generation processing are covered including supercritical fluid laser and cryoaerosol cleaning techniques Additionally theoretical aspects of the cleaning technologies and how these processes affect the wafer is discussed such as device damage and surface roughening will be discussed The analysis of the wafers surface is outlined A discussion of the new materials and the changes required for the surface conditioning process used for manufacturing is also included Focused on silicon wafer cleaning techniques including wet plasma and other surface conditioning techniques used to manufacture integrated circuits As this book covers the major technologies for removing contaminants it is a reliable reference for anyone that manufactures integrated circuits or supplies the semiconductor and microelectronics industries Covers processes and equipment as well as new materials and changes required for the surface conditioning process Editors are two of the top names in the field and are both extensively published Discusses next generation processing techniques including supercritical fluid laser and cryoaerosol

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conditioning techniques used to manufacture integrated circuits As this book covers the major technologies for removing contaminants it is a reliable reference for anyone that manufactures integrated circuits or supplies the semiconductor and microelectronics industries Covers processes and equipment as well as new materials and changes required for the surface conditioning process Editors are two of the top names in the field and are both extensively published Discusses next generation processing techniques including supercritical fluid laser and cryoaerosol

Processing of 'Wide Band Gap Semiconductors S. J. Pearton, 2013-01-15 Wide bandgap semiconductors made from such materials as GaN SiC diamond and ZnSe are undergoing a strong resurgence in recent years principally because of their direct bandgaps which give them a huge advantage over the indirect gap Sic As an example more than 10 million blue LEDs using this technology are sold each month and new high brightness 15 lumens per watt long life white LEDs are under development with the potential to replace incandescent bulbs in many situations This book provides readers with a broad overview of this rapidly expanding technology bringing them up to speed on new discoveries and commercial applications It provides specific technical applications of key processes such as laser diodes LEDs and very high temperature electronic controls on engines focusing on doping etching oxidation passivation growth techniques and more

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Ultrananocrystalline Diamond Olga A. Shenderova, Dieter M. Gruen, 2006-08-10 Ultrananocrystalline Diamond Syntheses Properties and Applications is a unique

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Microelectronics Manufacturing Diagnostics Handbook Abraham Landzberg, 2012-12-06 The world of microelectronics is filled with cusses measurement systems manufacturing many success stories From the use of semi control techniques test diagnostics and fail ure analysis It discusses methods for modeling conductors for powerful desktop computers to their use in maintaining optimum engine per and reducing defects and for preventing de formance in modem automobiles they have fects in the first place The approach described clearly improved our daily lives The broad while geared to the microelectronics world has useability of the technology is enabled how applicability to any manufacturing process of similar complexity The authors comprise some ever only by the progress made in reducing their cost and improving their reliability De of the best scientific minds in the world and fect reduction receives a significant focus in our are practitioners of the art The information modem manufacturing world and high quality captured here is world class I know you will diagnostics is the key step in that process find the material to be an excellent reference in of product failures enables step func Analysis your application tion improvements in yield and reliability which works to reduce cost and open up new Dr Paul R Low applications and technologies IBM Vice President and This book describes the process of defect re of Technology Products General Manager duction in the microelectronics world

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Handbook of Semiconductor Wafer Cleaning Technology Werner Kern, 1993-12-31 Discusses semiconductor wafer cleaning and the scientific and technical disciplines associated directly or indirectly with this subject Intended to serve as a handbook for practitioners and professionals in the field

Manufacturing Engineering Handbook, Second Edition Hwaiyu Geng, 2015-10-22 The new edition of this professional resource reveals how to optimize all aspects of the global manufacturing process to build the highest quality goods at the lowest price in the shortest possible time How can one apply technical and business knowledge to develop a strategic plan that delivers increased productivity quality sustainability reliability agility resilience and best practices with rapid time to production and value The answers are found in the fully updated new edition of Manufacturing Engineering Handbook The goal of this second edition is to provide the essential knowledge needed to build products with the highest quality at the lowest cost in the least amount of time by optimizing all aspects of the manufacturing process design development tools processes quality speed output safety and sustainability You will gain access to information on conventional and modern technologies

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Science and Technology of Semiconductor Surface Preparation: Volume 477 Gregg S. Hagashi, 1997-09-30 The focus of semiconductor wafer processing is rapidly changing Pure device performance is no longer sufficient to ensure a technology's success as IC chips approach 1GB Yield reliability cost and manufacturability are principal drivers of the industry Great strides have been made in understanding the science and impact of front end pre metal chemical surface preparations and are beginning to influence the real technology and its evolution Efforts in understanding particle and metals removal along with Si surface etching and microroughness are prime examples of areas where work is beginning to pay off together with processes related to the back end post metal The focus of this book is to report new findings and to discuss surface preparation with an emphasis on gaining a mechanistic understanding of the underlying science upon which the technology is based Topics include megasonic cleaning SC1 technology surface preparation and gate oxide reliability CMP CMP cleaning post etch processing surface microroughness wet chemical cleaning and gate oxide integrity analytical studies of surfaces wet chemical cleaning etching dry wafer cleaning and environmentally friendly processing

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