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By Ned Mohan Power Electronics Converters Applications And Design 3rd Edition

Ned Mohan, Swaroop Guggilam

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Smart Cities: Power Electronics, Renewable Energy, and Internet of Things Ahteshamul Haque, Akhtar Kalam, Himanshu Sharma, 2024-02-15 This book discusses the integration of power electronics renewable energy and the Internet of Things IoT from the perspective of smart cities in a single volume The text will be helpful for senior undergraduate graduate students and academic researchers in diverse engineering fields including electrical electronics and communication and computers The book Covers the integration of power electronics energy harvesting and the IoT for smart city applications Discusses concepts of power electronics and the IoT in electric vehicles for smart cities Examines the integration of power electronics in renewable energy for smart cities Discusses important concepts of energy harvesting including solar energy harvesting maximum power point tracking MPPT controllers and switch mode power supplies SMPS Explores IoT connectivity technologies such as long term evolution LTE narrow band NB IoT long range LoRa Bluetooth and ZigBee IEEE Standard 802 15 4 for low data rate wireless personal communication applications The text provides the knowledge about applications technologies and standards of power electronics renewable energy and IoT for smart cities It will serve as an ideal reference text for senior undergraduate graduate students and academic researchers in the fields of electrical engineering electronics and communication engineering computer engineering civil engineering and environmental engineering

Basics of Electrical Machines Prof. D. K. Palwalia, Dr. U. K. Kalla, Dr. R. K. Kumawat, 2025-01-01

Power Electronics: Theory and Practicals Lalit Chandra Saikia, 2025-06-01 On the perspectives of SiC MOSFETs in high-frequency and high-power isolated DC/DC converters Eial Awwad, Abdullah, 2020-08-11 Increasing demand for efficiency and power density pushes Si based devices to some of their inherent material limits including those related to temperature operation switching frequency and blocking voltage Recently SiC based power devices are promising candidates for high power and high frequency switching applications Today SiC MOSFETs are commercially available from several manufacturers Although technology affiliated with SiC MOSFETs is improving rapidly many challenges remain and some of them are investigated in this work. The research work in this dissertation is divided into the three following parts Firstly the static and switching characteristics of the state of the art 1 2 kV planar and double trench SiC MOSFETs from two different manufacturers are evaluated The effects of different biasing voltages DC link voltages and temperatures are analysed The characterisation results show that the devices exhibit superior switching performances under different operating conditions Moreover several aspects of using the SiC MOSFET's body diode in a DC DC converter are investigated comparing the body diodes of planar and double trench devices Reverse recovery is evaluated in switching tests considering the case temperature switching rate forward current and applied voltage Based on the measurement results the junction temperature is estimated to quarantee safe operation A simple electro thermal model is proposed in order to estimate the maximum allowed switching frequency based on the thermal design of the SiC devices Using these results hard and soft switching

converters are designed and devices are characterised as being in continuous operation at a very high switching frequency of 1 MHz Thereafter the SiC MOSFETs are operated in a continuous mode in a 10 kW 100 250 kHz buck converter comparing synchronous rectification the use of the body diode and the use of an external Schottky diode Further the parallel operation of the planar devices is considered Thus the paralleling of SiC MOSFETs is investigated before comparing the devices in continuous converter operation In this regard the impact of the most common mismatch parameters on the static and dynamic current sharing of the transistors is evaluated showing that paralleling of SiC MOSFETs is feasible Subsequently an analytical model of SiC MOSFETs for switching loss optimisation is proposed The analytical model exhibits relatively close agreement with measurement results under different test conditions. The proposed model tracks the oscillation effectively during both turn on and off transitions This has been achieved by considering the influence of the most crucial parasitic elements in both power and gate loops In the second part a comprehensive short circuit ruggedness evaluation focusing on different failure modes of the planar and double trench SiC devices is presented The effects of different biasing voltages DC link voltages and gate resistances are evaluated Additionally the temperature dependence of the short circuit capability is evaluated and the associated failure modes are analysed Subsequently the design and test of two different methods for overcurrent protection are proposed The desaturation technique is applied to the SiC MOSFETs and compared to a second method that depends on the stray inductance of the devices Finally the benefits of using SiC devices in continuous high frequency high power DC DC converters is experimentally evaluated In this regard a design optimisation of a high frequency transformer is introduced and the impact of different core materials conductor designs and winding arrangements are evaluated A ZVZCS Phase Shift Full Bridge unidirectional DC DC converter is proposed using only the parasitic leakage inductance of the transformer Experimental results for a 10 kW 100 250 kHz prototype indicate an efficiency of up to 98 1% for the whole converter Furthermore an optimized control method is proposed to minimise the circulation current in the isolated bidirectional dual active bridge DC DC converter based on a modified dual phase shift control method This control method is also experimentally compared with traditional single phase shift control yielding a significant improvement in efficiency The experimental results confirm the theoretical analysis and show that the proposed control can enhance the overall converter efficiency and expand the ZVZCS range Die steigende Nachfrage nach Effizienz und Leistungsdichte bringt Si basierte eistungsbauteile an einige inh rente Materialgrenzen die unter anderem mit der Temperaturbelastung der Schaltfrequenz und der Blockierspannung in Zusammenhang stehen In j ngster Zeit sind SiC basierte Leistungsbauelemente vielversprechende Kandidaten fr Hochleistungs und Hochfrequenzanwendungen Aktuell sind SiC MOSFETs von mehreren Herstellern im Handel erh ltlich Obwohl sich die Technologie der SiC MOSFETs rasch verbessert werden viele Herausforderungen bestehen bleiben Einige dieser Herausforderungen werden in dieser Arbeit untersucht Die Untersuchungen in dieser Dissertation gliedern sich in die drei folgenden Teile Im ersten Teil erfolgt die statische und die

transiente Charakterisierung der aktuellen 1 2 kV Planarund Doubletrench SiC MOSFETs verschiedener Hersteller Die Auswirkungen unterschiedlicher Gatespannungen Zwischenkreisspannungen und Temperaturen werden analysiert Die Ergebnisse der Charakterisierung zeigen dass die Bauteile berlegene Schaltleistungen unter verschiedenen Betriebsbedingungen aufweisen Dar ber hinaus wird der Einsatz der internen SiC Bodydioden in einem DC DC Wandler untersucht wobei die Unterschiede zwischen Planar und Doppeltrench Bauteilen aufgezeigt werden Das Reverse Recovery Verhalten wird unter Ber cksichtigung der Geh usetemperatur der Schaltgeschwindigkeit des Durchlassstroms und der angelegten Spannung bewertet Anhand der Messergebnisse wird die Sperrschichttemperatur gesch tzt damit ein sicherer Betrieb gew hrleistet ist Ein einfaches elektrothermisches Modell wird vorgestellt um die maximal zul ssige Schaltfrequenz auf der Grundlage des thermischen Designs der SiC Bauteile abzusch tzen Anhand dieser Ergebnisse werden hart und weichschaltende Umrichter konzipiert und die Bauteile werden im Dauerbetrieb mit einer sehr hohen Schaltfrequenz von 1 MHz untersucht Danach werden die SiC MOSFETs im Dauerbetrieb in einem 10 kW 100 250 kHz Tiefsetzsteller betrieben Dabei wird die Synchrongleichrichtung die Verwendung der internen Diode und die Verwendung einer externen Schottky Diode verglichen Au erdem wird die Parallelisierung von SiC MOSFETs untersucht bevor die Parallelschaltung der verschiedenen Bauelemente ebenso im kontinuierlichen Konverterbetrieb verglichen wird Es wird der Einfluss der h ufigsten Parametervariationen auf die statische und dynamische Stromaufteilung der Transistoren analysiert was zeigt dass eine Parallelisierung von SiC MOSFETs m glich ist Anschlie end wird ein analytisches Modell der SiC MOSFETs zur Schaltverlustoptimierung vorgeschlagen Das analytische Modell zeigt eine relativ enge bereinstimmung mit den Messergebnissen unter verschiedenen Testbedingungen Das vorgeschlagene Modell bildet die Schwingungen sowohl beim Ein als auch beim Ausschalten effektiv nach Dies wurde durch die Ber cksichtigung der wichtigsten parasit ren Elemente in Strom und Gatekreisen erreicht Im zweiten Teil wird eine umfassende Bewertung der Kurzschlussfestigkeit mit Fokus auf verschiedene Ausfallmodi der planaren und double trench SiC Bauelemente vorgestellt Die Auswirkungen unterschiedlicher Gatespannungen Zwischenkreisspannungen und Gate Widerst nde werden ausgewertet Zus tzlich wird die temperaturabh ngige Kurzschlussf higkeit ausgewertet und die zugeh rigen Fehlerf lle werden analysiert Anschlie end wird die Auslegung und Pr fung von zwei verschiedenen Verfahren zum berstromschutz evaluiert Die Desaturation Technik wird auf SiC MOSFETs angewendet und mit einer zweiten Methode verglichen welche die parasit re Induktivit t der Bauelemente nutzt Schlie lich wird der Nutzen des Einsatzes von SiC Bauteilen in kontinuierlichen Hochfrequenz Hochleistungs DC DC Wandlern experimentell untersucht In diesem Zusammenhang wird eine Designoptimierung eines Hochfrequenztransformators vorgestellt und der Einfluss verschiedener Kernmaterialien Leiterausf hrungen und Wicklungsanordnungen wird bewertet Es wird ein unidirektionaler ZVZCS Vollbr cken DC DC Wandler vorgestellt der nur die parasit re Streuinduktivit t des Transformators verwendet Experimentelle Ergebnisse f r einen 10 kW 100 250 kHz

Prototyp zeigen einenWirkungsgrad von bis zu 98 1% fr den gesamten Umrichter Abschlie end wird ein optimiertes Regelverfahren verwendet welches auf einem modifizierten Dual Phase Shift Regelverfahren basiert um den Kreisstrom im isolierten bidirektionalen Dual Aktiv Br cken DC DC Wandler zu minimieren Diese Regelmethode wird experimentell mit der herk mmlichen Single Phase Shift Regelung verglichen Hierbei zeigt sich eine deutliche Effizienzsteigerung durch die neue Regelmethode Die experimentellen Ergebnisse best tigen die theoretische Analyse und zeigen dass die vorgeschlagene Regelung den Gesamtwirkungsgrad des Umrichters erh hen und den ZVZCS Bereich erweitern kann Systems Igor Korotyeyev, Valerii Zhuikov, Radoslaw Kasperek, 2018-10-03 Advances in mathematical methods computer technology and electrotechnical devices in particular continue to result in the creation of programs that are leading to increased labor productivity Mathematical and simulation programs and other programs that unite these two operations provide the ability to calculate transitional steady state processes stability conditions and harmonic composition and are often used to analyze processes in power electronic systems Electrotechnical Systems Calculation and Analysis with Mathematica and PSpice explores the potential of two such programs Mathematica and ORCAD PSpice as they are used for analysis in various areas The authors discuss the formulation of problems and the steps in their solution They focus on the analysis of transient steady state processes and their stability in non stationary and nonlinear systems with DC and AC converters All problems are solved using Mathematica and program codes are presented The authors use ORCAD PSpice to compare the results obtained by employing Mathematica and to demonstrate the peculiarities associated with its use This book clearly and concisely illustrates represented expressions variables and functions and the general application of the mathematical pocket Mathematica 4 2 for the analysis of the electromagnetic processes in electrotechnical systems It will be a valuable addition to the library of anyone working with electrotechnical systems **Power Electronics** Ned Mohan.Tore M. Undeland, William P. Robbins, 2003 CD ROM contains PSpice based simulation to illustrate basic concepts magnetic component design program PowerPoint slides to summarise topics companion web site available Ned Mohan, 2012-01-18 Author Ned Mohan has been a leader in EES education and research for decades His three book series on Power Electronics focuses on three essential topics in the power sequence based on applications relevant to this age of sustainable energy such as wind turbines and hybrid electric vehicles. The three topics include power electronics power systems and electric machines Key features in the first Edition build on Mohan's successful MNPERE texts his systems approach which puts dry technical detail in the context of applications and substantial pedagogical support including PPT s video clips animations clicker questions and a lab manual It follows a top down systems level approach to power electronics to highlight interrelationships between these sub fields It s intended to cover fundamental and practical design This book also follows a building block approach to power electronics that allows an in depth discussion of several important topics that are usually left Topics are carefully sequenced to maintain continuity and interest Role of Plug-in Electric Vehicles in

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