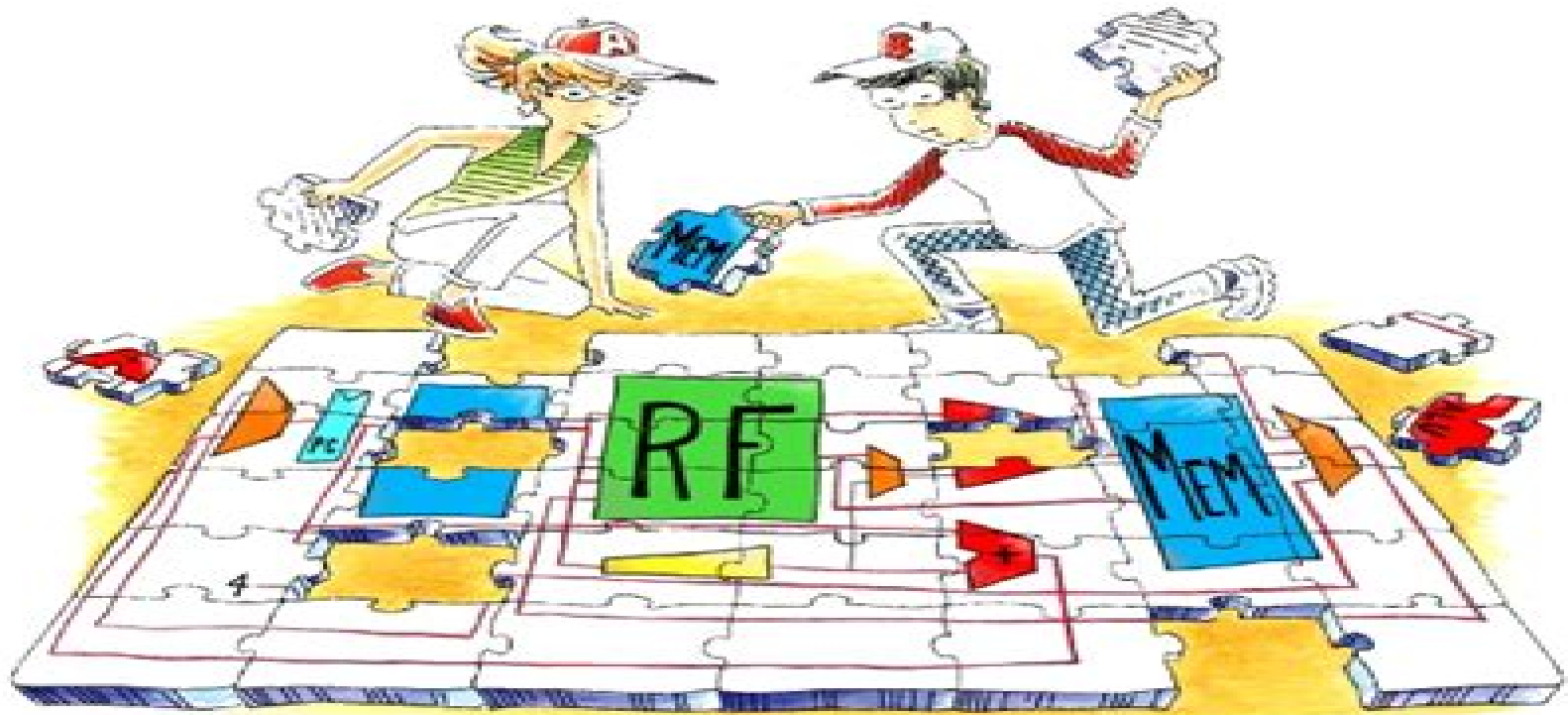


Digital Design and Computer Architecture

RISC-V Edition



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David Harris,Sarah Harris



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Digital Design and Computer Architecture David Harris, Sarah Harris, 2012-08-24 Digital Design and Computer Architecture Second Edition takes a unique and modern approach to digital design introducing the reader to the fundamentals of digital logic and then showing step by step how to build a MIPS microprocessor in both Verilog and VHDL. This new edition combines an engaging and humorous writing style with an updated and hands on approach to digital design. It presents new content on I/O systems in the context of general purpose processors found in a PC as well as microcontrollers found almost everywhere. Beginning with digital logic gates and progressing to the design of combinational and sequential circuits, the book uses these fundamental building blocks as the basis for the design of an actual MIPS processor. It provides practical examples of how to interface with peripherals using RS232, SPI, motor control, interrupts, wireless, and analog-to-digital conversion. SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD-based circuit design. There are also additional exercises and new examples of parallel and advanced architectures, practical I/O applications, embedded systems, and heterogeneous computing, plus a new appendix on C programming to strengthen the connection between programming and processor architecture. This new edition will appeal to professional computer engineers and to students taking a course that combines digital logic and computer architecture. Updated based on instructor feedback with more exercises and new examples of parallel and advanced architectures, practical I/O applications, embedded systems, and heterogeneous computing. Presents digital system design examples in both VHDL and SystemVerilog, updated for the second edition from Verilog, shown side by side to compare and contrast their strengths. Includes a new chapter on C programming to provide necessary prerequisites and strengthen the connection between programming and processor architecture. Companion Web site includes links to Xilinx CAD tools for FPGA design, lecture slides, laboratory projects, and solutions to exercises. Instructors can also register at textbooks.elsevier.com for access to Solutions to all exercises, PDF Lab materials with solutions, HDL for textbook examples, and exercise solutions. Lecture slides, PPT, Sample exams, Sample course syllabus, Figures from the text, JPG, PPT.

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for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of a RISC V microprocessor Gives students a full understanding of the RISC V instruction set architecture enabling them to build a RISC V processor and program the RISC V processor in hardware simulation software simulation and in hardware Includes both SystemVerilog and VHDL designs of fundamental building blocks as well as of single cycle multicycle and pipelined versions of the RISC V architecture Features a companion website with a bonus chapter on I O systems with practical examples that show how to use SparkFun s RED V RedBoard to communicate with peripheral devices such as LCDs Bluetooth radios and motors The companion website also includes appendices covering practical digital design issues and C programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises See the companion EdX MOOCs ENGR85A and ENGR85B with video lectures and interactive problems

Digital Design and Computer Architecture, ARM Edition Sarah Harris,David Harris,2015-04-09

Digital Design and Computer Architecture ARM Edition covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Combining an engaging and humorous writing style with an updated and hands on approach to digital design this book takes the reader from the fundamentals of digital logic to the actual design of an ARM processor By the end of this book readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works Beginning with digital logic gates and progressing to the design of combinational and sequential circuits this book uses these fundamental building blocks as the basis for designing an ARM processor SystemVerilog and VHDL are integrated throughout the text in examples illustrating the methods and techniques for CAD based circuit design The companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors This book will be a valuable resource for students taking a course that combines digital logic and computer architecture or students taking a two quarter sequence in digital logic and computer organization architecture Covers the fundamentals of digital logic design and reinforces logic concepts through the design of an ARM microprocessor Features side by side examples of the two most prominent Hardware Description Languages HDLs SystemVerilog and VHDL which illustrate and compare the ways each can be used in the design of digital systems Includes examples throughout the text that enhance the reader s understanding and retention of key concepts and techniques The Companion website includes a chapter on I O systems with practical examples that show how to use the Raspberry Pi computer to communicate with peripheral devices such as LCDs Bluetooth radios and motors The Companion website also includes appendices covering practical digital design issues and C

programming as well as links to CAD tools lecture slides laboratory projects and solutions to exercises

Digital Design and Computer Architecture David Harris, Sarah Harris, 2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material

Privacy Solutions and Security Frameworks in Information Protection Nemati, Hamid, 2012-09-30 While information technology continues to play a vital role in every aspect of our lives there is a greater need for the security and protection of this information Ensuring the trustworthiness and integrity is important in order for data to be used appropriately Privacy Solutions and Security Frameworks in Information Protection explores the areas of concern in guaranteeing the security and privacy of data and related technologies This reference source includes a range of topics in information security and privacy provided for a diverse readership ranging from academic and professional researchers to industry practitioners

Digital Design and Computer Organisation D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles

Large Floating Solutions Brydon T. Wang, Chien Ming Wang, Kim Weinert, Rutger de Graaf-van Dinther, 2025-09-26 Large Floating Solutions builds on the foundations laid in Large Floating Structures Technological Advances extending the analysis to the next

generation of Very Large Floating Structures VLFS as integrated climate adaptive urban systems This volume provides a comprehensive examination of the engineering environmental and architectural innovations required to deliver modular resilient floating cities alongside offshore energy aquaculture and transport infrastructure It addresses the complex legal regulatory and governance frameworks that underpin buoyant urbanism including property rights environmental compliance and international law and considers the role of artificial intelligence and autonomous maritime systems in urban management Together these contributions position floating infrastructure as a critical solution to escalating global water challenges

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IEEE Circuits & Devices, 2000 **Digital Design and Computer**

Architecture David Money Harris, Sarah L. Harris, 2015 Solutions Manual for Digital Design and Computer Organization Laurie Kelly, 2004-03 **Scientific and Technical Aerospace Reports**, 1985 Solutions manual for computer systems design and architecture Harry F. Jordan, Vincent P. Heuring, Miles Murdocca, 1996-10-01 27th EUROMICRO Conference, 2001 Annotation This proceedings volume contains the papers given by international researchers at the 27th Euromicro conference held in Warsaw in 2001 The conference featured workshops on multimedia and telecommunications software process and product improvement and component based software engineering A sampling of topics includes components for real time systems software reliability network protocols and audio video processing management The volume is not indexed c

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