

Digital Logic and Computer Design

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Yaohan Chu



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science from digital logic circuits to the design of a complete microcomputer system in a methodical and basic manner. Its intention is to present a clear understanding of the principles and basic tools required to design typical digital systems such as microcomputers. The book covers the latest version of Altera software called Quartus II. It provides a simplified introduction to VHDL along with a step by step procedure with tutorials on a CD. It is ideal for an introductory course in VHDL containing digital logic and microprocessors along with both VHDL and Verilog. The material in the text is divided into three sections: Fundamentals of digital logic circuits and design, Microprocessor microcomputer design, Overview of 16, 32 and 64 bit microprocessors manufactured by Intel and Motorola. *Digital Computer Design Fundamentals* Yaohan Chu, 1962

Digital Design and Computer Architecture David Harris, Sarah Harris, 2010-07-26. Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence. Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages (HDLs). Featuring examples of the two most widely used HDLs, VHDL and Verilog, the first half of the text prepares the reader for what follows in the second: the design of a MIPS Processor. By the end of Digital Design and Computer Architecture, readers will be able to build their own microprocessor and will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class. David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design. Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set, MIPS. Side by side examples of the two most prominent Hardware Design Languages, VHDL and Verilog, illustrate and compare the ways each can be used in the design of digital systems. Worked examples conclude each section to enhance the reader's understanding and retention of the material. *Introduction to Digital Logic Design* John Patrick Hayes, 1993. Textbook.

Fundamentals of Digital Logic and Microcomputer Design M. Rafiquzzaman, 2005-07-08. Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers. In this Fifth Edition, the author focuses on computer design at three levels: the device level, the logic level, and the system level. Basic topics are covered such as number systems and Boolean algebra, combinational and sequential logic design, as well as more advanced subjects such as assembly language programming and microprocessor based system design. Numerous examples are provided throughout the text. Coverage includes digital circuits at the gate and flip flop levels, analysis and design of combinational and sequential circuits, microcomputer organization, architecture, and programming concepts, design of computer instruction sets, CPU memory and I/O. System design features associated with popular microprocessors from Intel and Motorola. Future plans in microprocessor development. An instructor's manual available upon request. Additionally, the accompanying CD-ROM contains step by step procedures for installing and using

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Fundamentals of Digital Logic with Verilog Design is intended for an introductory course in digital logic design which is a basic course in most Electrical and Computer Engineering programs The authors provide a desirable balance between classical and modern design approaches Basic concepts are introduced using simple logic circuits which are designed by using both manual techniques and modern CAD tool based methods Having established the fundamental concepts more complex realistic circuits are then designed with the CAD tools The Verilog language is an integral part of design techniques used throughout the book Altera's advanced Max plus II CAD system on CD ROM and a series of step by step tutorials are included

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