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Second Edition



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Digital Logic Design B. Holdsworth, 2014-05-12 *Digital Logic Design* Second Edition provides a basic understanding of digital logic design with emphasis on the two alternative methods of design available to the digital engineer. This book describes the digital design techniques which have become increasingly important. Organized into 14 chapters, this edition begins with an overview of the essential laws of Boolean algebra, K map plotting techniques, as well as the simplification of Boolean functions. This text then presents the properties and develops the characteristic equations of a number of various types of flip flop. Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers. This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation. The final chapter deals with simple coding techniques and the principles of error detection and correction. This book is a valuable resource for undergraduate students, digital engineers, and scientists. **Subject Guide to Books in Print**, 1996 **Books in Print Supplement**, 2002 Electrónica digital y microprocesadores Eduardo Santamaría, 1993 **Scientific and Technical Books in Print**, 1972 **NBS Special Publication**, 1965 **National Bureau of Standards Miscellaneous Publication**, 1965 Miscellaneous Publication - National Bureau of Standards United States. National Bureau of Standards, 1965 **Wireless World and Radio Review**, 1979 **The British National Bibliography** Arthur James Wells, 1996 *Digital Logic Circuit Analysis and Design* Victor Peter Nelson, 1995 For introductory digital logic design or computer engineering courses in electrical and computer engineering or computer science at the sophomore or junior level. Many recent texts place instructors in the difficult position of choosing between authoritative state of the art coverage and an approach that is highly supportive of student learning. This carefully developed text was widely praised by reviewers for both its great clarity and its rigor. The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language and has abundant coverage of current topics of interest such as programmable devices, computer aided design, and testability. An unusually large number of illustrations, examples, and problems help students gain a solid sense of how theory underlies practice. **Books in Print**, 1981 **Computer Literature Bibliography** United States. National Bureau of Standards, 1965 Computer Education, 1976 **Computer Literature Bibliography: 1946-1963** W. W. Youden, 1965 *British Books in Print*, 1980 Digital Logic Circuit Analysis and Design [rental Edition] Victor P Nelson, Bill D Carroll, H Troy Nagle, David Irwin, 2020-02-18 This print textbook is available for students to rent for their classes. The Pearson print rental program provides students with affordable access to learning materials so they come to class ready to succeed. Balance breadth and depth of coverage with practical real world design methods. *Digital Logic Circuit Analysis and Design* provides an authoritative state of the art approach to the fundamentals of digital logic analysis and design that is highly supportive of student learning. The book balances theory and practice in depth without getting bogged down in excessive technical or mathematical language. Retaining its tradition of

both clarity and rigor the 2nd Edition features extensive coverage of current topics of interest such as modeling with Verilog and VHDL design with programmable devices and computer aided design Filled with updated illustrations examples and problems this text helps students gain a solid sense of how theory underlies practice This title is also available digitally as a standalone Pearson eText Contact your Pearson rep for more information *ISTFA 2010* ,2010-01-01 **The Publishers'**
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