

Brock J. LaMeres

Introduction to Logic Circuits & Logic Design with VHDL

Introduction To Logic Circuits Logic Design With Vhdl

Ian Grout



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LaMeres, 2019-03-19 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1 7 followed by logic design Chapters 8 13 or a single accelerated course that uses the early chapters as reference material

Introduction to Logic Design, Second Edition Sajjan G. Shiva, 1998-01-20 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery Fundamentals of

Digital Logic with VHDL Design Stephen D. Brown, Zvonko G. Vranesic, 2000 This text is intended for the digital logic design course found in most electrical and computer engineering programs The authors provide a balance between basic concepts and practical applications using computer aided design tools **Introduction to Logic Circuit Design Preliminary**

Edition Brock J. LaMeres, 2015-08-31 Fundamentals of Digital Logic with VHDL Design Stephen Brown, Zvonko

Vranesic,2022 Fundamentals of Digital Logic with VHDL Design is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs A successful designer of digital logic circuits needs a good understanding of the classical methods of logic design and a firm grasp of the modern design approach that relies on computer aided design CAD tools The main goals of this book are to teach students the fundamental concepts of classical manual digital design and to illustrate clearly the way in which digital circuits are designed today using CAD tools This title will be available in Connect with the MHeBook but will not have SmartBook at this time **Introduction to Logic Circuit Design** Brock J. Lameres,2015-10-12 Digital Electronics: A Primer - Introductory Logic Circuit Design Mark S Nixon,2015-01-27 This practical introduction explains exactly how digital circuits are designed from the basic circuit to the advanced system It covers combinational logic circuits which collect logic signals to sequential logic circuits which embody time and memory to progress through sequences of states The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions Based on the author s extensive experience in teaching digital electronics to undergraduates the book translates theory directly into practice and presents the essential information in a compact digestible style Worked problems and examples are accompanied by abbreviated solutions with demonstrations to ensure that the design material and the circuits operation are fully understood This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction

Fundamentals of Digital Logic with VHDL Design Stephen D. Brown,Zvonko G. Vranesic,2023 Fundamentals of Digital Logic with VHDL Design 4th Edition is intended for an introductory course in digital logic design which is a basic course in most electrical and computer engineering programs A successful designer of digital logic circuits needs a good understanding of basic concepts and a firm grasp of computer aided design CAD tools **Digital Systems Design with FPGAs and CPLDs** Ian Grout,2011-04-08 Digital Systems Design with FPGAs and CPLDs explains how to design and develop digital electronic systems using programmable logic devices PLDs Totally practical in nature the book features numerous quantify when known case study designs using a variety of Field Programmable Gate Array FPGA and Complex Programmable Logic Devices CPLD for a range of applications from control and instrumentation to semiconductor automatic test equipment Key features include Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design With this book engineers will be able to Use PLD technology to develop digital and mixed signal electronic systems Develop PLD based designs using both schematic capture and VHDL synthesis techniques Interface a PLD to digital and mixed signal systems Undertake complete design exercises from design concept through to the build and test of PLD based electronic hardwareThis book will be ideal for electronic and computer engineering students taking a practical or Lab based course on digital systems development using PLDs and for engineers in

industry looking for concrete advice on developing a digital system using a FPGA or CPLD as its core Case studies that provide a walk through of the design process highlighting the trade offs involved Discussion of real world issues such as choice of device pin out power supply power supply decoupling signal integrity for embedding FPGAs within a PCB based design

A Practical Guide for Simulation and FPGA Implementation of Digital Design Bekkay Hajji, Adel Mellit, Loubna Bouselham, 2022-03-21 This book introduces the FPGA technology used in the laboratory sessions and provides a step by step guide for designing and simulation of digital circuits It utilizes the VHDL language which is one of the most common language used to describe the design of digital systems The Quartus II Xilinx ISE 14.7 and ModelSim software are used to process the VHDL code and make simulations and then the Altera and Xilinx FPGA platforms are employed to implement the simulated digital designs The book is composed of four parts The first part of this book has two chapters and covers various aspects FPGA architectures ASIC vs FPGA comparison FPGA design flow and basic VHDL concepts necessary to describe the design of digital systems The second part of the book includes three chapters that deal with the design of digital circuits such as combinational logic circuits sequential logic circuits and finite state machines The third part of the book is reserved for laboratory projects carried out on the FPGA platform It is a largely hands on lab class for design digital circuits and implementing their designs on the Altera FPGA platform Finally the fourth part of this work is devoted to recent applications carried out on FPGAs in particular advanced techniques in renewable energy systems The book is primarily intended for students scholars and industrial practitioners interested in the design of modern digital systems

Fundamentals of Digital Logic with VHDL Design with CD-ROM Stephen Brown, Zvonko Vranesic, 2008-04-14

Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text

Introduction to Logic Design Svetlana N. Yanushkevich, Vlad P. Shmerko, 2008-01-25 With an abundance of insightful examples problems and computer experiments Introduction to Logic Design provides a balanced easy to read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the

A Tutorial Introduction to VHDL Programming Orhan Gazi, 2018-08-18 This book helps readers create good VHDL descriptions and simulate VHDL designs It teaches VHDL using

selected sample problems which are solved step by step and with precise explanations so that readers get a clear idea of what a good VHDL code should look like The book is divided into eight chapters covering aspects ranging from the very basics of VHDL syntax and the module concept to VHDL logic circuit implementations In the first chapter the entity and architecture parts of a VHDL program are explained in detail The second chapter explains the implementations of combinational logic circuits in VHDL language while the following chapters offer information on the simulation of VHDL programs and demonstrate how to define data types other than the standard ones available in VHDL libraries In turn the fifth chapter explains the implementation of clocked sequential logic circuits and the sixth shows the implementation of registers and counter packages The book's last two chapters detail how components functions and procedures as well as floating point numbers are implemented in VHDL The book offers extensive exercises at the end of each chapter inviting readers to learn VHDL by doing it and writing good code

Principles of Modern Digital Design Parag K. Lala, 2007-09-10

PRINCIPLES OF MODERN DIGITAL DESIGN FROM UNDERLYING PRINCIPLES TO IMPLEMENTATION A THOROUGH INTRODUCTION TO DIGITAL LOGIC DESIGN With this book readers discover the connection between logic design principles and theory and the logic design and optimization techniques used in practice Therefore they not only learn how to implement current design techniques but also how these techniques were developed and why they work With a deeper understanding of the underlying principles readers become better problem solvers when faced with new and difficult digital design challenges *Principles of Modern Digital Design* begins with an examination of number systems and binary code followed by the fundamental concepts of digital logic Next readers advance to combinational logic design Armed with this foundation they are then introduced to VHDL a powerful language used to describe the function of digital circuits and systems All the major topics needed for a thorough understanding of modern digital design are presented including Fundamentals of synchronous sequential circuits and synchronous sequential circuit design Combinational logic design using VHDL Counter design Sequential circuit design using VHDL Asynchronous sequential circuits VHDL based logic design examples are provided throughout the book to illustrate both the underlying principles and practical design applications Each chapter is followed by exercises that enable readers to put their skills into practice by solving realistic digital design problems An accompanying website with Quartus II software enables readers to replicate the book's examples and perform the exercises This book can be used for either a two or one semester course for undergraduate students in electrical and computer engineering and computer science Its thorough explanation of theory coupled with examples and exercises enables both students and practitioners to master and implement modern digital design techniques with confidence

Structured Logic Design with VHDL James R. Armstrong, F. Gail Gray, 1993

Hardware Logic Design **IEEE Circuits & Devices**, 2000

Fundamentals of Digital Logic with VHDL Design Stephen Brown, 2008

Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips

Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed The book emphasizes CAD through the use of Altera s Quartus II CAD software a state of the art digital circuit design package This software produces automatic mapping of designs written in VHDL into Field Programmable Gate Arrays

Practical Digital Design Bruce Reidenbach, 2022-07-15 The VHSIC Hardware Description Language VHDL is one of the two most popular languages used to design digital logic circuits This book provides a comprehensive introduction to the syntax and the most commonly used features of VHDL It also presents a formal digital design process and the best case design practices that have been developed over more than twenty five years of VHDL design experience by the author in military ground and satellite communication systems Unlike other books on this subject this real world professional experience captures not only the what of VHDL but also the how Throughout the book recommended methods for performing digital design are presented along with the common pitfalls and the techniques used to successfully avoid them Written for students learning VHDL for the first time as well as professional development material for experienced engineers this book s contents minimize design time while maximizing the probability of first time design success

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