

## **SIGNAL INTEGRITY AND ELECTROMAGNETIC BROADBAND PACKAGING MODEL EXTRACTION OF FULL DIFFERENTIAL BANDPASS FILTER ON IPD WITH BGA PACKAGING**

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**Abstract**—Since the system-level package was proposed, the electronics industry has increasingly attached importance to both directly relevant and related issues, and the scope of system-level package usage has increased. Creating more complex system-level package structures, thereby leading to the design of overall electrical effects, requires more electromagnetic simulation resources, and therefore a great deal of time in the design process. The main purpose of this paper is to analyze the effects of system-level packaging, and to establish systems-in-package in accordance with electrical specifications. Using a segmented approach, this paper also builds an overall model for designers to predict electrical characteristics, thus shortening the product development schedule. In this paper, the transmission effects of a substrate are analyzed by changing the length of the substrate transmission line, with or without a thermal ground ball and ground ring. Previously established package IP are cascaded to establish the model of the package substrate, which verifies the feasibility of the package IP. We then analyze the characteristics of the interference between chips and package using an integrated passive device, and propose a complete package equivalent circuit model.

### **1. INTRODUCTION**

As shown in Fig. 1 [1–13], Moore's Law proposed that the number of transistors on an integrated circuit would double every 24 months (in 1975 this was changed to 18 months), enabling more chip functions

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# Signal Integrity And Electromagnetic Broadband Packaging

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