

Labview Tutorial D: Using Design Variables and Parametric Analysis

Created for the NRE, NUSM programs by Casey Wallace
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Introduction

Design variables provide a method for specifying circuit parameters just before a simulation. This allows you to make quick changes to parameter values across single or multiple circuit components without having to edit any schematic schematics. Design variables can also be used in static parametric analysis, which can change a design variable across a range of values and display multiple simulation results as a single plot.

Using Design Variables

This part of the tutorial will show you how to parametrize the width of the NMOS and PMOS transistors of an inverter by using design variables. These variable values are then set using the **Running Design Environment** prior to simulation.

1. Select a copy of the circuitry with schematics you have previously designed, or create a new one.
2. Open the schematics of the circuit.
3. Right click on the NMOS transistor to select it, and open the properties by either clicking on it and selecting **Properties**, or you can press "q" as a shortcut.
4. Select the **width** in the **width** box, and type `W`. This specifies that a variable called `W` will be used to set the width of the NMOS. When you do this, observe that values for "Channel/Source/Drainless Area" change from a constant value to a formula that involves the variable `W`. Always check to make sure that formula is updated when you change the width to a variable; otherwise, your simulation results will be incorrect. Click **OK** to apply the change.
5. Repeat this procedure to change the width of the PMOS to `W`.
6. Select **Design** **W** **Channel** and **name**. Verify that there are no errors.
7. Launch the **Running Design Environment**.
8. Set up the simulation environment as you normally would for simulating a schematic. This includes specifying a stimulus file and transient run length, enabling the NUSM method under in the environment options, and selecting the outputs to be plotted on the schematic.
9. In **NUSM**, select **Variables** **W** **Copy Name(s) to**. The design variable `W` and `W` should appear in the **Design Variables** area of the **Running Environment Windows**.
10. In **NUSM**, select **Variable** **W** **Value**. Enter 100 in `W`. In the value box, type `1` `W` and then apply the value by clicking **Apply**. Change the value of `W` to `0.5` `W` and click **Apply**. Check the values have been assigned, click **OK**. See Figure 1 and 2 below.
11. In **NUSM**, use the connection by selecting **Transistors** **W** **Variable** and **W**. After setting the the simulation to complete, the variable values will go up. The results should be

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