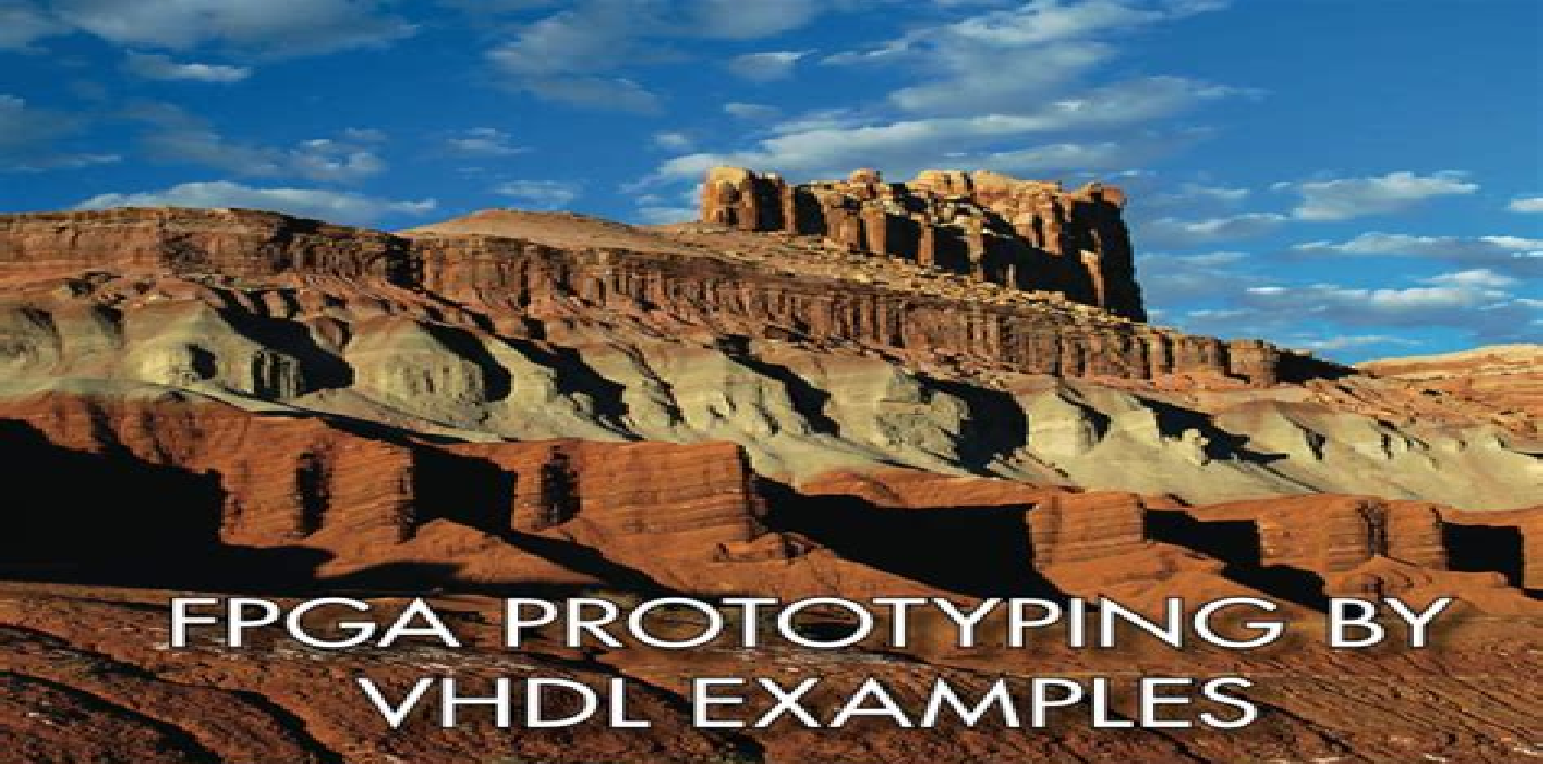




FPGA PROTOTYPING BY VHDL EXAMPLES

XILINX SPARTAN™-3 VERSION



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