

SEE INSTRUCTION SET

OPCODE		DESCRIPTION	386		486		PENTIUM	
AAA		Adjust register addition	AAA	status	Jump if not above or equal	FAHF		Push flags onto stack
AAD		Adjust register division	AAE	status	Jump if not below	FL	di, eax	Rotate left through carry
AAS		Adjust register subtract	AAH	status	Jump if below or equal	HL	di, eax	Rotate right through carry
AAM		Adjust register multiplication	AO	status	Jump if not carry	MO		Repeat string operation
ADD	di, eax	Add with carry	AOE	status	Jump if not equal	MOB		Repeat string operation
ADD	di, eax	Add	AOH	status	Jump if not greater	MOB		Repeat string operation
AND	di, eax	Logical AND	AOO	status	Jump if not greater or equal	MOVB		Repeat string operation
CALL	push	Call a procedure	AOE	status	Jump if not less or equal	MOVB	[reg]	Rotate from procthread
CM		Compare byte to word	AOH	status	Jump if not more	ROL	di, eax	Rotate left
CM		Compare word to word	AOO	status	Jump if not overflow	ROB	di, eax	Rotate right
CM		Compare instruction flag	AOE	status	Jump if not parity	ROB		Rotate left with flags
CM		Compare interrupt flag	AOH	status	Jump if not sign	ROB	di, eax	Rotate from procthread
CM		Compare carry flag	AOO	status	Jump if not sign	ROB	di, eax	Rotate from procthread
CM	di, eax	Compare	AOE	status	Jump if parity odd	ROB	di, eax	Rotate from procthread
CM	di, eax	Compare	AOH	status	Jump if parity even	ROB	di, eax	Rotate from procthread
CM	di, eax	Compare	AOO	status	Jump if sign	ROB	di, eax	Rotate from procthread
CM	di, eax	Compare	AOE	status	Jump if sign	ROB	di, eax	Rotate from procthread
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CM	di, eax	Compare	AOH	status	Jump if sign	ROB		

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8086/8088 User's Manual Intel Corporation,1989 Microprocessor 8085, 8086 Abhishek Yadav,2008

Microprocessor 8086 : Architecture, Programming and Interfacing Mathur Sunil,2010-12 Primarily intended for the undergraduate students of electronics and communication engineering computer science and engineering and information technology this book skilfully integrates both the hardware and software aspects of the 8086 microprocessor It offers the students an up to date account of the state of the art microprocessors and therefore can be regarded as an incomparable source of information on recently developed microprocessor chips The book covers the advanced microprocessor architecture of the Intel microprocessor family from 8086 to Pentium 4 The text is organized in four parts Part I Chapters 1 7 includes a detailed description of the architecture organization instruction set and assembler directives of microprocessor 8086 Part II Chapters 8 11 discusses the math coprocessor multiprocessing and multiprogramming the different types of data transfer schemes and memory concepts Part III Chapters 12 15 covers programmable interfacing chips with the help of extensive interfacing examples Part IV Chapters 16 18 deals with advanced processors from 80186 to Pentium 4 This well organized and student friendly text should prove to be an invaluable asset to the students as well as the practising engineers KEY FEATURES Gives elaborate programming examples to develop the analytical ability of students Provides solved examples covering different types of typical interfacing problems to develop the practical skills of students Furnishes chapter end exercises to reinforce the understanding of the subject **The 8086 Primer** Stephen P. Morse,1980 *The Intel*

Microprocessors Barry B. Brey,1994 *The 8086 and 80286 Microprocessors* Avtar Singh,Walter A. Triebel,1990

Programming the Intel 80386 Bud E. Smith,Mark T. Johnson,1987 Intel386 SX Microprocessor Programmer's Reference Manual Intel Corporation,1991 **The 8086 Book** Russell Rector,George Alexy,1980 **8086/8088, 80286,**

80386, and 80486 Assembly Language Programming Barry B. Brey,1994 *Introduction to Computer Engineering* Richard E. Haskell,1993 This text is for first and second year undergraduates studying the fundamentals of computer engineering digital logic and microprocessors Assuming little background in computer systems the book presents the basics then illustrates them with an examination of 8086 architecture and programming The intention is to teach digital logic by using programmable logic devices PLDs and the CUPL language **The 8086/8088 Family** John E. Uffenbeck,1987 This book presents the full range of Intel 80x86 microprocessors in context as a component of a comprehensive microprocessor system It provides a thorough single volume coverage of all Intel processors relative to their application in the PC and is as much an introduction to the PC itself as to Intel chips Covers all PC related technologies including memory data communications and PC bus standards The second edition of The 8086 8088 Family Design Programming and Interfacing has been revised to include the latest most up to date information and technologies This edition now covers Windows a description of the MS DOS BIOS services and function calls two completely revised software chapters an updated chapter on

memory coverage of the 16550 UART and common modern standards and a new chapter on PC architecture and the common bus systems

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