

SCICOS-HIL

Scicos Hardware In the Loop

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Project PETA-00 – Scilab/Scicos Dev. Team

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An Introduction to Scicos

Building the simulation of an hybrid dynamical system as scripts, using the powerful functions of the Scicos language is possible [1], but it is time consuming and it is very easy to insert bugs during the manual coding. To simplify this task, Scicos includes Scicos GUI a graphical hybrid dynamical system editor and compilation interface. Scicos is used for applications in control systems, communication, signal processing, operating systems, and to study physical and biological systems. Scicos helps graphical editor if it is possible to place, configure and connect blocks, to order to create diagrams to model hybrid dynamical systems, and simulate them.

Most of the Scicos graphical user interface is written in Scilab language, for complete integration with Scilab, easy customization and maximum flexibility. Scicos is included in Scilab and available for both Windows and Linux. To develop Scicos blocks the binary, pre-compiled Scilab/Scicos version is sufficient.

The Scicos blocks internal structure

Each blocks is represented by two functions:

- the interfacing function, written in Scilab language, it defines the graphical representation, the input, output and control ports, signals and the user configurable parameters,
- the computational function: the real code used during the simulation. The computational function can be written in Scilab language, for easy development, or in C, for maximum efficiency and speed. Further in

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**Herbert Hanselmann, Society of
Automotive Engineers**



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Modeling and Simulation in Scilab/Scicos with ScicosLab 4.4 Stephen L. Campbell, Jean-Philippe Chancelier, Ramine Nikoukhah, 2009-12-21 Scilab and its Scicos block diagram graphical editor with a special emphasis on modeling and simulation tools The first part is a detailed Scilab tutorial and the second is dedicated to modeling and simulation of dynamical systems in Scicos The concepts are illustrated through numerous examples and all code used in the book is available to the reader

Unmanned Aircraft Systems Ella Atkins, Anibal Ollero, Antonios Tsourdos, 2017-01-17 UNMANNED AIRCRAFT SYSTEMS UNMANNED AIRCRAFT SYSTEMS An unmanned aircraft system UAS sometimes called a drone is an aircraft without a human pilot on board instead the UAS can be controlled by an operator station on the ground or may be autonomous in operation UAS are capable of addressing a broad range of applications in diverse complex environments Traditionally employed in mainly military applications recent regulatory changes around the world are leading to an explosion of interest and wide ranging new applications for UAS in civil airspace Covering the design development operation and mission profiles of unmanned aircraft systems this single comprehensive volume forms a complete stand alone reference on the topic The volume integrates with the online Wiley Encyclopedia of Aerospace Engineering providing many new and updated articles for existing subscribers to that work The chapters cover the following items Airframe configurations and design launch systems power generation propulsion Operations missions integration issues and airspace access Coordination multivehicle cooperation and human oversight With contributions from leading experts this volume is intended to be a valuable addition and a useful resource for aerospace manufacturers and suppliers governmental and industrial aerospace research establishments airline and aviation industries university engineering and science departments and industry analysts consultants and researchers

Chip Hardware-in-the-loop Simulation Framework Christian Köhler, 2010 *Hardware in the Loop Simulation for Industrial Printers* Ricardo Nieto Rodríguez, 2019 Hardware in the loop simulation has been used in the last years in the automotive navigation and aerospace industry to eliminate iterations in the design face of hardware This process consists in 4 main parts The controller model or Algorithm model of the plant embedded hardware and the real time processor to generate analog digital I O and communications The aim of this project is to first model a subsystem plant that is essential in all the printers and generates a lot of problems which is the ink delivery system Second objective of the thesis is to integrate the 4 parts of the HIL running simulations to prove feasibility of the model and comparing it versus reality

Designing Time Efficient Real Time Hardware in the Loop Simulation Using Input Profile Temporal Compression Sourindu Chatterjee, 2017

Advances in Desktop Hardware-in-the-loop Simulation Herbert Hanselmann, Society of Automotive Engineers, 1997

Development of HiLS and Paramics/VS-PLUS Integration, 2008

The Time-based Approach to Embedded Programming Judith Liebman, 2002

Hardware-in-loop Simulation Using M68HC12 Deepthi B. Teegala, University of Hartford. College of Engineering, Technology, and Architecture, 2011

Design and Integration of Modular Hardware-in-the-loop Simulator Biljana Marinkovic,2008 *Development of a Hardware-in-the-loop Analysis Framework for Advanced Its Applications* Matthew Stephen Roe,2009

As Intelligent Transportation Systems ITS become more prevalent there is a need for a system capable of the rigorous evaluation of new ITS strategies for a wide variety of applications Pre deployment testing and fine tuning of the system performance evaluation and alternatives analysis are all potential benefits that could be gained through the evaluation of ITS Simulation an increasingly popular tool for transportation analysis would seem an ideal solution to this problem as it allows for the consideration of many scenarios that may be improbable or impossible to observe in the field Also simulation provides a framework that allows for the application of rigorous analysis techniques to the output data providing an accurate and statistically significant conclusion The difficulty is that many ITS strategies are difficult or impossible to implement in a simulated environment The rapid nature of technology development and the complicated nature of many ITS solutions are difficult to emulate in simulation models Furthermore the emulation of a particular ITS solution is not guaranteed to provide the same result that the physical system would were it subject to the same inputs This study seeks to establish a framework for the analysis of advanced ITS applications through the use of Hardware in the Loop Simulation HILS which provides a procedure for interfacing simulation models with real world hardware to conduct analysis This solution provides the benefits of both advanced ITS evaluation and simulation for powerful and accurate analysis A framework is established that includes all the steps of the modeling process including construction validation calibration and output analysis This ensures that the process surrounding the HILS implementation is valid so that the results of the evaluation are accurate and defensible Finally a case study of the application of the developed framework to the evaluation a real world implementation of an advanced ITS application SCATS in this case is considered The effectiveness of the framework in creating and evaluating a corridor using a simulation model wed to real world hardware is shown The results of the analysis show the power of this method when correctly applied and demonstrate where further analysis could expand upon the proposed procedure

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