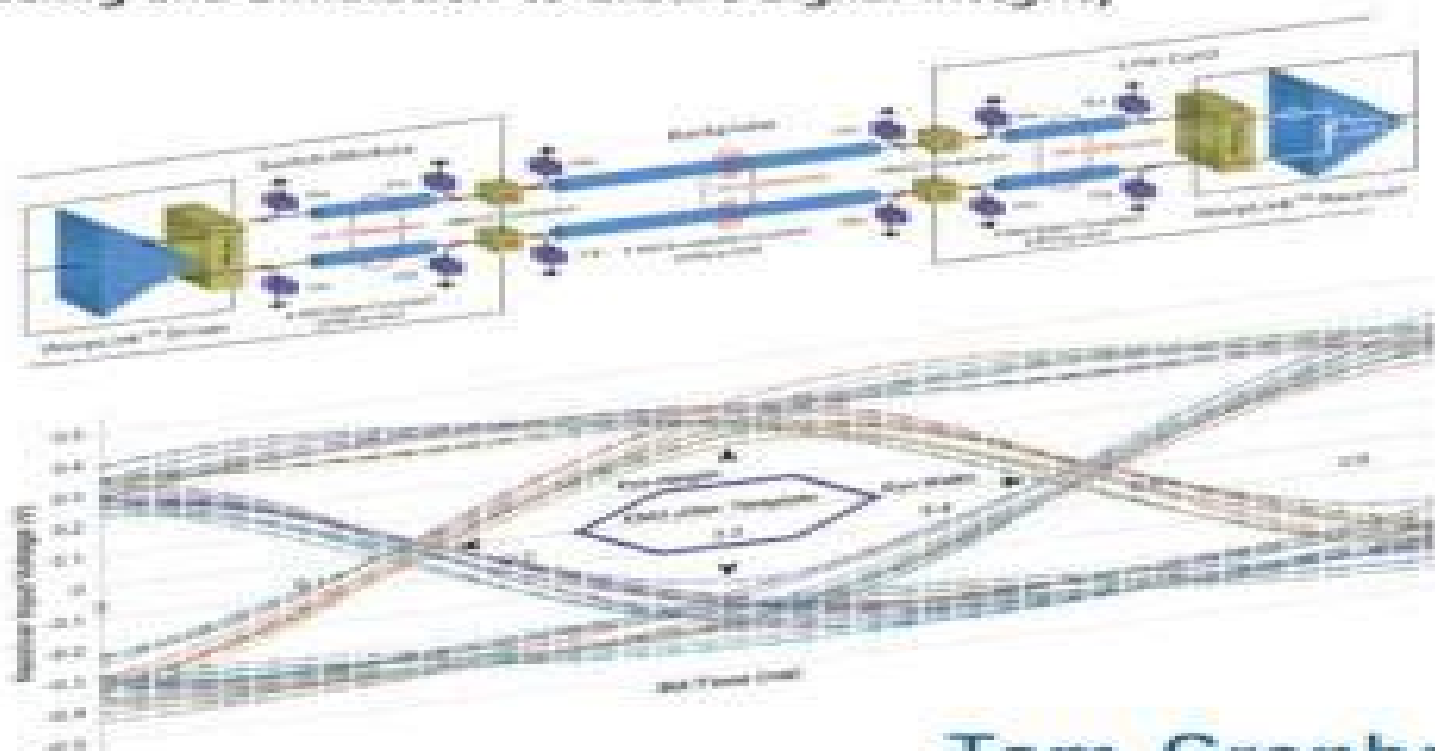


Handbook of Digital Techniques for High-Speed Design

Design Examples, Signaling and Memory Technologies, Fiber Optics,
Modeling and Simulation to Ensure Signal Integrity



Tom Granberg

Prentice Hall Modern Semiconductor Design Series

Handbook Of Digital Techniques For High Speed Design
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Handbook of Digital Techniques for High-Speed Design Tom Granberg, 2004-05-24 Semiconductor Modeling: Roy Leventhal, Lynne Green, 2007-01-10 Semiconductor Modeling For Simulating Signal Power and Electromagnetic Integrity assists engineers both recent graduates and working product designers in designing high speed circuits The authors apply circuit theory circuit simulation tools and practical experience to help the engineer understand semiconductor modeling as applied to high speed digital designs The emphasis is on semiconductor modeling with PCB transmission line effects equipment enclosure effects and other modeling issues discussed as needed The text addresses many practical considerations including process variation model accuracy validation and verification signal integrity and design flow Readers will benefit from its survey of modeling for semiconductors packages and interconnects along with usable advice on how to get complex high speed prototypes to work on the first try Highlights include Presents a very complete and well balanced treatment of modeling of semiconductors packages and interconnects Facilitates reader comprehension of the whole field of high speed modeling including digital and RF circuits Combines practical modeling techniques with the latest EDA tools for simulation and successful high speed digital design Facilitates resolution of practical every day problems Presents modeling from its historical roots to current state of the art Facilitates keeping abreast of the latest modeling developments as they continue to unfold **Practical FPGA Programming in C** David Pellerin, Scott Thibault, 2005 FPGA brings high performance applications to market quickly this book covers the many emerging platforms in a proven effective manner **Forthcoming Books** Rose Arny, 2004 *High-Speed Digital System Design* Stephen H. Hall, Garrett W. Hall, James A. McCall, 2000-09-08 The importance of interconnect design Ideal transmission line fundamentals Crosstalk Nonideal interconnect issues Connectors packages and vias Nonideal return paths simultaneous switching noise and power delivery Buffer modeling Digital timing analysis Design methodologies Radiated emissions compliance and system noise minimization High speed measurement techniques **Handbook of Digital Techniques for High-Speed Design**, 2007-09 *Digital Signal Integrity* Brian Young, 2001 State of the art techniques for predicting and achieving target performance levels Theory practice general signal integrity issues and leading edge experimental techniques Model and simulate high speed digital systems for maximum performance Maximizing the performance of digital systems means optimizing their high speed interconnections Digital Signal Integrity gives engineers all the theory and practical methods they need to accurately model and simulate those interconnections and predict real world performance Whether you re modeling microprocessors memories DSPs or ASICs these techniques will get you to market faster with greater reliability Coverage includes In depth reviews of inductance capacitance resistance single and multiconductor transmission lines generalized termination schemes crosstalk differential signaling and other modeling simulation issues Multiconductor

interconnects packages sockets connectors and buses Modal decomposition understanding the outputs generated by commercial modeling software Layer peeling with time domain reflectometry its power and limitations Experimental techniques for characterizing interconnect parasitics In Digital Signal Integrity Motorola senior engineer Brian Young presents broad coverage of modeling from data obtained through electromagnetic simulation transmission line theory frequency and time domain modeling analog circuit simulation digital signaling and architecture Young offers a strong mathematical foundation for every technique as well as over 100 end of chapter problems If you're stretching the performance envelope you must be able to rely on your models and simulations With this book you can *Advanced Signal Integrity for High-Speed Digital Designs* Stephen H. Hall, Howard L. Heck, 2011-09-20 A synergistic approach to signal integrity for high speed digital design This book is designed to provide contemporary readers with an understanding of the emerging high speed signal integrity issues that are creating roadblocks in digital design Written by the foremost experts on the subject it leverages concepts and techniques from non related fields such as applied physics and microwave engineering and applies them to high speed digital design creating the optimal combination between theory and practical applications Following an introduction to the importance of signal integrity chapter coverage includes Electromagnetic fundamentals for signal integrity Transmission line fundamentals Crosstalk Non ideal conductor models including surface roughness and frequency dependent inductance Frequency dependent properties of dielectrics Differential signaling Mathematical requirements of physical channels S parameters for digital engineers Non ideal return paths and via resonance I O circuits and models Equalization Modeling and budgeting of timing jitter and noise System analysis using response surface modeling Each chapter includes many figures and numerous examples to help readers relate the concepts to everyday design and concludes with problems for readers to test their understanding of the material *Advanced Signal Integrity for High Speed Digital Designs* is suitable as a textbook for graduate level courses on signal integrity for programs taught in industry for professional engineers and as a reference for the high speed digital designer **Solutions Manual for Handbook of Digital Techniques for High-Speed Design** Tom Granberg, 2004-06-01 **High Speed Digital Design: A Handbook Of Black Magic** Howard Johnson, Johnson, 1993 **Handbook Of Digital Techniques For High-Speed Design** Tom Granberg, Ph. D., 2004 *High Speed Digital Design* Hanqiao Zhang, Steven Krooswyk, Jeffrey Ou, 2015-08-17 High Speed Digital Design discusses the major factors to consider in designing a high speed digital system and how design concepts affect the functionality of the system as a whole It will help you understand why signals act so differently on a high speed digital system identify the various problems that may occur in the design and research solutions to minimize their impact and address their root causes The authors offer a strong foundation that will help you get high speed digital system designs right the first time Taking a systems design approach High Speed Digital Design offers a progression from fundamental to advanced concepts starting with transmission line theory covering core concepts as well as recent developments It then

covers the challenges of signal and power integrity offers guidelines for channel modeling and optimizing link circuits Tying together concepts presented throughout the book the authors present Intel processors and chipsets as real world design examples Provides knowledge and guidance in the design of high speed digital circuits Explores the latest developments in system design Covers everything that encompasses a successful printed circuit board PCB product Offers insight from Intel insiders about real world high speed digital design Digital System Design with SystemVerilog Mark Zwolinski, 2009-10-23

The Definitive Up to Date Guide to Digital Design with SystemVerilog Concepts Techniques and Code To design state of the art digital hardware engineers first specify functionality in a high level Hardware Description Language HDL and today s most powerful useful HDL is SystemVerilog now an IEEE standard Digital System Design with SystemVerilog is the first comprehensive introduction to both SystemVerilog and the contemporary digital hardware design techniques used with it Building on the proven approach of his bestselling Digital System Design with VHDL Mark Zwolinski covers everything engineers need to know to automate the entire design process with SystemVerilog from modeling through functional simulation synthesis timing simulation and verification Zwolinski teaches through about a hundred and fifty practical examples each with carefully detailed syntax and enough in depth information to enable rapid hardware design and verification All examples are available for download from the book s companion Web site zwolinski.org Coverage includes Using electronic design automation tools with programmable logic and ASIC technologies Essential principles of Boolean algebra and combinational logic design with discussions of timing and hazards Core modeling techniques combinational building blocks buffers decoders encoders multiplexers adders and parity checkers Sequential building blocks latches flip flops registers counters memory and sequential multipliers Designing finite state machines from ASM chart to D flip flops next state and output logic Modeling interfaces and packages with SystemVerilog Designing testbenches architecture constrained random test generation and assertion based verification Describing RTL and FPGA synthesis models Understanding and implementing Design for Test Exploring anomalous behavior in asynchronous sequential circuits Performing Verilog AMS and mixed signal modeling Whatever your experience with digital design older versions of Verilog or VHDL this book will help you discover SystemVerilog s full power and use it to the fullest

Digital Design and Fabrication Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains

a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

Digital Communications Test and Measurement Dennis Derickson, Marcus Müller, 2007-12-10 A Comprehensive Guide to Physical Layer Test and Measurement of Digital Communication Links Today s new data communication and computer interconnection systems run at unprecedented speeds presenting new challenges not only in the design but also in troubleshooting test and measurement This book assembles contributions from practitioners at top test and measurement companies component manufacturers and universities It brings together information that has never been broadly accessible before information that was previously buried in application notes seminar and conference presentations short courses and unpublished works Readers will gain a thorough understanding of the inner workings of digital high speed systems and learn how the different aspects of such systems can be tested The editors and contributors cover key areas in test and measurement of transmitters digital waveform and jitter analysis and bit error ratio receivers sensitivity jitter tolerance and PLL CDR characterization and high speed channel characterization in time and frequency domain Extensive illustrations are provided throughout Coverage includes Signal integrity from a measurement point of view Digital waveform analysis using high bandwidth real time and sampling equivalent time oscilloscopes Bit error ratio measurements for both electrical and optical links Extensive coverage on the topic of jitter in high speed networks State of the art optical sampling techniques for analysis of 100 Gbit/s signals Receiver characterization clock recovery phase locked loops jitter tolerance and transfer functions sensitivity testing and stressed waveform receiver testing Channel and system characterization TDR T and frequency domain based alternatives Testing and measuring PC architecture communication links PCIexpress SATA and FB DIMM

High-Speed System and Analog Input/Output Design Thanh T. Tran, 2022-08-17 The new edition of this textbook is based on Dr Thanh T Tran s 10 years experience teaching high speed digital and analog design courses at Rice University and 30 years experience working in high speed system design including signal and power integrity in digital signal processing DSP computer and embedded system The book provides hands on practical instruction on high speed digital and analog design for students and working engineers The author first presents good high speed digital and analog design practices that minimize both component and system noise and ensure system design success He then presents guidelines to be used throughout the design process to reduce noise and radiation and to avoid common pitfalls while improving quality and reliability The book is filled with tips on design and system simulation that minimize late stage redesign costs and product shipment delays Hands on design examples focusing on audio video analog filters DDR memory and power supplies are featured throughout In addition the author provides a practical approach to design multi gigahertz high speed serial

busses USB C PCIe HDMI DP and simulate printed circuit board insertion and return loss using s parameter models

Timing Analysis and Simulation for Signal Integrity Engineers Greg Edlund, 2007-10-22 Every day companies call upon their signal integrity engineers to make difficult decisions about design constraints and timing margins Can I move these wires closer together How many holes can I drill in this net How far apart can I place these chips Each design is unique there s no single recipe that answers all the questions Today s designs require ever greater precision but design guides for specific digital interfaces are by nature conservative Now for the first time there s a complete guide to timing analysis and simulation that will help you manage the tradeoffs between signal integrity performance and cost Writing from the perspective of a practicing SI engineer and team lead Greg Edlund of IBM presents deep knowledge and quantitative techniques for making better decisions about digital interface design Edlund shares his insights into how and why digital interfaces fail revealing how fundamental sources of pathological effects can combine to create fault conditions You won t just learn Edlund s expert techniques for avoiding failures you ll learn how to develop the right approach for your own projects and environment Coverage includes Systematically ensure that interfaces will operate with positive timing margin over the product s lifetime without incurring excess cost Understand essential chip to chip timing concepts in the context of signal integrity Collect the right information upfront so you can analyze new designs more effectively Review the circuits that store information in CMOS state machines and how they fail Learn how to time common clock source synchronous and high speed serial transfers Thoroughly understand how interconnect electrical characteristics affect timing propagation delay impedance profile crosstalk resonances and frequency dependent loss Model 3D discontinuities using electromagnetic field solvers Walk through four case studies coupled differential vias land grid array connector DDR2 memory data transfer and PCI Express channel Appendices present a refresher on SPICE modeling and a high level conceptual framework for electromagnetic field behavior Objective realistic and practical this is the signal integrity resource engineers have been searching for Preface xiii Acknowledgments xvi About the Author xix About the Cover xx Chapter 1 Engineering Reliable Digital Interfaces 1 Chapter 2 Chip to Chip Timing 13 Chapter 3 Inside IO Circuits 39 Chapter 4 Modeling 3D Discontinuities 73 Chapter 5 Practical 3D Examples 101 Chapter 6 DDR2 Case Study 133 Chapter 7 PCI Express Case Study 175 Appendix A A Short CMOS and SPICE Primer 209 Appendix B A Stroll Through 3D Fields 219 Endnotes 233 Index 235 Semiconductor Modeling: Roy Leventhal, Lynne Green, 2008-11-01 Discusses process variation model accuracy design flow and many other practical engineering reliability and manufacturing issues Gives a good overview for a person who is not an expert in modeling and simulation enabling them to extract the necessary information to competently use modeling and simulation programs Written for engineering students and product design engineers **Digital Design and Fabrication** Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as

well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user s point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

Digital System Design with Systemverilog (Paperback) Mark Zwolinski, 2016-08-29 The Definitive Up to Date Guide to Digital Design with SystemVerilog Concepts Techniques and Code To design state of the art digital hardware engineers first specify functionality in a high level Hardware Description Language HDL and today s most powerful useful HDL is SystemVerilog now an IEEE standard Digital System Design with SystemVerilog is the first comprehensive introduction to both SystemVerilog and the contemporary digital hardware design techniques used with it Building on the proven approach of his bestselling Digital System Design with VHDL Mark Zwolinski covers everything engineers need to know to automate the entire design process with SystemVerilog from modeling through functional simulation synthesis timing simulation and verification Zwolinski teaches through about a hundred and fifty practical examples each with carefully detailed syntax and enough in depth information to enable rapid hardware design and verification All examples are available for download from the book s companion Web site zwolinski.org Coverage includes Using electronic design automation tools with programmable logic and ASIC technologies Essential principles of Boolean algebra and combinational logic design with discussions of timing and hazards Core modeling techniques combinational building blocks buffers decoders encoders multiplexers adders and parity checkers Sequential building blocks latches flip flops registers counters memory and sequential multipliers Designing finite state machines from ASM chart to D flip flops next state and output logic Modeling interfaces and packages with SystemVerilog Designing testbenches architecture constrained random test generation and assertion based verification Describing RTL and FPGA synthesis models Understanding and implementing Design for Test Exploring anomalous behavior in asynchronous sequential circuits Performing Verilog AMS and mixed signal modeling Whatever your experience with digital design older versions of Verilog or VHDL this book will help you discover SystemVerilog s full power and use it to the fullest

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