

High Frequency Amplifier Transistor 20v 50ma 1 5ghz

David Willis Howell, Stanford University Stanford Electronics Laboratories

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73 Magazine for Radio Amateurs ,1979 The Wireless World ,1975 Transistor ,1988 Electronics World ,1999 Electronics World + Wireless World ,1991 Wireless World ,1975 Ham Radio Magazine ,1978-07

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linear power amplification of signals containing more than one frequency Linear power amplifier design is contingent upon having a suitable design technique Suitable often means being able to determine parameter values called for by that technique Conjugate impedance matching is a suitable technique and three of the four parameter values can be accurately determined In some cases manufacturers provide data for this technique titled Large signal parameters Author A Transistor Amplifier with 500-Mc Bandwidth David Willis Howell, Stanford University Stanford Electronics Laboratories, 1964. The design construction and evaluation of a lowpass transistor amplifier having 50 db insertion power gain and 500 Mc bandwidth are discussed The amplifier useful for i f rf or fast pulse amplification consists of seven common emitter stages each having collectorto base feedback A generalized design procedure is given which should be useful for the con struction of other lowpass amplifiers having bandwidths of 500 Mc or greater Author Optimum Two-terminal Interstage Design for High-frequency Transistor Amplifier WALTER H. KU, HERBERT J. CARLIN, POLYTECHNIC INST OF BROOKLYN NY MICROWAVE RESEARCH INST., 1959 High Frequency Transistor Power Amplifiers Stephen Amram Slenker, 1957

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