

1. Draw a circuit to realize the function: $F = ABC + A'BC + AB'C + ABC'$ (Use Boolean algebra, do not use K-map or Q-M method)

- Using one OR gate and three AND gates. The AND gates should have two inputs.
- Using two OR gates and two AND gates. All of the gates should have two inputs.

2. Realize $a'b'c' + a'bc + ab'c + abc'$ using only two-input equivalence [exclusive NOR] gates.

3. Design a circuit which will perform the following function of three 4-bit numbers:

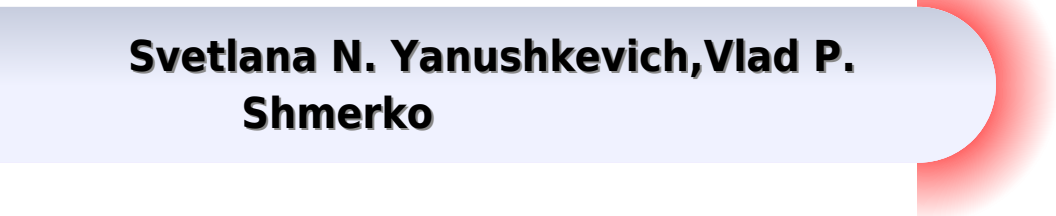
$$(X_3X_2X_1X_0 + Y_3Y_2Y_1Y_0) - Z_3Z_2Z_1Z_0$$

Use eight full adders and any other type of gates. Assume that negative number are represented in 2's complement.

4. Given $F_1 = \prod M(0, 4, 5, 6)$ and $F_2 = \prod M(0, 4, 7)$, find the maxterm expansion for $F_1 + F_2$. State a general rule for finding the maxterm expansion of $F_1 + F_2$, given the maxterm expansion of F_1 and F_2 . Prove your answer by using the general form of the maxterm expansion.

Digital Logic Design Problems And Solutions

**Svetlana N. Yanushkevich, Vlad P.
Shmerko**



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Rather they provide complementary methods and tools for designing software intensive systems with a human centric approach Bringing together prominent experts and practitioners to share their insights approaches and experiences the book sheds new light on the specific interpretations and meanings of design thinking in various fields such as engineering management and information technology As such it provides a framework for professionals to demonstrate the potential of design thinking for software development while offering academic researchers a roadmap for further research **The Art of Systems Architecting** Eberhardt Rechtin, Mark W. Maier, 2010-12-12 Today s architecting must handle systems of types unknown until very recently New domains including personal computers intersatellite networks health services and joint service command and control are calling for new architectures and for architects specializing in those domains Since the original publication of this bestselling text these Digital Logic Design MCQ (Multiple Choice Questions) Arshad Iqbal, 2019-06-11 The Digital Logic Design Multiple Choice Questions MCQ Quiz with Answers PDF Logic Design MCQ PDF Download Quiz Questions Chapter 1 12 Practice Tests with Answer Key Digital Logic Questions Bank MCQs Notes includes revision guide for problem solving with hundreds of solved MCQs Digital Logic Design MCQ with Answers PDF book covers basic concepts analytical and practical assessment tests Digital Logic Design MCQ PDF book helps to practice test questions from exam prep notes The Digital Logic Design MCQs with Answers PDF eBook includes revision guide with verbal quantitative and analytical past papers solved MCQs Digital Logic Design Multiple Choice Questions and Answers MCQs PDF Free download chapter 1 a book covers solved quiz questions and answers on chapters Algorithmic state machine asynchronous sequential logic binary systems Boolean algebra and logic gates combinational logics digital integrated circuits DLD experiments MSI and PLD components registers counters and memory units simplification of Boolean functions standard graphic symbols synchronous sequential logics tests for college and university revision guide Digital Logic Design Quiz Questions and Answers PDF free download eBook s sample covers beginner s solved questions textbook s study notes to practice online tests The book Digital Logic Design MCQs Chapter 1 12 PDF includes high school question papers to review practice tests for exams Digital Logic Design Multiple Choice Questions MCQ with Answers PDF digital edition eBook a study guide with textbook chapters tests for NEET Jobs Entry Level competitive exam Digital Logic Design Mock Tests Chapter 1 12 eBook covers problem solving exam tests from computer science textbook and practical eBook chapter wise as Chapter 1 Algorithmic State Machine MCQ Chapter 2 Asynchronous Sequential Logic MCQ Chapter 3 Binary Systems MCQ Chapter 4 Boolean Algebra and Logic Gates MCQ Chapter 5 Combinational Logics MCQ Chapter 6 Digital Integrated Circuits MCQ Chapter 7 DLD Experiments MCQ Chapter 8 MSI and PLD Components MCQ Chapter 9 Registers Counters and Memory Units MCQ Chapter 10 Simplification of Boolean Functions MCQ Chapter 11 Standard Graphic Symbols MCQ Chapter 12 Synchronous Sequential Logics MCQ The Algorithmic State Machine MCQ PDF e Book Chapter 1 practice test to solve MCQ questions on Introduction to algorithmic state machine algorithmic state machine chart ASM chart control implementation in

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