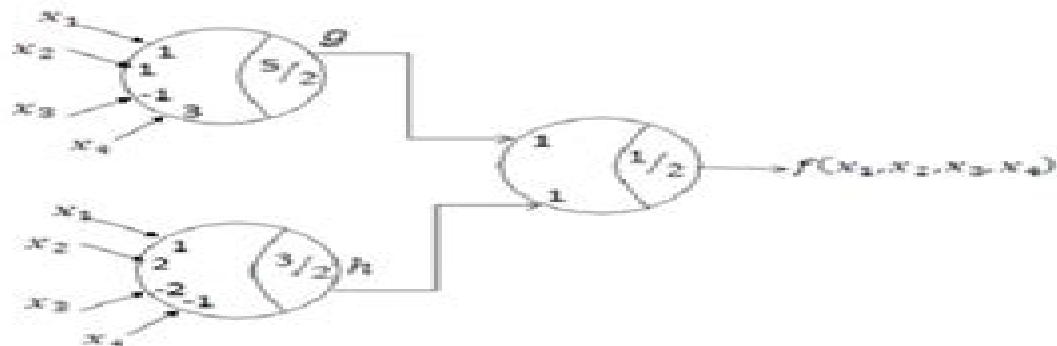


DIGITAL SWITCHING AND LOGIC DESIGN

QUESTION BANK

CHAPTER1: Threshold Logic

- Q1. Is it possible to design EX-OR gate with a single threshold element? Justify your answer.
- Q2. Let $f(x_1, x_2, x_3) = \sum(0,1,3)$. Realize it using a threshold element.
- Q3. What are the various capabilities of threshold logic?
- Q5. Determine whether the function $f(x_1, x_2, x_3, x_4) = \sum(0,1,3,4,5,6,7,12,13)$ is a threshold function, and, if it is, find a weight-threshold vector.
- Q6. By examining the linear inequalities, determine which of the following functions is a threshold function, and for each one that is, find the corresponding weight-threshold vector.
- a) $f_1(x_1, x_2, x_3) = \sum(1,2,3,7)$
 - b) $f_1(x_1, x_2, x_3) = \sum(0,2,4,5,6)$
 - c) $f_1(x_1, x_2, x_3) = \sum(0,3,5,6)$
- Q7. Determine the function $f(x_1, x_2, x_3, x_4)$ realized by the network as shown in fig below.



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Parag K. Lala



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begins with an overview of the essential laws of Boolean algebra K map plotting techniques as well as the simplification of Boolean functions This text then presents the properties and develops the characteristic equations of a number of various types of flip flop Other chapters consider the design of synchronous and asynchronous counters using either discrete flip flops or shift registers This book discusses as well the design and implementation of event driven logic circuits using the NAND sequential equation The final chapter deals with simple coding techniques and the principles of error detection and correction This book is a valuable resource for undergraduate students digital engineers and scientists **A TEXTBOOK OF DIGITAL LOGIC CIRCUITS (With Excellent Question Bank for University & Competitive Exams)** Na Vikraman,2020-06-07

This book has been written for the All year of BE B Tech students of All University with latest syllabus for ECE Digital Electronics EEE Digital Logic Circuits CSE IT Digital Principles and System Design The basic aim of this book is to provide a basic knowledge in Digital Electronics Digital Logic Circuits Digital Principles and System Design for engineering students of degree diploma AMIE courses and a useful reference for these preparing for competitive examinations All the concepts are explained in a simple clear and complete manner to achieve progressive learning Two marks questions and answers Short Long answer questions are provided at the end of fifth chapter This book is divided into five chapters Each chapter is well supported with the necessary illustration practical examples and solved problems **Digital Logic Techniques** John Stonham,2017-11-22

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Foundations of Digital Logic Design Gideon Langholz,Abraham Kandel,Joe L. Mott,1998 *Fundamentals and Applications of Digital Logic Circuits* Sol Libes,1975 DIGITAL LOGIC AND COMPUTER DESIGN ,2001 **Applied Digital Logic Exercises Using FPGAs** Kurt Wick,2017 Applied Digital Logic Exercises Using FPGAs is appropriate for anyone interested in digital logic who needs to learn how to implement it through detailed exercises with state of the art digital design tools and components The book exposes readers to combinational and sequential digital logic concepts and implements them with hands on exercises using the Verilog Hardware Description Language HDL and a Field Programmable Gate Arrays FPGA teaching board

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