

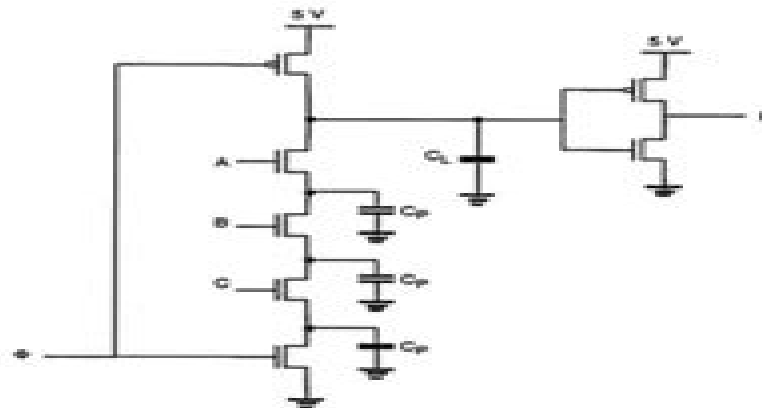


Figure P9.6

SOLUTION:

The total charge at node C_L before charge sharing is

$$Q_L = V_{in} \cdot C_L = 5C_L$$

After charge sharing, suppose the node voltage is V_{final} , then

$$Q_L = 5C_L = V_{final} \cdot C_L + V_{final} \cdot C_P$$

To prevent the inverter from switching,

$$V_{final} = \frac{5C_L}{C_L + C_P} > 3$$

The maximum ratio is:

$$\frac{C_P}{C_L} < \frac{2}{3}$$

9.7 Consider the following CMOS logic circuit which is a simple domino circuit. Node X is connected to a CMOS inverter so that the output of the inverter can be directly fed to the next stage of the domino circuit.

- (a) Explain how the voltage level at node X, after it is precharged to 5 V, can be affected by the charge sharing between node X and node Y if their node capacitances are the same. Express the final voltage at node X in terms of the initial voltage at node Y when the charge sharing is completed following the full precharge operation when the gate terminal of M2 transistor is fixed at 0 V.

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Kang



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Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits David A. Hodges, Yu Chen, Horace G. Jackson, 1983 *Solutions Manual for Digital Integrated Circuits* Ayers John E, 2003-09 Digital Integrated Circuits DeMassa, 1996-02-01 **Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition** Sung-Mo Kang, Yusuf Leblebici, 1999 *Solutions Manual Digital Integrated Circuits* CRC Press, 2009-07-10

Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field. Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization. Each chapter includes numerous worked examples, case studies, and SPICE computer simulations. The book's website offers supplementary material and more worked problems. Qualifying instructors will have access to a new instructor's manual.

Solutions Manual Kang, 1995-08-01 **Electronic Devices and Circuit Fundamentals, Solution Manual** Dale R. Patrick, Stephen W. Fardo, Ray E. Richardson, Vigyan (Vigs) Chandra, 2023-05-26

Devices and Circuit Fundamentals is Chapter Outline, Learning Objectives, Key Terms, Figure List, Chapter Summary, Formulas, Answers to Examples, Self Exams, Glossary of Terms defined.

Solutions Manual to Accompany Digital Concepts Using Standard Integrated Circuits Richard S. Sandige, 1978 Embedded SoPC Design with Nios II Processor and VHDL Examples Pong P. Chu, 2011-08-29

The book is divided into four major parts. Part I covers HDL constructs and synthesis of basic digital circuits. Part II provides an overview of embedded software development with the emphasis on low-level I/O access and drivers. Part III demonstrates the design and development of hardware and software for several complex I/O peripherals including PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD secure digital card. Part IV provides three case studies of the integration of hardware accelerators including a custom GCD, greatest common divisor circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology. The book utilizes FPGA devices, Nios II soft-core processor, and development platform from Altera Co., which is one of the two main FPGA manufacturers. Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions (details at www.altera.com/university). The two main educational prototyping boards are known as DE1-99 and DE2-269. All experiments can be implemented and tested with these boards. A board combined with this book becomes a turn-key solution for the SoPC design experiments and projects. Most HDL and C codes in the book are device-independent and can be adapted by other prototyping boards as long as a board has similar I/O configuration.

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-05-14

Explores the unique hardware programmability of FPGA-based embedded systems using a learn-by-doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC system on a

programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Engineering Education ,1989 Introduction to VLSI Systems Ming-Bo Lin,2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools Introduction to VLSI Systems A Logic Circuit and System Perspective addresses the need for teaching such a topic in terms of a logic circuit and system design perspective To achieve the above mentioned goals this classroom tested book focuses on Implementing a digital system as a full custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal oxide semiconductor CMOS VLSI Important issues of modern CMOS processes including deep submicron devices circuit optimization interconnect modeling and optimization signal integrity power integrity clocking and timing power dissipation and electrostatic discharge ESD Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up paying much attention to logic circuit layout and system designs Armed with these tools readers can not only comprehensively understand the features and limitations of modern VLSI technologies but also have enough background to adapt to this ever changing field

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