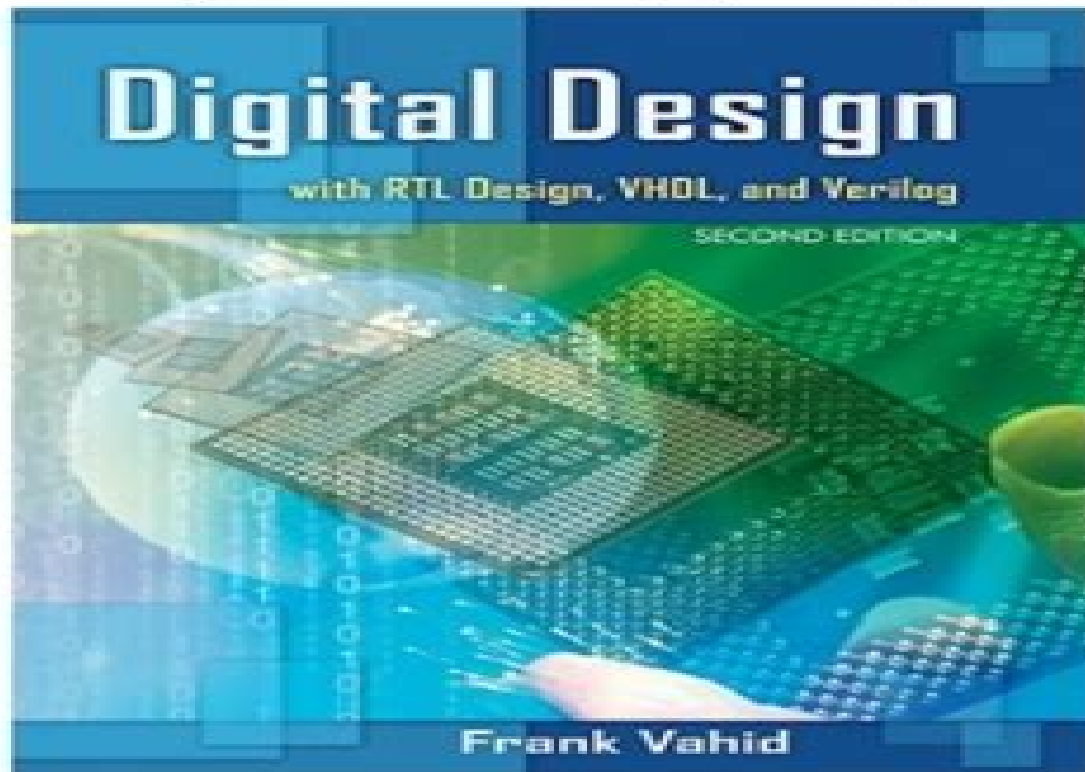




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Digital Logic Design Using Verilog Vaibbhav Taraate,2016-05-17 This book is designed to serve as a hands on professional reference with additional utility as a textbook for upper undergraduate and some graduate courses in digital logic design This book is organized in such a way that that it can describe a number of RTL design scenarios from simple to complex The book constructs the logic design story from the fundamentals of logic design to advanced RTL design concepts Keeping in view the importance of miniaturization today the book gives practical information on the issues with ASIC RTL design and how to overcome these concerns It clearly explains how to write an efficient RTL code and how to improve design performance The book also describes advanced RTL design concepts such as low power design multiple clock domain design and SOC based design The practical orientation of the book makes it ideal for training programs for practicing design engineers and for short term vocational programs The contents of the book will also make it a useful read for students and hobbyists

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Principles of Verilog Digital Design Wen-Long Chin, 2022-02-27 Covering both the fundamentals and the in depth topics related to Verilog digital design both students and experts can benefit from reading this book by gaining a comprehensive understanding of how modern electronic products are designed and implemented Principles of Verilog Digital Design contains many hands on examples accompanied by RTL codes that together can bring a beginner into the digital design realm without needing too much background in the subject area This book has a particular focus on how to transform design concepts into physical implementations using architecture and timing diagrams Common mistakes a beginner or even an experienced engineer can make are summarized and addressed as well Beyond the legal details of Verilog codes the book additionally presents what uses Verilog codes have through some pertinent design principles Moreover students reading this book will gain knowledge about system level design concepts Several ASIC designs are illustrated in detail as well In addition to design principles and skills modern design methodology and how it is carried out in practice today are explored in depth as well

PLD Based Design with VHDL Vaibbhav Taraate, 2017-01-13 This book covers basic fundamentals of logic design and advanced RTL design concepts using VHDL The book is organized to describe both simple and complex RTL design scenarios using VHDL It gives practical information on the issues in ASIC prototyping using FPGAs design challenges and how to overcome practical issues and concerns It describes how to write an efficient RTL code using VHDL and how to improve the design performance The design guidelines by using VHDL are also explained with the practical examples in this book The book also covers the ALTERA and XILINX FPGA architecture and the design flow for the PLDs The contents of this book will be useful to students researchers and professionals working in hardware design and optimization The book can also be used as a text for graduate and professional development courses

Digital Design and Verilog HDL Fundamentals Joseph Cavanagh, 2017-12-19 Comprehensive and self contained this tutorial covers the design of a plethora of combinational and sequential logic circuits using conventional logic design and Verilog HDL Number systems and number representations

are presented along with various binary codes Several advanced topics are covered including functional decomposition and iterative networks A variety of examples are provided for combinational and sequential logic computer arithmetic and advanced topics such as Hamming code error correction Constructs supported by Verilog are described in detail All designs are continued to completion Each chapter includes numerous design issues of varying complexity to be resolved by the reader

Digital Design M. Morris R. Mano, Michael D. Ciletti, 2017-02-27 For introductory courses on digital design in an Electrical Engineering Computer Engineering or Computer Science department A clear and accessible approach to the basic tools concepts and applications of digital design A modern update to a classic authoritative text Digital Design 5th Edition teaches the fundamental concepts of digital design in a clear accessible manner The text presents the basic tools for the design of digital circuits and provides procedures suitable for a variety of digital applications Like the previous editions this edition of Digital Design supports a multimodal approach to learning with a focus on digital design regardless of language Recognizing that three public domain languages Verilog VHDL and SystemVerilog all play a role in design flows for today's digital devices the 5th Edition offers parallel tracks of presentation of multiple languages but allows concentration on a single chosen language

RTL Hardware Design Using VHDL Pong P. Chu, 2006-04-20 The skills and guidance needed to master RTL hardware design This book teaches readers how to systematically design efficient portable and scalable Register Transfer Level RTL digital circuits using the VHDL hardware description language and synthesis software Focusing on the module level design which is composed of functional units routing circuit and storage the book illustrates the relationship between the VHDL constructs and the underlying hardware components and shows how to develop codes that faithfully reflect the module level design and can be synthesized into efficient gate level implementation Several unique features distinguish the book Coding style that shows a clear relationship between VHDL constructs and hardware components Conceptual diagrams that illustrate the realization of VHDL codes Emphasis on the code reuse Practical examples that demonstrate and reinforce design concepts procedures and techniques Two chapters on realizing sequential algorithms in hardware Two chapters on scalable and parameterized designs and coding One chapter covering the synchronization and interface between multiple clock domains Although the focus of the book is RTL synthesis it also examines the synthesis task from the perspective of the overall development process Readers learn good design practices and guidelines to ensure that an RTL design can accommodate future simulation verification and testing needs and can be easily incorporated into a larger system or reused Discussion is independent of technology and can be applied to both ASIC and FPGA devices With a balanced presentation of fundamentals and practical examples this is an excellent textbook for upper level undergraduate or graduate courses in advanced digital logic Engineers who need to make effective use of today's synthesis software and FPGA devices should also refer to this book

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SystemVerilog for Hardware Description Vaibbhav Taraate,2020-06-10 This book introduces the reader to FPGA based design for RTL synthesis It describes simple to complex RTL design scenarios using SystemVerilog The book builds the story from basic fundamentals of FPGA based designs to advance RTL design and verification concepts using SystemVerilog It provides practical information on the issues in the RTL design and verification and how to overcome these It focuses on writing efficient RTL codes using SystemVerilog covers design for the Xilinx FPGAs and also includes implementable code examples The contents of this book cover improvement of design performance assertion based verification verification planning and architecture and system testing using FPGAs The book can be used for classroom teaching or as a supplement in lab work for undergraduate and graduate coursework as well as for professional development and training programs It will also be of interest to researchers and professionals interested in the RTL design for FPGA and ASIC

Digital Design using Verilog HDL Atul P. Godse,Dr. Deepali A. Godse,2020-12-01 This book begins with an introduction to Verilog HDL It describes basic concepts in Verilog HDL language constructs and conventions and modeling styles gate level modeling data flow level modeling behavioral modeling and switch level modeling It also describes sequential models basic memory components functional register static machine coding and sequential synthesis The last section of the book focuses on component testing and verification It includes combinational circuits testing sequential circuit testing test bench techniques design verification and assertion verification

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