

Electrostatic Potential and Capacitance Class 12 Notes Chapter 2

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1. Electrostatic Potential The electrostatic potential at any point in an electric field is equal to the amount of work done per unit positive test charge or in bringing the unit positive test charge from infinite to that point, against the electrostatic force without acceleration.

$$\text{Electrostatic potential, } V = \frac{\text{Work done (W)}}{\text{Charge (q)}}$$

Its SI unit is volt (V) and $1 \text{ V} = 1 \text{ J/C}$ and its dimensional formula is $[\text{ML}^2\text{T}^{-2}\text{A}^{-1}]$. It is a scalar quantity.

NOTE: Electrostatic potential is a state dependent function as electrostatic forces are conservative forces.

2. Electrostatic Potential Difference The electrostatic potential difference between two points in an electric field is defined as the amount of work done in moving a unit positive test charge from one point to the other point against of electrostatic force without any acceleration (i.e. the difference of electrostatic potentials of the two points in the electric field).

$$V_B - V_A = \frac{W_{AB}}{q_0}$$

where, is work done in taking charge q_0 from A to B against of electrostatic force. Also, the line integral of electric field from initial position A to final position B along any path is termed as potential difference between two points in an electric field, i.e.

$$V_B - V_A = - \int_A^B \mathbf{E} \cdot d\mathbf{l}$$

NOTE: As, work done on a test charge by the electrostatic field due to any given charge configuration is independent of the path, hence potential difference is also same for any path.

For the diagram given as below, potential difference between points A and B will be same for any path.



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Per F Dahl



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ESD Protection Device and Circuit Design for Advanced CMOS Technologies Oleg Semenov, Hossein Sarbishaei, Manoj Sachdev, 2008-04-26 ESD Protection Device and Circuit Design for Advanced CMOS Technologies is intended for practicing engineers working in the areas of circuit design VLSI reliability and testing domains As the problems associated with ESD failures and yield losses become significant in the modern semiconductor industry the demand for graduates with a basic knowledge of ESD is also increasing Today there is a significant demand to educate the circuits design and reliability teams on ESD issues This book makes an attempt to address the ESD design and implementation in a systematic manner A design procedure involving device simulators as well as circuit simulator is employed to optimize device and circuit parameters for optimal ESD as well as circuit performance This methodology described in ESD Protection Device and Circuit Design for Advanced CMOS Technologies has resulted in several successful ESD circuit design with excellent silicon results and demonstrates its strengths

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