

CH. 9 – PROBLEM SOLUTIONS

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9.1

(a)

(b)

(c)

$V_E > 0 \Rightarrow Q_O = \text{OFF} \Rightarrow V_O = 2.5\text{V}$
 $\Rightarrow R_2 = R_3$. Let $R_2 = R_3 = 12\text{k}\Omega$.
 $V_E < 0 \Rightarrow Q_O = \text{SAT} \Rightarrow V_O = -2\text{V}$.
 kcal: $\frac{5 - (-2.5)}{12} + \frac{0 - (-2.5)}{12} = \frac{-2.5 - (-5)}{R_1}$
 $\Rightarrow R_1 = 5.0\text{ k}\Omega$.

9.2

Since the voltage developed by the thermistor decreases with T , it must be applied to the inverting input. $R(100^\circ\text{C}) = 10\text{ k}\Omega \times \exp[4000(1/373.2 - 1/298.2)] = 6.75\text{ k}\Omega$. Make all bridge resistors nominally $6.75\text{ k}\Omega$. To allow exact calibration, let $R_4 = 1\text{ k}\Omega$ pot and $R_2 = R_3 = 6.75 - 0.5 = 6.25\text{ k}\Omega$. The closest 1% standard values are $R_1 = 6.81\text{ k}\Omega$ and $R_2 = R_3 = 6.19\text{ k}\Omega$. Moreover, let $R_5 = 3.3\text{ k}\Omega$. To calibrate, place the thermistor in boiling water and adjust R_4 until V_O bounces back and forth between 0 and V_{CC} .

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JR Anderson



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Design with Operational Amplifiers and Analog Integrated Circuits Sergio Franco, 2001-08-08 Franco's Design with Operational Amplifiers and Analog Integrated Circuits 3e is intended for a design oriented course in applications with operational amplifiers and analog ICs It also serves as a comprehensive reference for practicing engineers This new edition includes enhanced pedagogy additional problems more in depth coverage of negative feedback more effective layout updated technology current feedback and folded cascode amplifiers and low voltage amplifiers and increased topical coverage current feedback amplifiers switching regulators and phase locked loops **Design With Operational Amplif** Franco, 2002-11

Analysis and Design of Analog Integrated Circuits Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2024-01-31 ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS Authoritative and comprehensive textbook on the fundamentals of analog integrated circuits with learning aids included throughout Written in an accessible style to ensure complex content can be appreciated by both students and professionals this Sixth Edition of Analysis and Design of Analog Integrated Circuits is a highly comprehensive textbook on analog design offering in depth coverage of the fundamentals of circuits in a single volume To aid in reader comprehension and retention supplementary material includes end of chapter problems plus a Solution Manual for instructors In addition to the well established concepts this Sixth Edition introduces a new super source follower circuit and its large signal behavior frequency response stability and noise properties New material also introduces replica biasing describes and analyzes two op amps with replica biasing and provides coverage of weighted zero value time constants as a method to estimate the location of dominant zeros pole zero doublets including their effect on settling time and three examples of circuits that create doublets the effect of feedback on pole zero doublets and MOS transistor noise performance including a thorough treatment on thermally induced gate noise Providing complete coverage of the subject Analysis and Design of Analog Integrated Circuits serves as a valuable reference for readers from many different types of backgrounds including senior undergraduates and first year graduate students in electrical and computer engineering along with analog integrated circuit designers *Design With Operational Amplifiers And Analog Integrated Circuits* S. Franco, *Solution Manual Operational Amplifier Circuits* David E. Johnson, V. Jayakumar, 1982

Solutions Manual for An Introduction to Digital and Analog Integrated Circuits and Applications Sanjit K. Mitra, Sanjit Kumar Mitra, 1981 **Analog Circuit Design** Johan Huijsing, Rudy J. van der Plassche, Willy M.C. Sansen, 2013-04-17 Many interesting design trends are shown by the six papers on operational amplifiers Op Amps Firstly there is the line of stand alone Op Amps using a bipolar IC technology which combines high frequency and high voltage This line is represented in papers by Bill Gross and Derek Bowers Bill Gross shows an improved high frequency compensation technique of a high quality three stage Op Amp Derek Bowers improves the gain and frequency behaviour of the stages of a two stage Op Amp Both papers also present trends in current mode feedback Op Amps Low voltage bipolar Op Amp design is presented by

leroen Fonderie He shows how multipath nested Miller compensation can be applied to turn rail to rail input and output stages into high quality low voltage Op Amps Two papers on CMOS Op Amps by Michael Steyaert and Klaas Bult show how high speed and high gain VLSI building blocks can be realised Without departing from a single stage OT A structure with a folded cascode output a thorough high frequency design technique and a gain boosting technique contributed to the high speed and the high gain achieved with these Op Amps Finally Rinaldo Castello shows us how to provide output power with CMOS buffer amplifiers The combination of class A and AB stages in a multipath nested Miller structure provides the required linearity and bandwidth

Solutions Manual Robert F. Coughlin, Frederick F. Driscoll, 1987 **Solutions Manual to Accompany "Analysis and Design of Analog Integrated Circuits"** Kuo-Chiang Hsieh, P. R. Gray, Kuang-Lu Lee, 1984

Operational Amplifiers and Linear Integrated Circuits (instructor's Solutions Manual with Transparency Masters) Robert F. Coughlin, Frederick F. Driscoll, 1991 *Analysis and Design of Analog Integrated Circuits* Paul R. Gray, Robert G. Meyer, 1992-08-24 This edition combines the consideration of metal oxide semiconductors MOS and bipolar circuits into a unified treatment that also includes MOS bipolar connections made possible by BiCMOS technology Contains extensive use of SPICE especially as an integral part of many examples in the problem sets as a more accurate check on hand calculations and as a tool to examine complex circuit behavior beyond the scope of hand analysis Concerned largely with the design of integrated circuits a considerable amount of material is also included on applications

Applications & Design with Analog Integrated Circuits J. Michael Jacob, 1982 *Operational Amplifier Speed and Accuracy Improvement* Vadim V. Ivanov, Igor M. Filanovsky, 2005-12-30 Operational Amplifier Speed and Accuracy Improvement proposes a new methodology for the design of analog integrated circuits The usefulness of this methodology is demonstrated through the design of an operational amplifier This methodology consists of the following iterative steps description of the circuit functionality at a high level of abstraction using signal flow graphs equivalent transformations and modifications of the graph to the form where all important parameters are controlled by dedicated feedback loops and implementation of the structure using a library of elementary cells Operational Amplifier Speed and Accuracy Improvement shows how to choose structures and design circuits which improve an operational amplifier's important parameters such as speed to power ratio open loop gain common mode voltage rejection ratio and power supply rejection ratio The same approach is used to design clamps and limiting circuits which improve the performance of the amplifier outside of its linear operating region such as slew rate enhancement output short circuit current limitation and input overload recovery

An Introduction to Operational Amplifiers Luces M. Faulkenberry, 1982-03-01 Solutions Manual for Analysis and Design of Analog Integrated Circuits Gray, 1977-09

ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS, 5TH ED, ISV Paul R. Gray, Hurst, Lewis, Meyer, 2009-06 Market_Desc Engineers Special Features Updates the coverage of bipolar technologies Enhances the discussion of biCMOS Provides a more unified treatment of digital and analog circuit design while

strengthening the coverage of CMOS Removes the chapter on non linear analog circuits Adds a new operational amplifier example to chapter 11 About The Book This is the only comprehensive book in the market for engineers that covers CMOS bipolar technologies and biCMOS integrated circuits The fifth edition retains its completeness updates the coverage of bipolar technologies and enhances the discussion of biCMOS It provides a more unified treatment of digital and analog circuit design while strengthening the coverage of CMOS The chapter on non linear analog circuits has been removed and chapter 11 has been updated to include an operational amplifier example With its streamlined and up to date coverage more engineers can turn to this resource to explore key concepts in the field **Operational Amplifiers & Linear Integrated**

Circuits Rajiv J. Kapadia, 2006 This book is a bold new approach to teaching about linear integrated circuits from a designer's point of view The study begins with the basics of the operational amplifier In a simple and straightforward manner it guides the student to the final equation for the analysis of the op amp circuit The book also teaches the student how to use other linear integrated circuits such as the 555 timer the phase locked loop the linear and the switching voltage regulators Key features Complete analysis of op amp circuits using ideal assumptions Each chapter includes a summary and review section These two sections will be useful to the students as well as their teachers Includes discussion about designing and practical applications of various op amp linear integrated circuits Laboratory exercises at the end of each chapter The students can complete these with minimal guidance from the instructor Includes a tutorial to PSPICE circuit analysis program and data sheets in the appendix **Bipolar and MOS Analog Integrated Circuit Design** Alan B. Grebene, 2002-11-21 A practical

engineering book discussing the most modern and general techniques for designing analog integrated circuits which are not digital excluding computer circuits Covers the basics of the devices manufacturing technology design procedures shortcuts and analytic techniques Includes examples and illustrations of the best current practice Amplifiers, Comparators, Multipliers, Filters, and Oscillators Tertulien Ndjountche, 2018-09-03 The book presents design methods for analog integrated circuits with improved electrical performance It describes different equivalent transistor models design methods and fabrication considerations for high density integrated circuits in nanometer CMOS processes and it analyzes circuit architectures that are suitable for analog building blocks Highlighting various design challenges the text offers a complete understanding of architectural and transistor level design issues of analog integrated circuits It examines important trends in the design of high speed and power efficient front end analog circuits that can be used for signal conditioning filtering and detection applications Offers a comprehensive resource for mastering the analysis of analog integrated circuits Describes circuit level details of high speed and power efficient analog building blocks Explores design methods based on various MOS transistor models MOSFET FinFET Provides mathematical derivations of all equations and formulas Emphasizes practical aspects relevant to integrated circuit implementation Includes open ended circuit design case studies **Operational Amplifiers with Linear Integrated Circuits** William D. Stanley, 1994 Through detailed explanations and mathematics

accessible to technology level readers this book establishes methods for analyzing modeling and predicting performance of op amps and linear integrated circuits KEY TOPICS It includes the common circuit configurations and devices to be used with these circuits Also includes Oscillators and waveform generators analog to digital and digital to analog conversion computer software analysis operational amplifier DC effects and limitations and more

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