

Final Exam for Electrical Signal Processing (EE-3300)

Time: 40 min (7:45–8:15) + Extension: 10 min (8:15–8:25)

1. (20 points) Let $x[n]$ be a real continuous signal.
 - (a) (5 points) Determine the discrete-time Fourier transform (DTFT) $X(e^{j\omega})$ of $x[n]$, if the signal $x[n]$ is given by $x[n] = \cos(\frac{\pi}{4}n)$.
 - (b) (5 points) Find the magnitude $|X(e^{j\omega})|$.
 - (c) (5 points) Find the phase $\angle X(e^{j\omega})$.
2. (20 points) A discrete-time system H_1 is specified by the following input-output difference equation

$$y[n] = 0.5x[n] - 0.2x[n-1] + 0.7y[n-1]$$
 - (a) (5 points) Find the frequency response $H_1(e^{j\omega})$.
 - (b) (15 points) Determine phase type of this filter by using Bode plot, magnitude and phase plot, and explain why.
3. (20 points) A discrete-time system H_2 is specified by the following input-output difference equation

$$y[n] + 0.5y[n-1] = 0.5x[n] + 0.2x[n-1]$$
 - (a) (5 points) Find the frequency response $H_2(e^{j\omega})$.
 - (b) (15 points) Determine phase type of this filter by using Bode plot, magnitude and phase plot, and explain why.
4. (20 points) A causal filter with time-invariant (LTI) system possesses the input-output relation $y[n] = \sum_{k=-\infty}^{\infty} x[k]h[n-k]$, and it is given that the input sequence $x[n]$ is $x[n] = \cos(\frac{\pi}{4}n)$ for $n \geq 0$ and 0 for $n < 0$.
 - (a) (5 points) Determine the input-output difference equation of this LTI system.
 - (b) (15 points) Determine the output sequence $y[n]$ of this LTI system.
5. (20 points)
 - (a) (5 points) Describe the operation of the Nyquist Filter. Draw condition (NFC) and reason. (NFC) condition: the signal $X(e^{j\omega})$ satisfies P_1

$$P_1: \text{The signal } X(e^{j\omega}) \text{ is } 0 \text{ for } \omega \in [\frac{\pi}{2}, \pi] \text{ and } \omega \in [-\pi, -\frac{\pi}{2}] \text{ and non-zero elsewhere.}$$
 - (b) (15 points) Let $x[n]$ be $x[n] = \cos(\frac{\pi}{4}n)$ for $n \geq 0$ and 0 for $n < 0$. Let $y[n]$ be the output sequence obtained for $n \geq 0$ by $y[n]$ which is the input sequence $x[n]$ and $y[n] = 0$ for $n < 0$. Let the input $X(e^{j\omega})$ and plot $\angle X(e^{j\omega})$. What $Y(e^{j\omega})$ for $\omega \in [-\pi, \pi]$ is?
 - (c) (10 points) Let $x[n]$ be $x[n] = \cos(\frac{\pi}{4}n)$ for $n \geq 0$ and 0 for $n < 0$. Let $y[n]$ be the output sequence obtained for $n \geq 0$ by $y[n]$ which is the input sequence $x[n]$ and $y[n] = 0$ for $n < 0$. Find the signal $y[n]$ and sketch $y[n]$ for $n \geq 0$.

Digital Signal Processing Final Exam Solutions

Richard J. Higgins



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United States Air Force Academy, 2003 *United States Air Force Academy* United States Air Force Academy, 1983

Annual Catalogue United States Air Force Academy, 1983 IEEE Digital Signal Processing Workshop , 1996

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web latch up is a failure mechanism of cmos integrated circuits characterized by excessive current drain coupled with functional failure parametric failure and or device destruction

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web although our aim is to give an understanding of latchup as it occurs in cmos switches similar principles apply to many

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web in electronics a latch up is a type of short circuit which can occur in an integrated circuit more specifically it is the inadvertent creation of a low impedance path between the power supply rails of a mosfet circuit triggering a parasitic structure which disrupts proper functioning of the part possibly even leading to

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web latch up is a condition where a low impedance path is created between a supply pin and ground this condition is caused by a trigger current injection or overvoltage but once activated the low impedance

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