[4218] - 6

F. Y. B. Sc. (Computer Science) Examination, October – 2012 ELECTRONICS

(Paper - II)

11312 - Fundamentals of Digital Electronics (2008 Pattern)

Time: Three Hours

Total Marks: 80

- Note: (i) All questions are compulsory.
 - (ii) Use of non-programmable electronic calculator is allowed
 - (iii) Draw neat diagrams wherever necessary.

Q. 1. Attempt all of the following questions:

 $[8 \times 2 = 16]$

- (a) What is parity? What is the use of parity bit?
- (b) State two de Morgan's theorems.
- (c) Define modulo of a counter. Give number of flipflops required for MOD 5 counter.
- (d) Write the formulae for output frequency and duty cycle for Astable multivibrator using IC 555.
- (e) Write truth table of 1: 2 demultiplexer.
- (f) Give the full form of EPROM and EEPROM.
- (g) For 4 bit R-2R type DAC with binary input 0101, find output voltage if $V_{ref} = 5V$.
- (h) What is the use of Assembler?

Digital Electronics Question Paper With Answer

Sabine Zange

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