

DIGITAL INTEGRATED CIRCUITS

A DESIGN PERSPECTIVE

SECOND EDITION



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Digital Integrated Circuits Jan M. Rabaey, 2003 Contents p pChapter 1 Introductionp pChapter 2 The Manufacturing Processp pChapter 3 The Devicesp pChapter 4 The Wirep pChapter 5 The CMOS Inverterp pChapter 6 Designing Combinational Logic Gates in CMOS **Embedded Systems** James K. Peckol, 2019-06-10 Embedded Systems A Contemporary Design Tool Second Edition Embedded systems are one of the foundational elements of today's evolving and growing computer technology From operating our cars managing our smart phones cleaning our homes or cooking our meals the special computers we call embedded systems are quietly and unobtrusively making our lives easier safer and more connected While working in increasingly challenging environments embedded systems give us the ability to put increasing amounts of capability into ever smaller and more powerful devices Embedded Systems A Contemporary Design Tool Second Edition introduces you to the theoretical hardware and software foundations of these systems and expands into the areas of signal integrity system security low power and hardware software co design The text builds upon earlier material to show you how to apply reliable robust solutions to a wide range of applications operating in today's often challenging environments Taking the user's problem and needs as your starting point you will explore each of the key theoretical and practical issues to consider when designing an application in today's world Author James Peckol walks you through the formal hardware and software development process covering Breaking the problem down into major functional blocks Planning the digital and software architecture of the system Utilizing the hardware and software co design process Designing the physical world interface to external analog and digital signals Addressing security issues as an integral part of the design process Managing signal integrity problems and reducing power demands in contemporary systems Debugging and testing throughout the design and development cycle Improving performance Stressing the importance of security safety and reliability in the design and development of embedded systems and providing a balanced treatment of both the hardware and the software aspects Embedded Systems A Contemporary Design Tool Second Edition gives you the tools for creating embedded designs that solve contemporary real world challenges Visit the book's website at <http://bcs.wiley.com> he bcs Books action index bcsId 11853 itemId 1119457505 *Dual Mode Logic* Itamar Levi, Alexander Fish, 2020-12-15 This book presents Dual Mode Logic DML a new design paradigm for digital integrated circuits DML logic gates can operate in two modes each optimized for a different metric Its on the fly switching between these operational modes at the gate block and system levels provide maximal E D optimization flexibility Each highly detailed chapter has multiple illustrations showing how the DML paradigm seamlessly implements digital circuits that dissipate less energy while simultaneously improving performance and reducing area without a significant compromise in reliability All the facets of the DML methodology are covered starting from basic concepts through single gate optimization general module optimization design trade offs and new ways DML can be integrated into standard design flows using standard EDA tools DML logic is compatible with numerous applications but is

particularly advantageous for ultra low power reliable high performance systems and advanced scaled technologies Written in language accessible to students and design engineers each topic is oriented toward immediate application by all those interested in an alternative to CMOS logic Describes a novel promising alternative to conventional CMOS logic known as Dual Mode Logic DML with which a single gate can be operated selectively in two modes each optimized for a different metric e g energy consumption performance size Demonstrates several techniques at the architectural level which can result in high energy savings and improved system performance Focuses on the tradeoffs between power area and speed including optimizations at the transistor and gate level including alternatives to DML basic cells Illustrates DML efficiency for a variety of VLSI applications

Introduction to VLSI Systems Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding

Low Power Networks-on-Chip Cristina Silvano, Marcello Lajolo, Gianluca Palermo, 2010-09-24 In recent years both Networks on Chip as an architectural solution for high speed interconnect and power consumption as a key design constraint have continued to gain interest in the design and research communities This book offers a single source reference to some of the most important design techniques proposed in the context of low power design for networks on chip architectures

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Integrated Circuit and System Design , 2004 Intelligent Computing Techniques for Smart Energy Systems Akhtar Kalam, Khaleequr Rehman Niazi, Amit Soni, Shahbaz Ahmed Siddiqui, Ankit Mundra, 2019-12-16 The book compiles the research works related to smart solutions concept in context to smart energy systems maintaining electrical grid discipline and resiliency computational collective intelligence consisted of interaction between smart devices smart environments and smart interactions as well as information technology support for such areas It includes high quality papers presented in the International Conference on Intelligent Computing Techniques for Smart Energy Systems organized by Manipal University Jaipur This book will motivate scholars to work in these areas The book also prophesies their approach to be used for the business and the humanitarian technology development as research proposal to various government organizations for funding approval

American Book Publishing Record , 2004 **2003 IEEE Conference on Electron Devices and Solid-State Circuits** , 2003 The proceedings from the 2003 IEEE Conference on Electron Devices and Solid State Circuits

FPGA 2008 , 2008 **Analysis of Pipelined FIR Structures for Adaptive Equalization of a Disk Drive EPR Read Channel** David J. Gilhooly, 2001 **Proceedings** , 2003 1999 IEEE International Conference on Acoustics, Speech, and Signal Processing , 1999 **Proceedings** ICASSP (24, 1999, Phoenix, Ariz.), 1999 Design of High-Performance Microprocessor Circuits Anantha Chandrakasan, 2001 The authors present readers with a compelling one stop advanced system perspective on the intrinsic issues of digital system design This invaluable reference prepares readers to meet the

emerging challenges of the device and circuit issues associated with deep submicron technology It incorporates future trends with practical contemporary methodologies **Index to IEEE Publications** Institute of Electrical and Electronics Engineers,1998 Issues for 1973 cover the entire IEEE technical literature **Indian National Bibliography** B. S. Kesavan,2003 **IEEE International Conference on Electronics, Circuits and Systems** ,2002 **Synthesis Methodology for Built-in At-speed Testing** Yinghua Li,2005

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