

Digital Logic Design Principles



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John Y. Cheung, Jon G. Bredeson



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Digital Logic Design Principles Norman Balabanian, Bradley Carlson, 2001 **Fundamentals of Digital Logic and Microcomputer Design** M. Rafiquzzaman, 2005-06-06 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor s manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6 11 8086 and 68asmsim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems **Digital Design and Computer Organisation** D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles **Digital Principles and Design** Donald D. Givone, 2003 Digital Integrated Circuit Design Hubert Kaeslin, 2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more **Digital Logic Design and Computer Organization with Computer Architecture for Security** Nikrouz Faroughi, 2014-09-08 A COMPREHENSIVE GUIDE TO THE DESIGN ORGANIZATION OF MODERN COMPUTING SYSTEMS Digital Logic Design and Computer Organization with Computer Architecture for Security provides practicing engineers and students with a clear

understanding of computer hardware technologies The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed The book covers computer organization and architecture modern design concepts and computer security through hardware Techniques for designing both small and large combinational and sequential circuits are thoroughly explained This detailed reference addresses memory technologies CPU design and techniques to increase performance microcomputer architecture including plug and play device interface and memory hierarchy A chapter on security engineering methodology as it applies to computer architecture concludes the book Sample problems design examples and detailed diagrams are provided throughout this practical resource

COVERAGE INCLUDES

Combinational circuits small designs Combinational circuits large designs Sequential circuits core modules Sequential circuits small designs Sequential circuits large designs Memory Instruction set architecture Computer architecture interconnection Memory system Computer architecture security

Principles of Testing Electronic Systems Samiha Mourad,Yervant Zorian,2000-07-25 A pragmatic approach to testing electronic systems As we move ahead in the electronic age rapid changes in technology pose an ever increasing number of challenges in testing electronic products Many practicing engineers are involved in this arena but few have a chance to study the field in a systematic way learning takes place on the job By covering the fundamental disciplines in detail *Principles of Testing Electronic Systems* provides design engineers with the much needed knowledge base Divided into five major parts this highly useful reference relates design and tests to the development of reliable electronic products shows the main vehicles for design verification examines designs that facilitate testing and investigates how testing is applied to random logic memories FPGAs and microprocessors Finally the last part offers coverage of advanced test solutions for today s very deep submicron designs The authors take a phenomenological approach to the subject matter while providing readers with plenty of opportunities to explore the foundation in detail Special features include An explanation of where a test belongs in the design flow Detailed discussion of scan path and ordering of scan chains BIST solutions for embedded logic and memory blocks Test methodologies for FPGAs A chapter on testing system on a chip Numerous references

VLSI Test Principles and Architectures Laung-Terng Wang,Cheng-Wen Wu,Xiaoqing Wen,2006-08-14 This book is a comprehensive guide to new DFT methods that will show the readers how to design a testable and quality product drive down test cost improve product quality and yield and speed up time to market and time to volume Most up to date coverage of design for testability Coverage of industry practices commonly found in commercial DFT tools but not discussed in other books Numerous practical examples in each chapter illustrating basic VLSI test principles and DFT architectures

VLSI Testing Stanley Leonard Hurst,1998 Hurst an editor at the *Microelectronics Journal* analyzes common problems that electronics engineers and circuit designers encounter while testing integrated circuits and the systems in which they are used and explains a variety of solutions available for overcoming them in both digital and mixed circuits Among his topics are faults in digital circuits generating a digital test pattern signatures and self

tests structured design for testability testing structured digital circuits and microprocessors and financial aspects of testing The self contained reference is also suitable as a textbook in a formal course on the subject Annotation copyrighted by Book News Inc Portland OR *The Digital Logic Handbook, 1966-67 Edition*, 1966 Electronic Design Automation Laung-Terng Wang, Yao-Wen Chang, Kwang-Ting (Tim) Cheng, 2009-03-11 This book provides broad and comprehensive coverage of the entire EDA flow EDA VLSI practitioners and researchers in need of fluency in an adjacent field will find this an invaluable reference to the basic EDA concepts principles data structures algorithms and architectures for the design verification and test of VLSI circuits Anyone who needs to learn the concepts principles data structures algorithms and architectures of the EDA flow will benefit from this book Covers complete spectrum of the EDA flow from ESL design modeling to logic test synthesis verification physical design and test helps EDA newcomers to get up and running quickly Includes comprehensive coverage of EDA concepts principles data structures algorithms and architectures helps all readers improve their VLSI design competence Contains latest advancements not yet available in other books including Test compression ESL design modeling large scale floorplanning placement routing synthesis of clock and power ground networks helps readers to design develop testable chips or products Includes industry best practices wherever appropriate in most chapters helps readers avoid costly mistakes The Art of Digital Design Franklin P. Prosser, David E. Winkel, 1987

Hardware Logic Design **Introduction to VLSI Systems** Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools Introduction to VLSI Systems A Logic Circuit and System Perspective addresses the need for teaching such a topic in terms of a logic circuit and system design perspective To achieve the above mentioned goals this classroom tested book focuses on Implementing a digital system as a full custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal oxide semiconductor CMOS VLSI Important issues of modern CMOS processes including deep submicron devices circuit optimization interconnect modeling and optimization signal integrity power integrity clocking and timing power dissipation and electrostatic discharge ESD Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up paying much attention to logic circuit layout and system designs Armed with these tools readers can not only comprehensively understand the features and limitations of modern VLSI technologies but also have enough background to adapt to this ever changing field The Digital Logic Handbook Digital Equipment Corporation, 1966

Digital Circuit Simulation Using Excel Anthony Mazzurco, 2020-09-30 This book develops techniques for simulating digital

logic circuits in Microsoft's Excel that provide a useful tool to both the student and the practicing engineer. It introduces the subject by showing how logic devices taken from the standard 7400 family can be modeled and stored in a library. These functions can then be combined by the user as building blocks to design more complex circuits both combinatorial and sequential. The simulations not only allow the designs to be verified to ensure they provide expected outputs and states but can also perform detailed timing analysis. Worst case timing conditions can be imposed to stress the circuits so that the resultant behavior can be observed. Methods showing how the user can add additional devices to the simulation library are also covered.

Digital System Test and Testable Design Zainalabedin Navabi, 2010-12-10 This book is about digital system testing and testable design. The concepts of testing and testability are treated together with digital design practices and methodologies. The book uses Verilog models and testbenches for implementing and explaining fault simulation and test generation algorithms. Extensive use of Verilog and Verilog PLI for test applications is what distinguishes this book from other test and testability books. Verilog eliminates ambiguities in test algorithms and BIST and DFT hardware architectures and it clearly describes the architecture of the testability hardware and its test sessions. Describing many of the on-chip decompression algorithms in Verilog helps to evaluate these algorithms in terms of hardware overhead and timing and thus feasibility of using them for System on Chip designs. Extensive use of testbenches and testbench development techniques is another unique feature of this book. Using PLI in developing testbenches and virtual testers provides a powerful programming tool interfaced with hardware described in Verilog. This mixed hardware software environment facilitates description of complex test programs and test strategies.

Design of Computers and Other Complex Digital Devices Sunggu Lee, 2000 Uniquely this advanced digital logic design textbook has as its design target an actual commercial 8-bit processor, the Intel 8080, serving as an extended example of the effective use of VHDL, a hardware description language. EPG As field programmable gate arrays and the ASM (Algorithmic State Machine) method to achieve this end. Part I provides a refresher course in basic digital logic design. Part II examines the use of programmable logic devices, hardware description languages and the ASM method for implementation of general algorithms in hardware. Part III details the microprocessor's design and implementation specifications. Appends an overview of the Intel 8080 instruction set and suggested lab projects for junior and senior level students in electrical and computer engineering.

Foundations of Embedded Systems Alexander Barkalov, Larysa Titarenko, Małgorzata Mazurkiewicz, 2019-02-04 This book is devoted to embedded systems (ESs) which can now be found in practically all fields of human activity. Embedded systems are essentially a special class of computing systems designed for monitoring and controlling objects of the physical world. The book begins by discussing the distinctive features of ESs above all their cybernetic physical character and how they can be designed to deliver the required performance with a minimum amount of hardware. In turn it presents a range of design methodologies. Considerable attention is paid to the hardware implementation of computational algorithms. It is shown that different parts of complex ESs could be

implemented using models of finite state machines FSMs Also field programmable gate arrays FPGAs are very often used to implement different hardware accelerators in ESs The book pays considerable attention to design methods for FPGA based FSMs before the closing section turns to programmable logic controllers widely used in industry This book will be interesting and useful for students and postgraduates in the area of Computer Science as well as for designers of embedded systems In addition it offers a good point of departure for creating embedded systems for various spheres of human activity **Digital Electronics and System** Abhishek Bhatt,2025-06-01 Digital Design John F. Wakerly,1990 This popular volume provides a solid foundation in the elements of basic digital electronics and switching theory that are used in most practical digital design today and builds on that theory with discussions of real world digital components design methodologies and tools Covers a full range of topics number systems and codes digital circuits combinational logic design principles and practices combinational logic design with PLDs sequential logic design principles and practices sequential logic design with PLDs memory and additional real world topics e g computer aided engineering tools design for testability estimating digital system reliability and transmission lines reflections and termination This edition introduces PLDs as soon as possible emphasizes CMOS logic families and introduces digital circuits in a strongly technology independent fashion covers the latest Generic Array Logic GAL devices offers expanded coverage of ROM and RAM system level design and provides additional design examples For those needing a solid introduction or review of the principles and practices of modern digital design Previously announced in Oct 1992 PTR Catalogue

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