



FEATURES

- Monolithic synthesizer at 2.5 GSPS update rate
 - 50% to 1.25 GHz in baseband mode
 - 1.25 GHz to 5.0 GHz in subcarrier
- Outstanding floating-point multiplication of carrier synthesizer
 - $f_{out} = 500\text{ MHz}$, $f_{CLK} = 400\text{ MHz}$
 - $f_{out} = 500\text{ MHz}$, $f_{CLK} = 400\text{ MHz}$
 - $f_{out} = 500\text{ MHz}$, $f_{CLK} = 400\text{ MHz}$
- Multiport SPI data interface
 - Up to 1.25 GSPS operation
 - Source synchronous I/O timing
- Not compatible with the [AD9738](#)
- Outstanding spurious-free dynamic range
- Programmable output current 0.7 mA to 25.7 mA
- Low power 1.75 W at 2.5 GSPS

APPLICATIONS

- Baseband communication systems
- Military systems
- Instrumentation, test and measurement equipment
- Radio systems

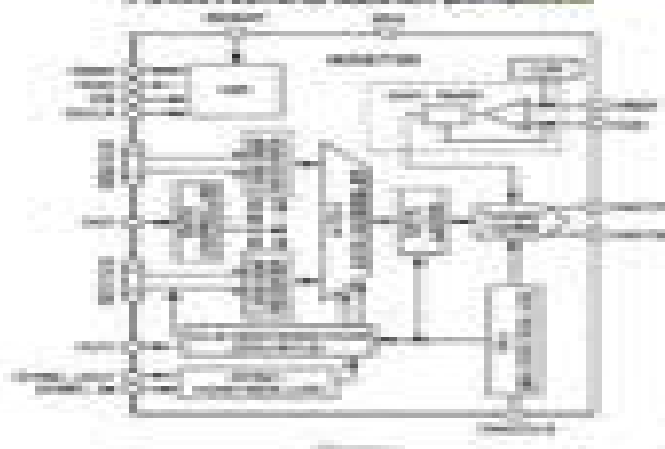
GENERAL DESCRIPTION

The AD9739 is a 14-bit, 2.5 GSPS high performance (HP) digital-to-analog converter (DAC) capable of converting baseband signals from 0.5 to 1.25 GHz, or subcarrier signals from 1.25 GHz to 5.0 GHz. The AD9739 uses a quadrature carrier architecture that provides exceptionally low distortion performance with an excellent floating-point carrier frequency capability. This feature enables customers to generate up to the Nyquist frequency in baseband mode as well as spread and wideband signals in subcarrier mode. The output current can be programmed over the full range of 0.7 mA to 25.7 mA.

The AD9739 is an integrated solution for digital communications. It has a built-in carrier synthesizer, a 14-bit digital-to-analog converter, a digital baseband processor, and a multiport SPI interface. The AD9739 is designed to be used in a variety of applications, including baseband and subcarrier mode. The AD9739 is a high performance DAC that can be used in a variety of applications, including baseband and subcarrier mode. The AD9739 is a high performance DAC that can be used in a variety of applications, including baseband and subcarrier mode.

The AD9739 is manufactured in a 0.18 μm 1.25 GSPS process and operates from 1.2 V and 1.5 V supplies. It is supplied in a 100-lead chip-scale package for volume production.

FUNCTIONAL BLOCK DIAGRAM



PRODUCT HIGHLIGHTS

1. Offers an outstanding high quality synthesized signal with baseband up to 1.25 GHz or subcarrier up to 5.0 GHz.
2. A programmable quadrature baseband architecture provides exceptional baseband performance while maintaining low power operation.
3. A built-in carrier synthesizer (CTSC) interface supports the customer's preferred carrier frequency.
4. Offers a wide range of output current and spreadable bandwidth.
5. A wideband architecture capability.
6. Programmable differential output current up to 25.7 mA.

14 Bit 2.5 Gbps Rf Digital To Analog Converter Data

**Jun-an Zhang, Ruitao Zhang, Guangjun
Li**



14 Bit 2.5 Gsps Rf Digital To Analog Converter Data:

Millimeter-Wave (mmWave) Communications Manuel García Sanchez, 2020-03-25 The millimeter wave frequency band 30-300 GHz is considered a potential candidate to host very high data rate communications. First used for high capacity radio links and then for broadband indoor wireless networks, the interest in this frequency band has increased as it is proposed to accommodate future 5G mobile communication systems. The large bandwidth available will enable a number of new uses for 5G. In addition, due to the large propagation attenuation, this frequency band may provide some additional advantages regarding frequency reuse and communication security. However, a number of issues have to be addressed to make mm-wave communications viable. This book collects a number of contributions that present solutions to these challenges. Flexible Bioelectronics with Power Autonomous Sensing and Data Analytics Sameer Sonkusale, Maryam Shojaei Baghini, Shuchin Aeron, 2022-06-14 This book provides readers with an introduction to the materials and devices necessary for flexible sensors and electronics, followed by common techniques for fabrication of such devices and system-level integration. Key insights into fabrication and processing will guide readers through the tradeoff choices in designing such platforms. A comprehensive review of two specific flexible bioelectronic platforms related to smart bandages for wound monitoring and thread-based diagnostics for wearable health will demonstrate practical application at the system level. The book also provides a unique electrical engineering perspective by reviewing circuit architectures for low-noise signal conditioning of weak signals from sensors and for low-power analog-to-digital converters for signal acquisition. To achieve energy autonomy, authors provide several examples of CMOS energy harvesting front-end circuits and voltage boosters. Beyond circuit architectures, the book also provides a review of the modern theory of sampling and recovery of sparse signals, also known as compressed sensing. They then highlight how these principles can be leveraged for design and implementation of efficient signal acquisition hardware and reliable processing of acquired data for flexible electronic platforms. **High-Speed and High-Performance Direct Digital Frequency Synthesizer Design** Jun-an Zhang, Ruitao Zhang, Guangjun Li, 2022-03-22 The book focuses on design technology of high-speed and high-performance direct digital frequency synthesizer (DDS) chip. The technologies involve phase-to-amplitude converter design, D/A converter design, phase accumulator design, multi-chip synchronization, circuit design, etc. In each chapter, the concept of the technology is explained first, and then the features of different implementation schemes are introduced through real design cases. Moreover, a design case of a 2.5 GHz monolithic DDS in 0.18 μm CMOS, which was designed by the authors, is introduced in detail, which can help the reader understand about the DDS design deeply. The book is suitable for readers who are interested to learn practical design technology in DDS. The book can benefit researchers, engineers, and graduate students in fields of mixed-signal IC design, communication engineering, electronics engineering, and radar engineering, etc. **Wireless Communications Systems** Randy L. Haupt, 2019-12-17 A comprehensive introduction to the fundamentals of design and applications of wireless communications

Wireless Communications Systems starts by explaining the fundamentals needed to understand design and deploy wireless communications systems. The author, a noted expert on the topic, explores the basic concepts of signals, modulation, antennas, and propagation with a MATLAB emphasis. The book emphasizes practical applications and concepts needed by wireless engineers. The author introduces applications of wireless communications and includes information on satellite communications, radio frequency identification, and offers an overview with practical insights into the topic of multiple input multiple output (MIMO). The book also explains the security and health effects of wireless systems concerns on users and designers. Designed as a practical resource, the text contains a range of examples and pictures that illustrate many different aspects of wireless technology. The book relies on MATLAB for most of the computations and graphics. This important text reviews the basic information needed to understand and design wireless communications systems. Covers topics such as MIMO systems, adaptive antennas, direction finding, wireless security, internet of things (IoT), radio frequency identification (RFID), and software defined radio (SDR). Provides examples with a MATLAB emphasis to aid comprehension. Includes an online solutions manual and video lectures on selected topics. Written for students of engineering and physics and practicing engineers and scientists. Wireless Communications Systems covers the fundamentals of wireless engineering in a clear and concise manner and contains many illustrative examples.

Handbook on Sensor Networks Yang Xiao, Hui Chen, Frank Haizhon Li, 2010. Sensor networks have many interesting applications with great utility; however, their actual deployment and realization rely on continuous innovations and solutions to many challenging problems. Thus, sensor networks have recently attracted the attention of many researchers and practitioners. The compilation of the Handbook on Sensor Networks will meet the demand of the sensor network community for a comprehensive reference and summary of the current state of the area. The Handbook on Sensor Networks is a collection of approximately 40 chapters on sensor network theory and applications. The book spans a wide spectrum and includes topics in medium access control, routing, security, and privacy; coverage and connectivity; modeling and simulations; multimedia; energy efficiency; localization and tracking; design and implementation, as well as sensor network applications. *EDN*, 2007.

Current Trends In Heterojunction Bipolar Transistors M F Chang, 1996-01-29. Recent advances in communication, digital signal processing, and computational systems demand very high performance electronic circuits. Heterojunction Bipolar Transistors (HBTs) have the potential of providing a more efficient solution to many key system requirements through intrinsic device advantages. This book reviews the present status of GaAs, InP, and silicon-based HBT technologies and their applications to digital, analog, microwave, and mixed signal circuits and systems. It represents the first major effort to cover the complete scope of the HBT technology development in the past decade, starting from the fundamental device physics, material growth, device reliability, scaling, processing, modeling, to advanced HBT integrated circuit design for various system applications. *Millimeter-Wave Antennas: Configurations and Applications* Jaco du Preez, Saurabh Sinha, 2016-06-20. This book comprehensively reviews the state of the art in millimeter

wave antennas traces important recent developments and provides information on a wide range of antenna configurations and applications While fundamental theoretical aspects are discussed whenever necessary the book primarily focuses on design principles and concepts manufacture measurement techniques and practical results Each of the various antenna types scalable to millimeter wave dimensions is considered individually with coverage of leaky wave and surface wave antennas printed antennas integrated antennas and reflector and lens systems The final two chapters address the subject from a systems perspective providing an overview of supporting circuitry and examining in detail diverse millimeter wave applications including high speed wireless communications radio astronomy and radar The vast amount of information now available on millimeter wave systems can be daunting for researchers and designers entering the field This book offers readers essential guidance helping them to gain a thorough understanding based on the most recent research findings and serving as a sound basis for informed decision making

Low-Level Radio Frequency Systems Stefan Simrock,Zheqiao Geng,2022-03-02 This book begins with an overview of the RF control concepts and strategies It then introduces RF system models for optimizing the system parameters to satisfy beam requirements and for controller design In addition to systematically discussing the RF field control algorithms it presents typical architecture and algorithms for RF signal detection and actuation Further the book addresses the analysis of the noise and nonlinearity in LLRF systems to provide a better understanding of the performance of the RF control system and to specify the performance requirements for different parts of the RF system Today accelerators require increased RF stability and more complex operation scenarios such as providing beam for different beam lines with various parameters and as a result LLRF systems are becoming more critical and complex This means that LLRF system developers need have extensive knowledge of the entire accelerator complex and a wide range of other areas including RF and digital signal processing noise analysis accelerator physics and systems engineering Providing a comprehensive introduction to the basic theories algorithms and technologies this book enables LLRF system developers to systematically gain the knowledge required to specify design and implement LLRF systems and integrate them with beam acceleration It is intended for graduate students professional engineers and researchers in accelerator physics

Index to IEEE Publications Institute of Electrical and Electronics Engineers,1990 Issues for 1973 cover the entire IEEE technical literature **Ultrafast Electronics and Optoelectronics** ,1997 **15th Symposium on Integrated Circuits and Systems Design** Ricardo Augusto da Luz Reis,Ney Calazans,2002 **Proceedings** ,1997

Нанотехнологии в электронике. Выпуск 2 Коллектив авторов,2015-08-04 3 5 *Design of a 2-MS/s 14-bit Extended Counting Analog-to-Digital Converter* ,2022 **A 14-bit 200MS/s Digital to Analog Converter Design**

Tzung-shen Chen,2004 **Radio Frequency Digital to Analog Converter** Susan Luschas,2003 Dynamic performance of high speed high resolution digital to analog converters DACs is limited by distortion at the data switching instants Inter symbol interference ISI imperfect timing synchronization and clock jitter are all culprits A DAC output current controlled by

an oscillating waveform is proposed to mitigate the effects of the switching distortion. The oscillating waveform should be a multiple $k f_s$ of the sampling frequency f_s where $k \neq 1$. The waveforms can be aligned so that the data switching occurs in the zero regions of the oscillating output. This makes the DAC insensitive to switch dynamics and jitter. The architecture has the additional benefit of mixing the DAC impulse response energy to a higher frequency. An image of a low IF input signal can therefore be output directly at a high IF or RF frequency for transmit communications applications. A narrow band sigma delta DAC with eight unit elements is chosen to demonstrate the radio frequency digital to analog converter (RF DAC) concept. A sigma delta architecture allows the current source transistors to be smaller since mismatch shaping is employed. Smaller current source transistors have a lower drain capacitance allowing large high frequency output impedance to be achieved without an extra cascode transistor. Elimination of the cascode reduces transistor headroom requirements and allows the DAC to be built with a 1.8V supply. The RF DAC prototype is targeted to GSM transmit specifications and implemented in 0.18 μm CMOS technology. Measured single tone SFDR is 75dBc, SNR is 52dB and IMD3 is 70.8dBc over a 17.5MHz bandwidth centered at 942.5MHz. Measured SNR has the predicted dependence on the phase alignment of the data clock and oscillating pulse.

1-GS/s, 14-bit Digital-to-analog Converter and Track-and-hold Amplifier Dongwon Seo, 2000 **A 14-bit 40GS/s Digital-to-analog Converter with Mixing Switching Glitch Compensation**, 2024 **Data Converters** G. B. Clayton, 1982. A complete guide to data converters: how they work, what they can do, how to select and use them. Provides knowledge of these devices which is elemental to understanding the important interface between analogue and digital systems. Discusses principles underlying the operation of data converters, converter applications and procedures for implementing these applications. Covers signal sampling, acquisition systems, multiplexers, converter terminology and data sheet specifications. An essential guide to anyone interested in modern electronic instrumentation.

Reviewing **14 Bit 2 5 Gsps Rf Digital To Analog Converter Data**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is really astonishing. Within the pages of "**14 Bit 2 5 Gsps Rf Digital To Analog Converter Data**," an enthralling opus penned by a very acclaimed wordsmith, readers embark on an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve into the book's central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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Table of Contents 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data

1. Understanding the eBook 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data
 - The Rise of Digital Reading 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data
 - Advantages of eBooks Over Traditional Books
2. Identifying 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data
 - User-Friendly Interface
4. Exploring eBook Recommendations from 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data
 - Personalized Recommendations
 - 14 Bit 2 5 Gsps Rf Digital To Analog Converter Data User Reviews and Ratings

- 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data and Bestseller Lists
- 5. Accessing 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data Free and Paid eBooks
 - 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data Public Domain eBooks
 - 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data eBook Subscription Services
 - 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data Budget-Friendly Options
- 6. Navigating 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data eBook Formats
 - ePub, PDF, MOBI, and More
 - 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data Compatibility with Devices
 - 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data Enhanced eBook Features
- 7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Highlighting and Note-Taking 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Interactive Elements 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
- 8. Staying Engaged with 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
- 9. Balancing eBooks and Physical Books 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
- 10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
- 11. Cultivating a Reading Routine 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Setting Reading Goals 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Fact-Checking eBook Content of 14 Bit 2 5 Gbps Rf Digital To Analog Converter Data
 - Distinguishing Credible Sources

13. Promoting Lifelong Learning
 - Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
 - Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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