

160 dB Range 100 pA to 10 mA Low Cost Logarithmic Converter

Data Sheet ADL5303

FEATURES

Optimized for fiber optic photodiode interfacing 8 full decades of range
Law conformance: 0.1 d8 from 1 nA to 1 mA
Single-supply operation: 3.0 V to 5.5 V
Complete and temperature stable
Accurate laser trimmed scaling
Logarithmic slope of 10 mV/d8 (at the VLOG pin)
Basic logarithmic intercept at 100 pA
Easy adjustment of slope and intercept
Output bandwidth of 10 MHz, 15 V/µs slew rate
Miniature 16-lead package (LFCSP)
Low power: -4.5 mA quiescent current (enabled)

APPLICATIONS

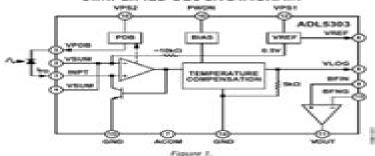
High accuracy optical power measurement Wide range baseband log compression Versatile detector for APC loops

GENERAL DESCRIPTION

The ADL5303 is a monolithic logarithmic detector optimized for the measurement of low frequency signal power in fiber optic systems and offers a large dynamic range in a versatile and easily used form. Wide measurement range and accuracy are achieved using proprietary design and precise laser trimming. The ADL5303 requires only a single positive supply, V_{m_i} of 5 V, When using low supply voltages, the log slope can be altered to fit the available span. Low quiescent current and chip disable facilitate use in battery-operated applications.

The input current, L₁₀, flows in the collector of an optimally scaled NPN transistor, connected in a feedback path around a low offset IFET amplifier. The current summing input node operates at a constant voltage, independent of current, with a default value of 0.5 V; this may be adjusted over a wide range. An adaptive biasing scheme is provided for reducing photodiode dark current at very low light input levels. The VPDB pin applies approximately 0.1 V reverse bias across the photodiode for L₁₀ = 100 pA, rising linearly to 2.0 V of reverse bias at L₁₀ = 10 mA to improve response time at higher power levels. The

SIMPLIFIED BLOCK DIAGRAM



input pin INPT is flanked by the VSUM guard pins that track the voltage at the summing node. Connecting the exposed pad of the device to the VSUM pins provides a continuous guard to minimize leakage into the INPT pin.

The default value of the logarithmic slope at the VLOG output is set by an internal 5 ktl resistor. Logarithmic slope can be loseered with an external shant resistor or increased using the buffer and a pair of external feedback resistors. The addition of a capacitor at the VLOG pin provides a simple low-pass filter. The intermediate voltage, $V_{\rm non}$, is buffered in an output stage that can swing to within about 100 mV of ground and the positive supply, $V_{\rm ro}$, and provides a peak current drive capacity of $\pi 20$ mA. An on-board 2 V reference is provided to facilitate the repositioning of the intercept. The incremental bandwidth of a translinear logarithmic amplifier inherently diminishes for small input currents. At $I_{\rm ro} = 1$ nA, the bandwidth of the ADL5303 is approximately 2 kHz increasing in proportion to $I_{\rm ro}$ up to a maximum value of 10 MHz.

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B Lingard

160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic:

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The book delves into 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic. 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic is a vital topic that needs to be grasped by everyone, ranging from students and scholars to the general public. This book will furnish comprehensive and in-depth insights into 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic, encompassing both the fundamentals and more intricate discussions.

- 1. The book is structured into several chapters, namely:
 - Chapter 1: Introduction to 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic
 - Chapter 2: Essential Elements of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic
 - o Chapter 3: 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic in Everyday Life
 - Chapter 4: 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic in Specific Contexts
 - \circ Chapter 5: Conclusion
- 2. In chapter 1, the author will provide an overview of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic. This chapter will explore what 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic is, why 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic is vital, and how to effectively learn about 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic.
- 3. In chapter 2, the author will delve into the foundational concepts of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic. The second chapter will elucidate the essential principles that must be understood to grasp 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic in its entirety.
- 4. In chapter 3, this book will examine the practical applications of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic in daily life. This chapter will showcase real-world examples of how 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic can be effectively utilized in everyday scenarios.
- 5. In chapter 4, this book will scrutinize the relevance of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic in specific contexts. The fourth chapter will explore how 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic is applied in specialized fields, such as education, business, and technology.
- 6. In chapter 5, this book will draw a conclusion about 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic. This chapter will summarize the key points that have been discussed throughout the book.
 - The book is crafted in an easy-to-understand language and is complemented by engaging illustrations. It is highly recommended for anyone seeking to gain a comprehensive understanding of 160 Db Range 100 Pa To 10 Ma Low Cost Logarithmic.

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