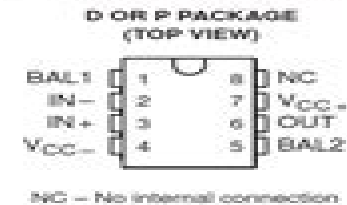


- Low Input Bias Current, 50 pA Typ
- Low Input Noise Current, 0.01 pA/√Hz Typ
- Low Supply Current, 2 mA Typ
- High Input Impedance, $10^{12} \Omega$ Typ
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- Low 1/f Noise Corner, 50 Hz Typ
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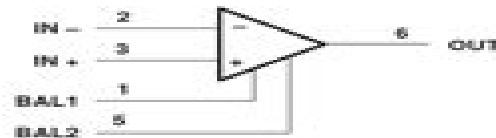
description

This device is a low-cost, high-speed, JFET-input operational amplifier with very low input offset voltage and a maximum input offset voltage drift. It requires low supply current, yet maintains a large gain-bandwidth product and a fast slew rate. In addition, the matched high-voltage JFET input provides very low input bias and offset currents.

The LF411 can be used in applications such as high-speed integrators, digital-to-analog converters, sample-and-hold circuits, and many other circuits.

The LF411C is characterized for operation from 0°C to 70°C. The LF411H is characterized for operation from –40°C to 85°C.

symbol



AVAILABLE OPTIONS

T _A	V _{IO} max AT 25°C	PACKAGE	
		SMALL OUTLINE (D)	PLASTIC DIP (P)
0°C to 70°C	2 mV	LF411CD	LF411CP
–40°C to 85°C	2 mV	LF411HD	LF411HP

The D packages are available taped and reeled. Add the suffix R to the device type (i.e., LF411CDR).



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