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Digital Circuits and Design

FIFTH EDITION



S. Salivahanan ■ S. Arivazhagan

Digital Circuit And Design Salivahanan Arivazhagan

S. Arivazhagan



Digital Circuit And Design Salivahanan Arivazhagan :

Digital Circuits and Design S. Salivahanan, 2018-05-15 Digital Circuits and Design is a textbook dealing with the basics of digital technology including the design aspects of circuits. The book fulfils the requirements of the students of electrical electronics and computer science engineering for the first course on the subject. The book is divided into 16 chapters. Each chapter begins with an introduction and ends with a set of review questions and problems. All the topics have been illustrated with clear diagrams. A variety of examples are given to enable students to design digital circuits efficiently. The fifth edition of the book provides discussion of Verilog, a popular hardware description language to demonstrate solutions to problems in digital design. The current edition also provides additional example problems. **Digital Design and Computer**

Organisation D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems. It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits. The book includes an accompanying CD that includes the majority of circuits highlighted in the text, delivering you hands on experience in the simulation and observation of circuit functionality. These circuits were designed and tested with a user friendly Electronics Workbench package Multisim. Textbook Edition that enables your progression from truth tables onward to more complex designs. This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture. The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles. Digital Circuits And Design S. Arivazhagan, 2003 **Digital Circuits And Design, 3E**

Arivazhagan S. Salivahanan, 2009-11 The Use Of Digital Circuits Is Increasing In All Disciplines Of Engineering. Consequently Students Need To Have An In Depth Knowledge On Them. Digital Circuits And Design Is A Textbook Dealing With The Basics Of Digital Technology Including The Design Asp. *Intelligent Copyright Protection for Images* Subhrajit Sinha

Roy, Abhishek Basu, Avik Chattopadhyay, 2019-04-16 This book describes the need of copyright protection for multimedia objects and develops an invisible image watermarking scheme to serve the purpose of copyright protection. Here intelligent systems are introduced to generate a better visual transparency with increased payload. **Digital Circuits and Design S.**

Salivahanan, S. Arivazhagan, 2000-08-01 **Digital Circuits & Design** D.P Kothari, J.S Dhillon, 2015 This student friendly practical and example driven book gives students a solid foundation in the basics of digital circuits and design. The fundamental concepts of digital electronics such as analog digital signals and waveforms, digital information and digital integrated circuits are discussed in detail using relevant pedagogy. **Digital Electronics: A Primer - Introductory Logic**

Circuit Design Mark S Nixon, 2015-01-27 This practical introduction explains exactly how digital circuits are designed from the basic circuit to the advanced system. It covers combinational logic circuits which collect logic signals to sequential logic circuits which embody time and memory to progress through sequences of states. The primer also highlights digital

arithmetic and the integrated circuits that implement the logic functions Based on the author s extensive experience in teaching digital electronics to undergraduates the book translates theory directly into practice and presents the essential information in a compact digestible style Worked problems and examples are accompanied by abbreviated solutions with demonstrations to ensure that the design material and the circuits operation are fully understood This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction **Digital Logic Design (gtu)** Arivazhagan S,2010 Digital Circuits and Logic Design Samuel C. Lee,1976

Digital Circuit Design for Computer Science Students Niklaus Wirth,2012-12-06 This book emerged from lecture notes of a course taught in the second year to students of Computer Science at the Federal Institute of Technology Zurich The topic of hardware design plays a relatively minor role in Computer Science curricula at many universities Most courses concentrate on the various aspects of theory software and of information systems Students therefore obtain few opportunities to deal with concrete engineering problems and physical devices We consider this as rather unfortunate particularly for technical universities As a result we observe a growing gap between interest in and understanding of design issues involving not only software but also hardware and interfaces This is regrettable at a time when new and advanced solutions to many problems are often crucially influenced by recent hardware developments at a time when the engineer needs to be competent in both software and hardware issues in order to find an optimally integrated competitive solution It turns out that the hesitation of many students in Computer Science to take an active interest in hardware is or her daily tool does not only stem from a preference of clean abstract concepts with a corresponding distaste for dealing with concrete components construction techniques and the dirty realities inflicted by nature but also stems from the lack of a bridge between the two realms Digital Electronic Circuits Shuqin Lou,Chunling Yang,2019-05-20 This book presents three aspects of digital circuits digital principles digital electronics and digital design The modern design methods of using electronic design automation EDA are also introduced including the hardware description language HDL designs with programmable logic devices and large scale integrated circuit LSI The applications of digital devices and integrated circuits are discussed in detail as well Modern Digital Electronics R Jain,2006-08-21 Part of the McGraw Hill Core Concepts Series Modern Digital Electronics is an ideal textbook for a course on digital electronics at the undergraduate level The text introduces digital systems and techniques through a bottom up approach that allows users to start out with the basics of integrated circuits circuit design and delve into topics such as digital design flip flops A D and D A The book then moves on to explore elements of complex digital circuits with material like FPGAs PLDs PLAs and more Rich pedagogical features include review questions with answers a glossary of key terms a large number of solved examples and numerous practice problems This is a concise less expensive alternative to other digital logic designs This series is edited by Dick Dorf **A Definitive Guide to Logic Circuits and Advanced Circuits Mastering Digital Electronics** ,2024-01-18 Introduction The Aims and

Objectives of the Book My main aim in writing this book is to introduce you to the exciting and challenging field of digital electronics I want to develop your desire and ability to understand how digital circuits work After reading this book you should be able to do some or all of the following You will understand what TTL and CMOS mean and appreciate their main differences You should know what the five main logic gates are and their respective symbols and Boolean expressions You should know the basics of Boolean algebra and use it to simplify logic expressions and circuits You should know what Karnaugh maps are and how to use them to simplify logic circuits and expressions You should know how to implement the 1st and 2nd canonical formats for Karnaugh maps You will know how the JK flip flop works and how it was born out of the SR latch You should be able to use the JK flip flop and the D type latch to create a series of counters and different shift registers such as SIPO SISO PIPO and PISO You should understand the difference between sequential and combinational logic You should be able to use a range of design techniques that is state diagrams transition tables etc You should be able to create a range of combinational logic circuits such as half and full adders binary subtractors multiplexers etc You should understand how the 555 timer IC works and how to configure it in a range of different applications such as the monostable the astable and PWM You should be able to design a range of logic circuits You should be able to use the ECAD software TINA 12

Digital Design Nicholas Pappas, 2019-10-22 Digital Design This text is different from the many introductory digital design texts because products are actually designed by implementing a design and not just talk about logic circuits used in a digital circuit And you are asked to work hard doing experiments so that you acquire real world experience with commercially available digital circuits To be fair the contents of this text are only a beginning because modern digital circuits are assemblies of thousands of logic gates where the traditional schematic is essentially useless Consequently traditional gate level circuit design the schematic capture method of a thousand gate circuit is impractical The solution to this design problem is a hardware description language or HDL The HDL language we prefer is Verilog However there is a catch You have to know how to design using schematic capture before you can design using Verilog s text capture method Design using schematic capture is what this text is about We provide a start showing how to design with Verilog s text capture method so that you can move on to multi 1 000 gate chip designs We start at the beginning There are three basic tools necessary to execute any digital design Two tools are the Truth Tables and Karnaugh maps which are graphical displays of switching functions The third tool is the theoretical basis of digital design a Switching Algebra The three tools provide means to analyze and understand the basic circuits of digital logic The basic circuits are standard commercially available logic circuits which are described and their equations are presented The experiments include applications of these building blocks The Algorithmic State Machine ASM chart a fourth tool is a preferred way to represent any algorithm describing a digital function We show how to create ASM charts that represent state machines From the ASM one can derive the associated truth table the associated equations and the digital circuit design Many designs of complex building blocks with memory are

based on one bit memory circuits referred to as flip flops The commercially available D and JK flip flops are the preferred flip flops Circuit designs using flip flops are implemented by the ASM method Designs include up and up down synchronous counters shift registers and linear feedback shift registers This is followed by showing how to design memory systems with and without a cache hierarchy A high reliability memory system incorporates error correction and control ecc circuits How to add ecc circuits to a memory system is demonstrated The required mathematics is discussed and the coding process is described Maurice Wilkes invented a tremendously simplified state machine design process about 1950 which is described here Verilog is a large language with many parts We only discuss the basic parts For large circuit designs text capture of digital designs is preferred to schematic capture Verilog uses text capture to represent digital circuits with a hierarchy of modules that are interconnected via input and output ports Examples show how to write modules defining digital circuits so that you can move on to designing multi 1 000 gate chip designs using Verilog which is classified as a hardware description language HDL Design examples show how to derive a truth table from an ASM chart how to derive design equations from the truth table and then how to design the circuit the equations represent

Mastering Digital Electronics Hubert Henry Ward, 2024 Discover the essential knowledge and practical skills to excel in the dynamic field of digital electronics with Mastering Digital Electronics From the fundamentals of diode resistor logic to unraveling the intricacies of TTL and CMOS logic gates this book takes you on a journey through the evolution of digital electronics Starting with the humble SR latch and progressing to the revolutionary JK flip flop that powers today's computer technology you'll gain a solid foundation in binary arithmetic Boolean algebra and the representation and optimization of digital circuits The book gives a firm understanding of digital electronics then moves on to using standard design methods such as state diagrams to design everyday logic circuits such as counters shift registers PISO and SIPO registers It delves into how to design some challenging digital circuits such as a crossroad set of traffic lights with a pelican crossing and how to use the 555 timer to control a variety of applications At every stage of the book you will be learning how to use TINA version 12 an industry standard ECAD and many of its functions Moreover the book ventures into using the software to simulate and so confirm all the circuits you design For readers interested in advancing to the practical implementation of the circuits discussed the book provides opportunities to design PCB circuit boards for selected circuits using the software This book will suit any student of digital electronics at any level and provide them with essential reference material for them to start a career in digital electronics

What You'll Learn What logic gates are and the difference between sequential and combinational logic How to use binary numbers and how to perform all arithmetic operations Learn about the five basic logic gates AND NAND OR NOR and EXOR gates How to minimize logic circuits using Boolean algebra and Karnaugh maps How to state diagrams to design logic circuits How to design a range of logic circuits Learn about combinational circuits and the half and full adder as well as a logic circuit to subtract binary numbers Learn about the 555 timer and how to apply it to a range of logic circuits Who This Book Is

For Students who are studying for an engineering qualification Hobbyist who wants to design digital circuits Engineers who need a go to reference book on digital electronics Digital Circuits And Logic Design Lee,2009 **Digital Electronics 2** Tertulien Ndjountche,2016-08-16 As electronic devices become increasingly prevalent in everyday life digital circuits are becoming even more complex and smaller in size This book presents the basic principles of digital electronics in an accessible manner allowing the reader to grasp the principles of combinational and sequential logic and the underlying techniques for the analysis and design of digital circuits Providing a hands on approach this work introduces techniques and methods for establishing logic equations and designing and analyzing digital circuits Each chapter is supplemented with practical examples and well designed exercises with worked solutions This second of three volumes focuses on sequential and arithmetic logic circuits It covers various aspects related to the following topics latch and flip flop binary counters shift registers arithmetic and logic circuits digital integrated circuit technology semiconductor memory programmable logic circuits Along with the two accompanying volumes this book is an indispensable tool for students at a bachelors or masters level seeking to improve their understanding of digital electronics and is detailed enough to serve as a reference for electronic automation and computer engineers Digital Circuit Design with an Introduction to CPLDs and FPGAs Steven T. Karris,2005 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It supplements our Electronic Devices and Amplifier Circuits ISBN 0 9744239 4 7 It is self contained begins with the basics and ends with the latest developments of the digital technology The intent is to prepare the reader for advanced digital circuit design and programming the powerful Complex Programmable Logic Devices CPLDs and Field Programmable Gate Arrays FPGAs The prerequisites for this text are just basic high school math Accordingly it can be read and understood by high school seniors trade school community college and 4 year university students It is ideal for self study Chapter 1 is an introduction to the decimal binary octal and hexadecimal numbers their representation and conversion from one base to another Chapter 2 presents an introduction to arithmetic operations in binary octal and hexadecimal numbers The tens complement and nines complements in the decimal system and the twos complement and ones complements in the binary system are discussed and illustrated with numerous examples Chapter 3 begins with an introduction to sign magnitude representation of binary numbers It concludes with a discussion on floating point arithmetic for representing large numbers and the IEEE standard that specifies single precision 32 bit and double precision 64 bit floating point representation of numbers Chapter 4 describes the most commonly used binary codes The Binary Coded Decimal BCD the Excess 3 Code the 2 421 Code the Gray Code and the American Standard Code for Information Interchange ASCII code are introduced as well as the use of parity bits Chapter 5 begins with the basic logic operations and continues with the fundamentals of Boolean algebra and the basic postulates and theorems as applied to electronic logic circuits Truth tables are defined and examples are given to illustrate how they can be used to prove Boolean algebra theorems or equivalent logical expressions Chapter 6

introduces the standard forms of expressing Boolean functions the minterms and maxterms also known as standard products and standard sums respectively A procedure is also presented to show how one can convert one form to the other This topic is essential in understanding the programming of Programmable Logic Arrays PLAs discussed in Chapter 11 Chapter 7 is an introduction to combinational logic circuits It begins with methods of implementing logic diagrams from Boolean expressions the derivation of Boolean expressions from logic diagrams input and output waveforms and the use of Karnaugh maps for simplifying Boolean expressions Chapter 8 is an introduction to sequential logic circuits It begins with a discussion of the different types of flip flops and continues with the analysis and design of binary counters registers ring counters and ring oscillators Chapter 9 is an introduction to computer memory devices We discuss the random access memory RAM read only memory ROM row and column decoders memory chip organization static RAMs SRAMs dynamic RAMs DRAMs volatile nonvolatile programmable ROMs PROMs Erasable PROMs EPROMs Electrically Erasable PROMs EEPROMs flash memories and cache memory Chapter 10 begins with an introduction to the basic components of a digital computer It continues with a discussion of the basic microprocessor operations and concludes with the description of more advanced arithmetic and logic operations We consider Chapter 11 as the highlight of this text It is an introduction to Field Programmable Devices FPDs also referred to as Programmable Logic Devices PLDs It begins with the description and applications of Programmable Logic Arrays PLAs continues with the description of Simple PLDs SPLDs and Complex PLDs CPLDs and concludes with the description of Field Programmable Gate Arrays FPGAs This text includes also four appendices Appendix A is an overview of the Advanced Boolean Equation Language ABEL which is an industry standard Hardware Description Language HDL used in Programmable Logic Devices PLDs Appendix B describes the VHSIC Hardware Description Language briefly referred to as VHDL This language was developed to be used for documentation verification and synthesis of large digital designs Appendix C introduces the Verilog Hardware Description Language HDL Like VHDL introduced in Appendix B Verilog is a programming language used to describe a digital system and its components Appendix D is a brief discussion on the boundary scan architecture and the new technology trends that make using boundary scan essential for the reduction in development and production costs

Digital Circuits and Logic Designs Mr. Rohit Manglik, 2024-07-07 EduGorilla Publication is a trusted name in the education sector committed to empowering learners with high quality study materials and resources Specializing in competitive exams and academic support EduGorilla provides comprehensive and well structured content tailored to meet the needs of students across various streams and levels

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