

$$C_{jsw} = \sqrt{\frac{\epsilon_s q N_A \phi_B / N_D - 1}{2 N_A \phi_B N_D}} \quad \frac{11.7 \cdot 8.85 \cdot 10^{-14} \text{ F/cm} \cdot 1.6 \cdot 10^{19}}{2} \quad \frac{2.4 \cdot 10^{15} \cdot 10^{19}}{2.4 \cdot 10^{15} \cdot 10^{19}} \quad \frac{1}{1.02 \text{ V}}$$

$$39.6 \cdot 10^8 \text{ F/cm}^2$$

$$C_{jsw} (V) = \frac{P \cdot X \cdot C_{jsw}}{\sqrt{1 - \frac{V}{V_{bi}}}} \quad \frac{2175 \cdot 400 \cdot 10^7 \cdot 32 \cdot 10^7 \cdot 39.6 \cdot 10^8}{\sqrt{1 - \frac{V}{V_{bi}}}}$$

$$\frac{1.77 \cdot 10^{14}}{\sqrt{1 - \frac{V}{V_{bi}}}} \text{ F}$$

$$C_{jsw} (1.2 \text{ V}) = \frac{1.77 \cdot 10^{14}}{\sqrt{1 - \frac{1.2}{1.02}}} = 12 \cdot 10^{11} \text{ F}$$

$$C_{jsw} (0.6 \text{ V}) = \frac{1.77 \cdot 10^{14}}{\sqrt{1 - \frac{0.6}{1.02}}} = 14 \cdot 10^{11} \text{ F}$$

$$\{C_{jsw}\}_{1.2 \text{ V}} \cdot C_{j, 1.2 \text{ V}} \cdot C_{jsw, 1.2 \text{ V}} = 0.423 \cdot 12 \cdot 12.423 \text{ fF}$$

$$\{C_{jsw}\}_{0.6 \text{ V}} \cdot C_{j, 0.6 \text{ V}} \cdot C_{jsw, 0.6 \text{ V}} = 0.496 \cdot 39.6 \cdot 40.096 \text{ fF}$$

(b)

$$C_{ox} = \frac{\epsilon_{ox}}{t_{ox}} = \frac{3.9 \cdot 8.85 \cdot 10^{-14}}{18 \cdot 10^8} = 1.92 \cdot 10^6 \text{ F/cm}^2$$

$$C_{ox} = C_{ox} \cdot W \cdot L = 1.92 \cdot 10^6 \cdot 400 \cdot 10^7 \cdot 0.01 \cdot 10^4 = 0.077 \text{ fF}$$

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III demonstrates the design and development of hardware and software for several complex I O peripherals including PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides three case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology The book utilizes FPGA devices Nios II soft core processor and development platform from Altera Co which is one of the two main FPGA manufactures Altera has a generous university program that provides free software and discounted prototyping boards for educational institutions details at www.altera.com/university The two main educational prototyping boards are known as DE1 99 and DE2 269 All experiments can be implemented and tested with these boards A board combined with this book becomes a turn key solution for the SoPC design experiments and projects Most HDL and C codes in the book are device independent and can be adapted by other prototyping boards as long as a board has similar I O configuration

Engineering

Education, 1989 **Embedded SoPC Design with Nios II Processor and Verilog Examples** Pong P. Chu, 2012-05-14

Explores the unique hardware programmability of FPGA based embedded systems using a learn by doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog An SoPC system on a programmable chip integrates a processor memory modules I O peripherals and custom hardware accelerators into a single FPGA field programmable gate array device In addition to the customized software customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft core processor create tailored I O interfaces and develop specialized hardware accelerators for computation intensive tasks Utilizing an Altera FPGA prototyping board and its Nios II soft core processor Embedded SoPC Design with Nios II Processor and Verilog Examples takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board Emphasizing hardware design and integration throughout the book is divided into four major parts Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I O peripherals including a PS2 keyboard and mouse a graphic video controller an audio codec and an SD secure digital card Part IV provides several case studies of the integration of hardware accelerators including a custom GCD greatest common divisor circuit a Mandelbrot set fractal circuit and an audio synthesizer based on DDFS direct digital frequency synthesis methodology While designing and developing an embedded SoPC can be rewarding the learning can be a long and winding journey This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology

Introduction to VLSI Systems Ming-Bo Lin, 2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration

VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools

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Fundamentals of Solid-state Electronics Chih-Tang Sah,1996 This Solution Manual a companion volume of the book Fundamentals of Solid State Electronics provides the solutions to selected problems listed in the book Most of the solutions are for the selected problems that had been assigned to the engineering undergraduate students who were taking an introductory device core course using this book This Solution Manual also contains an extensive appendix which illustrates the application of the fundamentals to solutions of state of the art transistor reliability problems which have been taught to advanced undergraduate and graduate students

Digital Integrated Electronics [with] Solutions Manual Herbert Taub,1977

Automatic Analog IC Sizing and Optimization Constrained with PVT Corners and Layout Effects Nuno Lourenço,Ricardo Martins,Nuno Horta,2016-07-29 This book introduces readers to a variety of tools for automatic analog integrated circuit IC sizing and optimization The authors provide a historical perspective on the early methods proposed to tackle automatic analog circuit sizing with emphasis on the methodologies to size and optimize the circuit and on the methodologies to estimate the circuit s performance The discussion also includes robust circuit design and optimization and the most recent advances in layout aware analog sizing approaches The authors describe a methodology for an automatic flow for analog IC design including details of the inputs and interfaces multi objective optimization techniques and the enhancements made in the base implementation by using machine learning techniques The Gradient model is discussed in detail along with the methods to include layout effects in the circuit sizing The concepts and algorithms of all the modules are thoroughly described enabling readers to reproduce the methodologies improve the quality of their designs or use them as starting point for a new tool An extensive set of application examples is included to demonstrate the capabilities and features of the methodologies described

Satellite Communications Timothy Pratt,Jeremy E. Allnutt,2019-12-16 Extensive revision of the best selling text on satellite communications includes new

chapters on cubesats NGSO satellite systems and Internet access by satellite There have been many changes in the thirty three years since the first edition of Satellite Communications was published There has been a complete transition from analog to digital communication systems with analog techniques replaced by digital modulation and digital signal processing While distribution of television programming remains the largest sector of commercial satellite communications low earth orbit constellations of satellites for Internet access are set to challenge that dominance In the third edition chapters one through three cover topics that are specific to satellites including orbits launchers and spacecraft Chapters four through seven cover the principles of digital communication systems radio frequency communications digital modulation and multiple access techniques and propagation in the earth s atmosphere topics that are common to all radio communication systems Chapters eight through twelve cover applications that include non geostationary satellite systems low throughput systems direct broadcast satellite television Internet access by satellite and global navigation satellite systems The chapter on Internet access by satellite is new to the third edition and each of the chapters has been extensively revised to include the many changes in the field since the publication of the second edition in 2003 Two appendices have been added that cover digital transmission of analog signals and antennas An invaluable resource for students and professionals alike this book Focuses on the fundamental theory of satellite communications Explains the underlying principles and essential mathematics required to understand the physics and engineering of satellite communications Discusses the expansion of satellite communication systems in areas such as direct broadcast satellite TV GPS and internet access Introduces the rapidly advancing field of small satellites referred to as SmallSats or CubeSats Provides relevant practice problems based on real world satellite systems Satellite Communications is required reading for undergraduate and postgraduate students in satellite communications courses and an authoritative reference for engineers working in communications systems and networks and satellite operations and management Subject Guide to Books in Print ,1993 Treatability Manual ,1983

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design

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