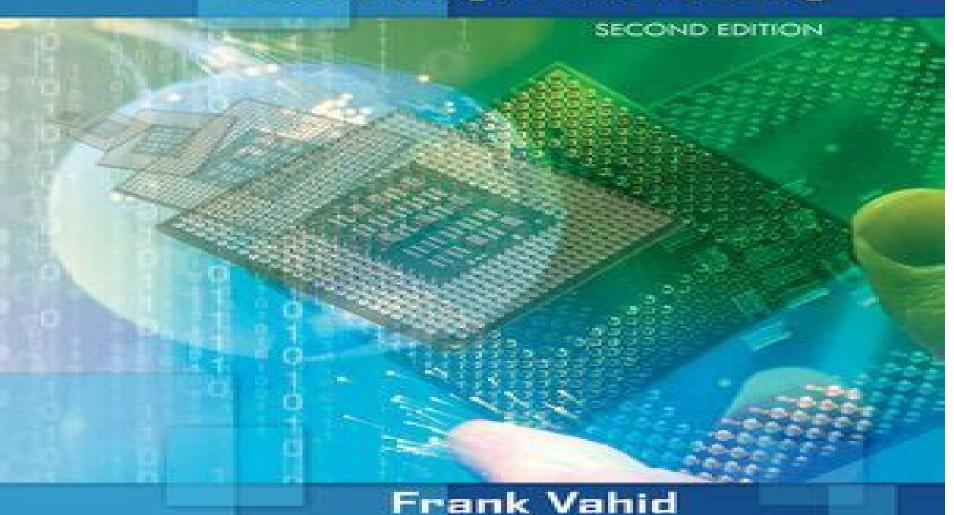
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with RTL Design, VHDL, and Verilog



<u>Digital Systems Design Frank Vahid Solutions Manual</u>

Gregory L. Moss

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digital systems It teaches the skills needed for current industrial digital system design using a hardware description language Verilog and modern CAD tools Particular attention is paid to systemlevel issues including factoring and partitioning digital systems interface design and interface timing Topics needed for a deep understanding of digital circuits such as timing analysis metastability and synchronization are also covered Of course we cover the manual design of combinational and sequential logic circuits However we do not dwell on these topics because there is far more to digital system design than designing such simple modules

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