CHAPTER 1
INTRODUCTION

1.1 An ADD/SUBTRACT logic circuit is shown below. It performs the ADD operation for P = 0, and SUBTRACT for P = 1.

(a) Draw an equivalent CMOS logic diagram by noting that most CMOS gates, except for transmission gate and XOR, are inverting. For example, AND gate is implemented with NAND followed by an inverter.

(b) By using the gate array platform given on page 47 , implement the CMOS circuit as compactly as possible with the aspect ratio, which is the ratio of vertical dimension to horizontal dimension, as close to 1 as possible.

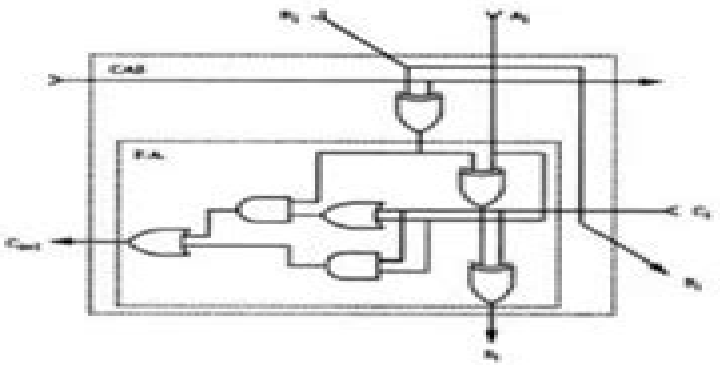

Figure P14.1(a)

SOLUTION:

(b) The CMOS circuit implementation using the gate array platform:

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Tim Piessens,Michiel Steyaert

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Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition Sung-Mo Kang,Yusuf Leblebici,1999 **Design and Analysis of High Efficiency Line Drivers for xDSL** Tim Piessens,Michiel Steyaert,2005-12-30 Design and Analysis of High Efficiency Line Drivers for xDSL covers the most important building block of an xDSL ADSL VDSL system the line driver Traditional Class AB line drivers consume more than 70% of the total power budget of state of the art ADSL modems This book describes the main difficulties in designing line drivers for xDSL The most important specifications are elaborated staring from the main properties of the channel and the signal properties The traditional class AB state of the art class G and future technologies class K are discussed The main part of Design and Analysis of High Efficiency Line Drivers for xDSL describes the design of a novel architecture the Self Oscillating Power Amplifier or SOPA **Building Embedded Systems** Changyi Gu,2016-05-26 Develop the software and hardware you never think about We re talking about the nitty gritty behind the buttons on your microwave inside your thermostat inside the keyboard used to type this description and even running the monitor on which you are reading it now Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level Because yes many people quietly make a successful career doing just that Building embedded systems can be both fun and intimidating Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project Programmers who know software will learn what they need to know about hardware Engineers with hardware knowledge likewise will learn about the software side Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

Analysis and Solutions for Switching Noise Coupling in Mixed-Signal ICs X. Aragones,J.L. Gonzalez,Antonio Rubio,2013-03-09 Modern microelectronic design is characterized by the integration of full systems on a single die These systems often include large high performance digital circuitry high resolution analog parts high driving I O and maybe RF sections Designers of such systems are constantly faced with the challenge to achieve compatibility in electrical characteristics of every section some circuitry presents fast transients and large consumption spikes whereas others require quiet environments to achieve resolutions well beyond millivolts Coupling between those sections is usually unavoidable since the entire system shares the same silicon substrate bulk and the same package Understanding the way coupling is produced and knowing methods to isolate coupled circuitry and how to apply every method is then mandatory knowledge for every IC designer Analysis and Solutions for Switching Noise Coupling in Mixed Signal ICs is an in depth look at coupling through the common silicon substrate and noise at the power supply lines It explains the elementary knowledge needed to understand these phenomena and presents a review of previous works and new research results The aim is to provide an understanding of the reasons for these particular ways of coupling review and suggest solutions to noise coupling and provide criteria to apply noise reduction Analysis and Solutions for Switching Noise Coupling in Mixed Signal ICs is an ideal book both as introductory material to noise coupling problems in mixed signal ICs and for more advanced designers facing this problem *Solutions Manual for Digital Integrated Circuits* Ayers John E,2003-09 **Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation** Vassilis Paliouras,Johan Vounckx,Diederik Verkest,2005-08-25 Welcome to the proceedings of PATMOS 2005 the 15th in a series of international workshops PATMOS2005wasorganizedbyIMECwithtechnicalco sponsorshipfrom the IEEE Circuits and Systems Society Over the years PATMOS has evolved into an important European event where searchers from both industry and academia discuss and investigate the emerging ch lenges in future and contemporary applications design methodologies and tools quired for the developmentof upcominggenerationsof integrated circuits and systems The technical program of PATMOS 2005 contained state of the art technical contri tions three invited talks a special session on hearing aid design and an embedded torial The technical program focused on timing performance and power consumption as well as architectural aspects with particular emphasis on modeling design char terization analysis and optimization in the nanometer era The Technical Program Committee with the assistance of additional expert revi ers selected the 74 papers to be presented at PATMOS The papers were divided into 11 technical sessions and 3 poster sessions As is always the case with the PATMOS workshops the review process was anonymous full papers were required and several reviews were carried out per paper Beyond the presentations of the papers the PATMOS technical program was riched by a series of speeches offered by world class experts on important emerging research issues of industrial relevance Prof Jan Rabaey Berkeley USA gave a talk on Traveling the Wild Frontier of Ulta Low Power Design Dr Sung Bae Park S sung gave a presentation on DVL Deep Low Voltage Circuits and Devices Prof

Wireless Security: Models, Threats, and Solutions Randall K. Nichols,Panos C. Lekkas,2001-11-22 REAL WORLD WIRELESS SECURITY This comprehensive guide catalogs and explains the full range of the security challenges involved in wireless communications Experts Randall K Nichols and Panos C Lekkas lay out the vulnerabilities response options and real world costs connected with wireless platforms and applications Read this book to develop the background and skills to Recognize new and established threats to wireless systems Close gaps that threaten privary profits and customer loyalty Replace temporary fragmented and partial solutions with more robust and durable answers Prepare for the boom in m business Weigh platforms against characteristic attacks and protections Apply clear guidelines for the best solutions now and going forward Assess today s protocol options and compensate for documented shortcomings A COMPREHENSIVE GUIDE TO THE STATE OF THE ART Encryption algorithms you can use now End to end hardware solutions and field programmable gate arrays Speech cryptology Authentication strategies and security protocols for wireless systems Infosec and infowar experience Adding satellites to your security mix **Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits** David A. Hodges,Yu Chen,Horace G. Jackson,1983 **Cryptographic Engineering** Cetin Kaya Koc,2008-12-11 Cryptographic Engineering is the first book that discusses the design techniques and methods The material of this book is scattered in journal and conference articles and authors lecture notes This is a first attempt by top cryptographic engineers to bring this material in a book form and make it available to electrical engineering and computer science students and engineers working for the industry This book is intended for a graduate level course in Cryptographic Engineering to be taught in Electrical Engineering Computer Engineering and Computer Science departments Students will have to have the knowledge of basic cryptographic algorithms before taking this course which will teach them how to design cryptographic hardware FPGA ASIC custom and embedded software to be used in secure systems Additionally engineers working in the industry will be interested in this book to learn how to design cryptographic chips and embedded software Engineers working on the design of cellular phones mobile computing and sensor systems web and enterprise security systems which rely upon cryptographic hardware and software will be interested in this book Essential and advanced design techniques for cryptography will be covered by this book High-Performance Digital VLSI Circuit Design Richard X. Gu,Khaled M. Sharaf,Mohamed I. Elmasry,2012-12-06 High Performance Digital VLSI Circuit Design is the first book devoted entirely to the design of digital high performance VLSI circuits CMOS BiCMOS and bipolar ciruits are covered in depth including state of the art circuit structures Recent advances in both the computer and telecommunications industries demand high performance VLSI digital circuits Digital processing of signals demands high speed circuit techniques for the GHz range The design of such circuits represents a great challenge one that is amplified when the power supply is scaled down to 3 3 V Moreover the requirements of low power high performance circuits adds an extra dimension to the design of such circuits High Performance Digital VLSI Circuit Design is a self contained text introducing the subject of high performance VLSI

circuit design and explaining the speed power tradeoffs The first few chapters of the book discuss the necessary background material in the area of device design and device modeling respectively High performance CMOS circuits are then covered especially the new all N logic dynamic circuits Propagation delay times of high speed bipolar CML and ECL are developed analytically to give a thorough understanding of various interacting process device and circuit parameters High current phenomena of bipolar devices are also addressed as these devices typically operate at maximum currents for limited device area Different new high performance BiCMOS circuits are presented and compared to their conventional counterparts These new circuits find direct applications in the areas of high speed adders frequency dividers sense amplifiers level shifters input output clock buffers and PLLs The book concludes with a few system application examples of digital high performance VLSI circuits Audience A vital reference for practicing IC designers Can be used as a text for graduate and senior undergraduate students in the area **China, India, and East and Southeast Asia: Assessing Sustainability** Ray C. Anderson,Sam Geall,Jingjing Liu,Sony Pellissery,E. N. Anderson,Joel R. Campbell,Joanna I. Lewis,Muhammad Aurang Zeb Mughal,Mark Wilson,2012-11-01 China India and East and Southeast Asia Assessing Sustainability provides unprecedented analyses by regional experts and scholars elsewhere in the world on China India and their neighbors Despite growing demands internally on their natural resources China and India alone are home to more than one third of the world s population the expanding global economic influence of this region makes these countries vital players in a sustainable future for all citizens of the Earth Regional coverage includes topics such as business and commerce environmental and corporate law and lifestyles and values Low Power Design Essentials Jan Rabaey,2009-04-21 Low Power Design Essentials contains all the topics of importance to the low power designer The book lays the foundation with background chapters entitled Advanced MOS Transistors and Their Models and Power Basics These chapters are followed by chapters on the design process including optimization architecture and algorithm level memory run time standby logic and standby memory Chapters on special topics are also included power management and modal design ultra low power and low power design methodology and flows The book concludes with a chapter on case studies as well as a chapter on Projection into the Future These chapters are all based on the extensive amount of teaching that the author has carried out both at universities and companies worldwide All chapters have been drawn up specifically for self study They aim however at different levels of understanding All the chapters start with elementary material but most also contain advanced material **Integrated Circuit and System Design** ,2005 **Binary Decision Diagrams and Applications for VLSI CAD** Shin-ichi Minato,2012-12-06 Symbolic Boolean manipulation using binary decision diagrams BDDs has been successfully applied to a wide variety of tasks particularly in very large scale integration VLSI computer aided design CAD The concept of decision graphs as an abstract representation of Boolean functions dates back to the early work by Lee and Akers In the last ten years BDDs have found widespread use as a concrete data structure for symbolic Boolean manipulation With BDDs functions can be constructed

manipulated and compared by simple and efficient graph algorithms Since Boolean functions can represent not just digital circuit functions but also such mathematical domains as sets and relations a wide variety of CAD problems can be solved using BDDs Binary Decision Diagrams and Applications for VLSI CAD provides valuable information for both those who are new to BDDs as well as to long time aficionados from the Foreword by Randal E Bryant Over the past ten years BDDs have attracted the attention of many researchers because of their suitability for representing Boolean functions They are now widely used in many practical VLSI CAD systems this book can serve as an introduction to BDD techniques and it presents several new ideas on BDDs and their applications many computer scientists and engineers will be interested in this book since Boolean function manipulation is a fundamental technique not only in digital system design but also in exploring various problems in computer science from the Preface by Shin ichi Minato **Digital Integrated Circuits** DeMassa,1996-02-01 Quick-Turnaround ASIC Design in VHDL N. Bouden-Romdhane,Vijay Madisetti,J.W. Hines,2012-12-06 From the Foreword Modern digital signal processing applications provide a large challenge to the system designer Algorithms are becoming increasingly complex and yet they must be realized with tight performance constraints Nevertheless these DSP algorithms are often built from many constituent canonical subtasks e g IIR and FIR filters FFTs that can be reused in other subtasks Design is then a problem of composing these core entities into a cohesive whole to provide both the intended functionality and the required performance In order to organize the design process there have been two major approaches The top down approach starts with an abstract concise functional description which can be quickly generated On the other hand the bottom up approach starts from a detailed low level design where performance can be directly assessed but where the requisite design and interface detail take a long time to generate In this book the authors show a way to effectively resolve this tension by retaining the high level conciseness of VHDL while parameterizing it to get good fit to specific applications through reuse of core library components Since they build on a pre designed set of core elements accurate area speed and power estimates can be percolated to high level design routines which explore the design space Results are impressive and the cost model provided will prove to be very useful Overall the authors have provided an up to date approach doing a good job at getting performance out of high level design The methodology provided makes good use of extant design tools and is realistic in terms of the industrial design process The approach is interesting in its own right but is also of direct utility and it will give the existing DSP CAD tools a highly competitive alternative The techniques described have been developed within ARPAs RASSP Rapid Prototyping of Application Specific SignalProcessors project and should be of great interest there as well as to many industrial designers Professor Jonathan Allen Massachusetts Institute of Technology Advanced Concepts in Adaptive Signal Processing W. Kenneth Jenkins,Andrew W. Hull,Jeffrey C. Strait,Bernard A. Schnaufer,Xiaohui Li,2012-12-06 Although adaptive filtering and adaptive array processing began with research and development efforts in the late 1950 s and early 1960 s it was not until the publication of the pioneering books

by Honig and Messerschmitt in 1984 and Widrow and Stearns in 1985 that the field of adaptive signal processing began to emerge as a distinct discipline in its own right Since 1984 many new books have been published on adaptive signal processing which serve to define what we will refer to throughout this book as conventional adaptive signal processing These books deal primarily with basic architectures and algorithms for adaptive filtering and adaptive array processing with many of them emphasizing practical applications Most of the existing textbooks on adaptive signal processing focus on finite impulse response FIR filter structures that are trained with strategies based on steepest descent optimization or more precisely the least mean square LMS approximation to steepest descent While literally hundreds of archival research papers have been published that deal with more advanced adaptive filtering concepts none of the current books attempt to treat these advanced concepts in a unified framework The goal of this new book is to present a number of important but not so well known topics that currently exist scattered in the research literature The book also documents some new results that have been conceived and developed through research conducted at the University of Illinois during the past five years

Software Synthesis from Dataflow Graphs Shuvra S. Bhattacharyya,Praveen K. Murthy,Edward A. Lee,2012-12-06 Software Synthesis from Dataflow Graphs addresses the problem of generating efficient software implementations from applications specified as synchronous dataflow graphs for programmable digital signal processors DSPs used in embedded real time systems The advent of high speed graphics workstations has made feasible the use of graphical block diagram programming environments by designers of signal processing systems A particular subset of dataflow called Synchronous Dataflow SDF has proven efficient for representing a wide class of unirate and multirate signal processing algorithms and has been used as the basis for numerous DSP block diagram based programming environments such as the Signal Processing Workstation from Cadence Design Systems Inc COSSAP from Synopsys both commercial tools and the Ptolemy environment from the University of California at Berkeley A key property of the SDF model is that static schedules can be determined at compile time This removes the overhead of dynamic scheduling and is thus useful for real time DSP programs where throughput requirements are often severe Another constraint that programmable DSPs for embedded systems have is the limited amount of on chip memory Off chip memory is not only expensive but is also slower and increases the power consumption of the system hence it is imperative that programs fit in the on chip memory whenever possible Software Synthesis from Dataflow Graphs reviews the state of the art in constructing static memory optimal schedules for programs expressed as SDF graphs Code size reduction is obtained by the careful organization of loops in the target code Data buffering is optimized by constructing the loop hierarchy in provably optimal ways for many classes of SDF graphs The central result is a uniprocessor scheduling framework that provably synthesizes the most compact looping structures called singleappearance schedules for a certain class of SDF graphs In addition algorithms and heuristics are presented that generate single appearance schedules optimized for data buffering usage Numerous practical examples and extensive

experimental data are provided to illustrate the efficacy of these techniques *Automatic Speech and Speaker Recognition* Chin-Hui Lee,Frank K. Soong,Kuldip K. Paliwal,2012-12-06 Research in the field of automatic speech and speaker recognition has made a number of significant advances in the last two decades influenced by advances in signal processing algorithms architectures and hardware These advances include the adoption of a statistical pattern recognition paradigm the use of the hidden Markov modeling framework to characterize both the spectral and the temporal variations in the speech signal the use of a large set of speech utterance examples from a large population of speakers to train the hidden Markov models of some fundamental speech units the organization of speech and language knowledge sources into a structural finite state network and the use of dynamic programming based heuristic search methods to find the best word sequence in the lexical network corresponding to the spoken utterance Automatic Speech and Speaker Recognition Advanced Topics groups together in a single volume a number of important topics on speech and speaker recognition topics which are of fundamental importance but not yet covered in detail in existing textbooks Although no explicit partition is given the book is divided into five parts Chapters 1 2 are devoted to technology overviews Chapters 3 12 discuss acoustic modeling of fundamental speech units and lexical modeling of words and pronunciations Chapters 13 15 address the issues related to flexibility and robustness Chapter 16 18 concern the theoretical and practical issues of search Chapters 19 20 give two examples of algorithm and implementational aspects for recognition system realization Audience A reference book for speech researchers and graduate students interested in pursuing potential research on the topic May also be used as a text for advanced courses on the subject Forthcoming Books Rose Arny,2003

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