

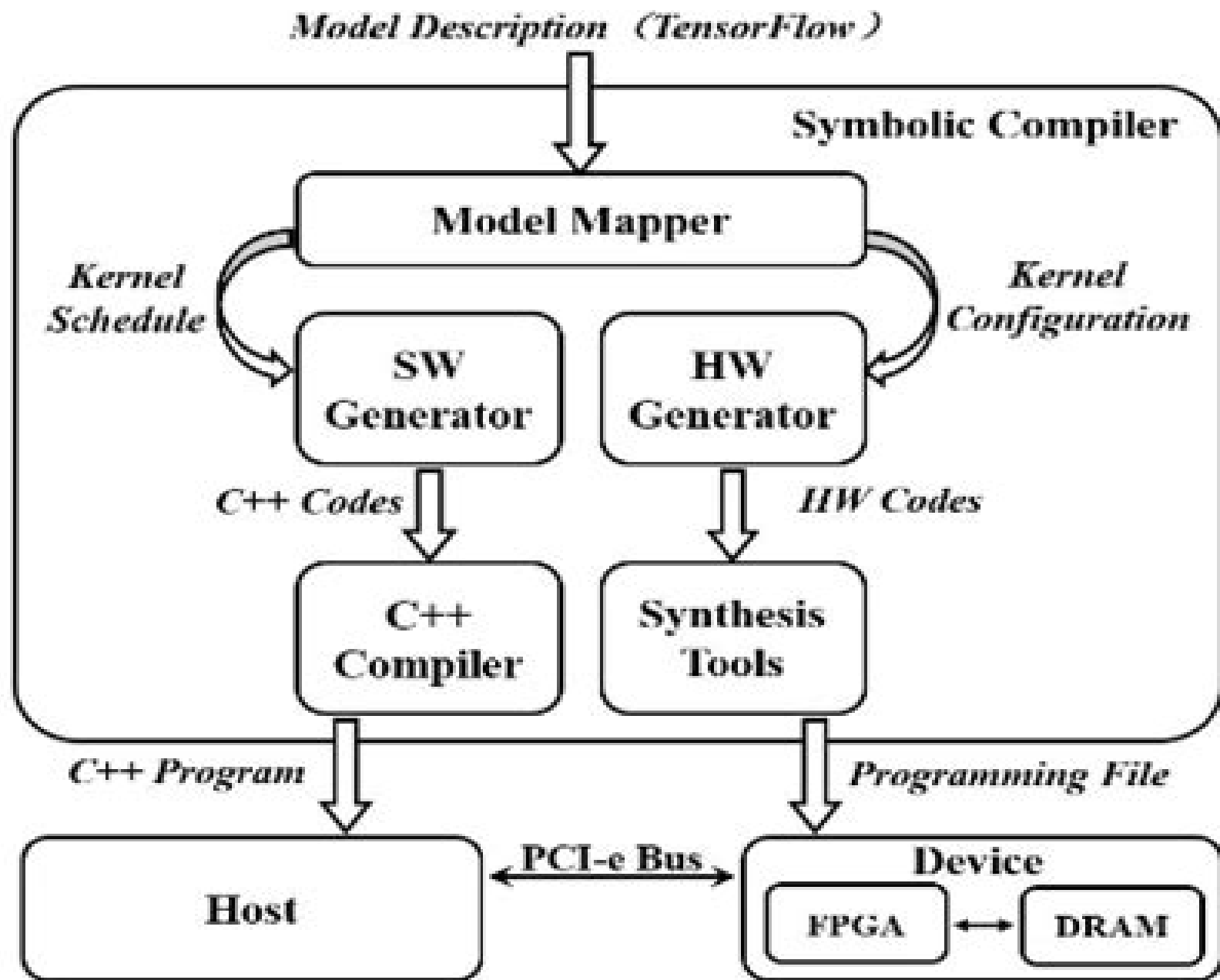


Fig. 1: FP-DNN Framework

Fp Dnn An Automated Framework For Mapping Deep Neural

**Steven Derrien, Frank Hannig, Pedro C.
Diniz, Daniel Chillet**



Fp Dnn An Automated Framework For Mapping Deep Neural:

Embedded Computer Systems: Architectures, Modeling, and Simulation Luigi Carro,Francesco

Regazzoni,Christian Pilato,2025-01-27 The two volume set LNCS 15226 and 15227 constitutes the refereed proceedings of the 24th International Conference on Embedded Computer Systems Architectures Modeling and Simulation SAMOS 2024 held in Samos Greece during June 29 July 4 2024 The 24 full papers 10 invited full papers and 4 poster papers included in the proceedings were carefully reviewed and selected from 57 submissions They deal with embedded computer systems focusing on their applications architectures and underlying processors as well as methods created to automate their design

Robotic Computing on FPGAs Shaoshan Liu,Zishen Wan,Bo Yu,Yu Wang,2022-05-31 This book provides a thorough overview of the state of the art field programmable gate array FPGA based robotic computing accelerator designs and summarizes their adopted optimized techniques This book consists of ten chapters delving into the details of how FPGAs have been utilized in robotic perception localization planning and multi robot collaboration tasks In addition to individual robotic tasks this book provides detailed descriptions of how FPGAs have been used in robotic products including commercial autonomous vehicles and space exploration robots

Innovations in Smart Cities Applications Volume 5 Mohamed Ben Ahmed,Anouar Abdelhakim Boudhir,İsmail Rakıp Karas,Vipul Jain,Sehl Mellouli,2022-03-03 This book sets the innovative research contributions works and solutions for almost all the intelligent and smart applications in the smart cities The smart city concept is a relevant topic for industrials governments and citizens Due to this the smart city considered as a multi domain context attracts tremendously academics researchers and practitioners who provide efforts in theoretical proofs approaches architectures and in applied researches The importance of smart cities comes essentially from the significant growth of populations in the near future which conducts to a real need of smart applications that can support this evolution in the future cities The main scope of this book covers new and original ideas for the next generations of cities using the new technologies The book involves the application of the data science and AI IoT technologies and architectures smart earth and water management smart education and E learning systems smart modeling systems smart mobility and renewable energy It also reports recent research works on big data technologies image processing and recognition systems and smart security and privacy

Network and Parallel Computing Shaoshan Liu,Xiaohui Wei,2022-11-30 This book constitutes the proceedings of the 19th IFIP WG 10 3 International Conference on Network and Parallel Computing NPC 2022 which was held in Jinan China during September 24 25 2022 The 23 full papers and 8 short papers presented in this volume were carefully reviewed and selected from 89 submissions They were organized in topical sections as follows computer architecture cloud computing deep learning emerging applications and storage and IO

Applied Reconfigurable Computing. Architectures, Tools, and Applications Fernando Rincón,Jesús Barba,Hayden K. H. So,Pedro Diniz,Julián

Caba,2020-03-25 This book constitutes the proceedings of the 16th International Symposium on Applied Reconfigurable

Computing ARC 2020 held in Toledo Spain in April 2020 The 18 full papers and 11 poster presentations presented in this volume were carefully reviewed and selected from 40 submissions The papers are organized in the following topical sections design methods design space exploration high level synthesis architectures applications **Applied Reconfigurable**

Computing. Architectures, Tools, and Applications Steven Derrien, Frank Hannig, Pedro C. Diniz, Daniel

Chillet, 2021-06-23 This book constitutes the proceedings of the 17th International Symposium on Applied Reconfigurable Computing ARC 2021 held as a virtual event in June 2021 The 14 full papers and 11 short presentations presented in this

volume were carefully reviewed and selected from 40 submissions The papers cover a broad spectrum of applications of reconfigurable computing from driving assistance data and graph processing acceleration computer security to the societal relevant topic of supporting early diagnosis of Covid infectious conditions **Proceedings of Ninth International**

Congress on Information and Communication Technology Xin-She Yang, Simon Sherratt, Nilanjan Dey, Amit

Joshi, 2024-07-29 This book gathers selected high quality research papers presented at the Ninth International Congress on Information and Communication Technology held in London on February 19 22 2024 It discusses emerging topics pertaining to information and communication technology ICT for managerial applications e governance e agriculture e education and computing technologies the Internet of Things IoT and e mining Written by respected experts and researchers working on ICT the book offers an asset for young researchers involved in advanced studies The work is presented in ten volumes

Techniques for Mapping Deep Neural Network Frameworks to Programmable Accelerators Stefan Hadjis, 2021

The trend towards increasing specialization in DNN accelerators is first discussed as well as why FPGA hardware is sometimes selected The two major ways that DNN applications can be automatically mapped to FPGAs are then reviewed 1 mapping to manually optimized template designs or overlay architectures which is suited to DNN frameworks as a mapping source and 2 mapping by compiling automatically designed hardware Next an open source end to end toolchain to map TensorFlow DNNs to cloud FPGAs is described which is the first open source toolchain to use a modern DNN framework as a starting point and either 1 target public cloud FPGA hardware or 2 compile DNNs reaching state of the art accuracy on an FPGA cloud or not This compiler is used to explore tradeoffs in DNN to FPGA mapping including tensor storage format and architecture specialization and to examine how different layer dimensions and other characteristics such as locality affect design decisions Next optimizations to improve circuits automatically designed by hardware compilation tools and DSLs are investigated An algorithm for high level hardware compilers is presented which reduces resource utilization for on chip memory accesses common in DNNs and computer vision Its applicability to general dense access patterns and applications is also demonstrated For each of these observations generalization is made beyond DNN or ML domains and examples are shown where increasing specialization or heterogeneity in storage formats processor architecture and on chip data structures can improve FPGA accelerator resource utilization timing closure and bandwidth requirements Framework for

Mapping Convolutional Neural Networks on FPGAs Masoud Shahshahani, 2021 Artificial Intelligence AI applications are on the rise. Recent advances in machine learning and deep learning have created various applications for medicine, healthcare, financial markets, security, entertainment, and social sciences. Deep Learning especially has demonstrated tremendous opportunities in computer vision, autonomous driving, natural language processing, and many more. Deep learning allows machines to solve complex problems using Artificial Neural Networks (ANNs) and the learning itself can be supervised or semisupervised. Multilayered artificial neural networks are called Deep Neural Networks (DNNs). These deep computational models are composed of multiple sequentially processing layers that help learn the representations within a given data set. Convolutional Neural Networks (CNN) are a particular class of deep networks that use convolution to extract features from usually a time domain or frequency domain data and then use the extracted features to classify that data for final inferencing. Several software tools and frameworks are available to facilitate the deep learning community with the fast development and high performance execution of DNNs. Tool flows such as PyTorch, Caffe, Theano, and TensorFlow aim to increase the productivity of CNN software developers by providing a pathway for implementing deep networks on high performance multi-core CPUs, GPUs, and DSPs. GPUs especially provide easy access to floating point operations and also allow very high memory bandwidths. Some of the latest Nvidia GPUs, Nvidia GeForce RTX2080, consume as much as 300 watts of power. Excessive power dissipation can make GPUs an unfavorable candidate for implementing CNNs for a variety of applications. Field Programmable Gate Arrays (FPGAs) provide a high degree of customized parallelization and offer far superior performance per watt. We believe that FPGA-based accelerators are ideal platforms for implementing Convolutional Neural Networks for computer vision and related applications. Software engineers with minimal hardware design skills demand tremendous support within the tool flows, and FPGA vendors are fully embracing new methodologies like high level synthesis where the designs can be described as a program written in languages like C/C++. However, commercial FPGAs are resource scarce; the CNN mapping design space is enormous, and efficient mapping of CNN can quickly become a challenging task. The requirement of FPGA resources, latency, and power is affected by many parameters, including the CNN architecture and the level of computational parallelism. In practice, a software designer first explores various CNN architectures in software to improve architecture validation accuracy. Once an architecture has been finalized, the designer ports the architecture design to FPGA for inference acceleration. The mapping process undergoes performance optimization by tweaking many design-related parameters during the design space exploration and changing the operating frequencies. The entire process is highly time-consuming. This dissertation describes a fully automated end-to-end design framework for implementing CNNs on FPGAs. The framework allows a designer to express the CNNs in commonly preferred Python language descriptions and provides a guided tool flow to generate a custom Intellectual Property (IP) block. In addition, the framework allows easy and complete exploration for selecting final design implementations based on optimization parameters that include Performance

Power and Area PPA **Efficient Processing of Deep Neural Networks** Vivienne Sze,Yu-Hsin Chen,Tien-Ju Yang,Joel S. Emer,2022-05-31 This book provides a structured treatment of the key principles and techniques for enabling efficient processing of deep neural networks DNNs DNNs are currently widely used for many artificial intelligence AI applications including computer vision speech recognition and robotics While DNNs deliver state of the art accuracy on many AI tasks it comes at the cost of high computational complexity Therefore techniques that enable efficient processing of deep neural networks to improve key metrics such as energy efficiency throughput and latency without sacrificing accuracy or increasing hardware costs are critical to enabling the wide deployment of DNNs in AI systems The book includes background on DNN processing a description and taxonomy of hardware architectural approaches for designing DNN accelerators key metrics for evaluating and comparing different designs features of DNN processing that are amenable to hardware algorithm co design to improve energy efficiency and throughput and opportunities for applying new technologies Readers will find a structured introduction to the field as well as formalization and organization of key concepts from contemporary work that provide insights that may spark new ideas **Deep Neural Network Applications** Hasmik Osipyan,Bosedede Iyiade Edwards,Adrian David Cheok,2022-04-27 The world is on the verge of fully ushering in the fourth industrial revolution of which artificial intelligence AI is the most important new general purpose technology Like the steam engine that led to the widespread commercial use of driving machineries in the industries during the first industrial revolution the internal combustion engine that gave rise to cars trucks and airplanes electricity that caused the second industrial revolution through the discovery of direct and alternating current and the Internet which led to the emergence of the information age AI is a transformational technology It will cause a paradigm shift in the way s problems are solved in every aspect of our lives and from it innovative technologies will emerge AI is the theory and development of machines that can imitate human intelligence in tasks such as visual perception speech recognition decision making and human language translation This book provides a complete overview on the deep learning applications and deep neural network architectures It also gives an overview on most advanced future looking fundamental research in deep learning application in artificial intelligence Research overview includes reasoning approaches problem solving knowledge representation planning learning natural language processing perception motion and manipulation social intelligence and creativity It will allow the reader to gain a deep and broad knowledge of the latest engineering technologies of AI and Deep Learning and is an excellent resource for academic research and industry applications *Deep Neural Networks in a Mathematical Framework* Anthony L. Caterini,Dong Eui Chang,2018-03-22 This SpringerBrief describes how to build a rigorous end to end mathematical framework for deep neural networks The authors provide tools to represent and describe neural networks casting previous results in the field in a more natural light In particular the authors derive gradient descent algorithms in a unified way for several neural network structures including multilayer perceptrons convolutional neural networks deep autoencoders and recurrent neural networks

Furthermore the authors developed framework is both more concise and mathematically intuitive than previous representations of neural networks This SpringerBrief is one step towards unlocking the black box of Deep Learning The authors believe that this framework will help catalyze further discoveries regarding the mathematical properties of neural networks This SpringerBrief is accessible not only to researchers professionals and students working and studying in the field of deep learning but also to those outside of the neural network community

A Framework for FPGA-based Acceleration of Neural Network Inference with Limited Numerical Precision Via High-level Synthesis with Streaming Functionality Ruo Long Lian,2016

Deep Learning and Practice with MindSpore Lei Chen,2021-08-17

This book systematically introduces readers to the theory of deep learning and explores its practical applications based on the MindSpore AI computing framework Divided into 14 chapters the book covers deep learning deep neural networks DNNs convolutional neural networks CNNs recurrent neural networks RNNs unsupervised learning deep reinforcement learning automated machine learning device cloud collaboration deep learning visualization and data preparation for deep learning To help clarify the complex topics discussed this book includes numerous examples and links to online resources

Multi-faceted Deep Learning Jenny Benois-Pineau,Akka Zemhari,2021-10-20 This book covers a large set of methods in the field of Artificial Intelligence Deep Learning applied to real world problems The fundamentals of the Deep Learning approach and different types of Deep Neural Networks DNNs are first summarized in this book which offers a comprehensive preamble for further problem oriented chapters The most interesting and open problems of machine learning in the framework of Deep Learning are discussed in this book and solutions are proposed This book illustrates how to implement the zero shot learning with Deep Neural Network Classifiers which require a large amount of training data The lack of annotated training data naturally pushes the researchers to implement low supervision algorithms Metric learning is a long term research but in the framework of Deep Learning approaches it gets freshness and originality Fine grained classification with a low inter class variability is a difficult problem for any classification tasks This book presents how it is solved by using different modalities and attention mechanisms in 3D convolutional networks Researchers focused on Machine Learning Deep learning Multimedia and Computer Vision will want to buy this book Advanced level students studying computer science within these topic areas will also find this book useful

Deep Neural Evolution Hitoshi Iba,Nasimul Noman,2020-05-21

This book delivers the state of the art in deep learning DL methods hybridized with evolutionary computation EC Over the last decade DL has dramatically reformed many domains computer vision speech recognition healthcare and automatic game playing to mention only a few All DL models using different architectures and algorithms utilize multiple processing layers for extracting a hierarchy of abstractions of data Their remarkable successes notwithstanding these powerful models are facing many challenges and this book presents the collaborative efforts by researchers in EC to solve some of the problems in DL EC comprises optimization techniques that are useful when problems are complex or poorly understood or insufficient

information about the problem domain is available This family of algorithms has proven effective in solving problems with challenging characteristics such as non convexity non linearity noise and irregularity which dampen the performance of most classic optimization schemes Furthermore EC has been extensively and successfully applied in artificial neural network ANN research from parameter estimation to structure optimization Consequently EC researchers are enthusiastic about applying their arsenal for the design and optimization of deep neural networks DNN This book brings together the recent progress in DL research where the focus is particularly on three sub domains that integrate EC with DL 1 EC for hyper parameter optimization in DNN 2 EC for DNN architecture design and 3 Deep neuroevolution The book also presents interesting applications of DL with EC in real world problems e g malware classification and object detection Additionally it covers recent applications of EC in DL e g generative adversarial networks GAN training and adversarial attacks The book aims to prompt and facilitate the research in DL with EC both in theory and in practice *Automated Feature Engineering for Deep Neural Networks with Genetic Programming* Jeff Heaton, 2017

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