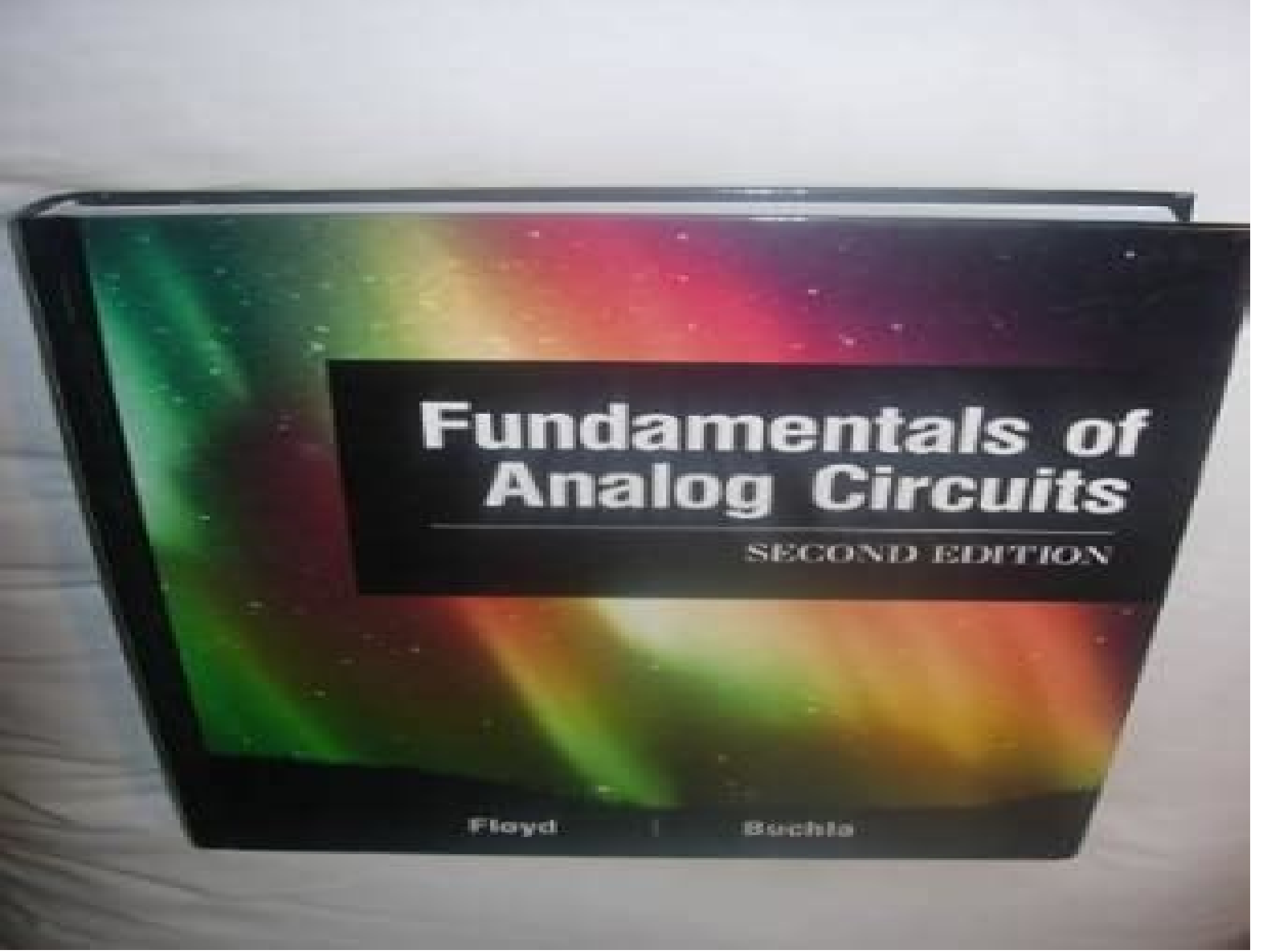




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and interconnection analysis methods The review in Chapter 2 gives an understanding of the advantages and disadvantages of the many techniques applied in modern digital macromodels The book also presents a wide variety of techniques for performing nonlinear macromodeling of digital MOS subcircuits which address a large number of shortcomings in existing digital MOS macromodels Specifically the techniques address the device model detail transistor coupling capacitance effective channel length modulation series transistor reduction effective transconductance input terminal dependence gate parasitic capacitance the body effect the impact of parasitic RC interconnects and the effect of transmission gates The techniques address major sources of errors in existing macromodeling techniques which must be addressed if macromodeling is to be accepted in commercial CAD tools by chip designers The techniques presented in Chapters 4 6 can be implemented in other macromodels and are demonstrated using the macromodel presented in Chapter 3 The new techniques are validated over an extremely wide range of operating conditions much wider than has been presented for previous macromodels thus demonstrating the wide range of applicability of these techniques

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Analog Circuits Esteban Tlelo-Cuautle, Mourad Fakhfakh, Luis Gerardo de la Fraga, 2017 This book includes recent research that focuses on analog integrated circuits and covers three main topics namely fundamentals synthesis and performance Eleven chapters are divided among these three topics as follows Chapters One to Four are a part of fundamentals The first chapter The Next Generation of Nanomaterials for Designing Analog Integrated Circuits describes new directions for applying nanomaterials for the design of modern analog circuits Chapter Two Application of Nullors in Designing Analog Circuits for Frequency Bandwidth uses the pathological circuit element known as a nullor to design analog integrated circuits with frequency specifications to accomplish a desired bandwidth Chapter Three RC and RL to LC Circuit Conversion and its Application in Poles and Zeros Identification details an important property from circuit theory to estimate roots by performing conversions of passive elements Chapter Four Enhanced and Improved Symbolic Circuit Analysis Using MATLAB

relays the development of symbolic circuit analysis and focuses on enhancing an already developed symbolic tool to allow the symbolic analysis of large circuits The synthesis of analog integrated circuits has been a challenge because there is no way to establish general rules to cover the gap between the behavioral and transistor circuit levels of abstraction In this book the second topic includes four chapters from Five to Eight Chapter Five On the Synthesis of Sinusoidal Oscillators Using Nullors just as in Chapter Two uses the pathological circuit element known as a nullor to perform the synthesis of sinusoidal oscillators which are quite useful in many electronic systems Other kinds of oscillators are described in Chapter Six Synthesis of SRCOs and Multi Phase Oscillators from State Variables to their Implementation Using CMOS IC Technology where the synthesis process identifies the resistor that controls the oscillating frequency and applies a state variable approach Chapter Seven Evolutionary Optimisation in the Design of CMOS Analog Integrated Circuits shows the application of heuristics for circuit optimisation and how it can be extended to bigger analog integrated circuits Chapter Eight provides details on the synthesis and design of a CMOS harmonic mixer with output power management for narrowband and wideband wireless communications the Bluetooth and UWB cases The third part of this book is devoted to analog circuit performances and includes three chapters Chapter Nine details the FPGA realisation of radio frequency RF power amplifier models In this case the system is modeled in the analog domain and implemented in the digital one Chapter Ten White Box Models of Optimal Sized Solutions of Analog Integrated Circuits generates analytical expressions for modeling the dominant behavior of CMOS analog circuits Finally Chapter Eleven Radial Basis Function Surrogate Modeling for the Accurate Design of Analog Circuits applies modern modeling approaches to accomplish real target specifications and to improve the design of reliable circuits

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