

DESIGN AUTOMATION EMBEDDED SYSTEMS

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Youn-Long Steve Lin



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Electronic Design Automation for IC System Design, Verification, and Testing Luciano Lavagno,Igor L. Markov,Grant Martin,Louis K. Scheffer,2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals

System Design Automation Renate Merker,Wolfgang Schwarz,2013-03-09 Design automation of electronic and hybrid systems is a steadily growing field of interest and a permanent challenge for researchers in Electronics Computer Engineering and Computer Science System Design Automation presents some recent results in design automation of different types of electronic and mechatronic systems It deals with various topics of design automation ranging from high level digital system synthesis through analogue and heterogeneous system analysis and design up to system modeling and simulation Design automation is treated from the aspects of its theoretical fundamentals its basic approach and its methods and tools Several application cases are presented in detail The book consists of three chapters High Level System Synthesis Digital Hardware Software Systems Here embedded systems distributed systems and processor arrays as well as hardware software codesign are treated Also three special application cases are discussed in detail Analog and Heterogeneous System Design System Approach and Methodology This chapter copes with the analysis and design of hybrid systems comprised of analog and digital electronic and mechanical components System Simulation and Evaluation Methods and Tools In this chapter object oriented Modelling analog system simulation including fault simulation parameter optimization and system validation are regarded The contents of the book are based on material presented at the Workshop System Design Automation SDA 2000 organised by the Sonderforschungsbereich 358 of the Deutsche Forschungsgemeinschaft at TU Dresden

Model-Based Design for Embedded Systems Gabriela Nicolescu,Pieter J. Mosterman,2018-09-03 The demands of increasingly complex

embedded systems and associated performance computations have resulted in the development of heterogeneous computing architectures that often integrate several types of processors analog and digital electronic components and mechanical and optical components all on a single chip As a result now the most prominent challenge for the design automation community is to efficiently plan for such heterogeneity and to fully exploit its capabilities A compilation of work from internationally renowned authors Model Based Design for Embedded Systems elaborates on related practices and addresses the main facets of heterogeneous model based design for embedded systems including the current state of the art important challenges and the latest trends Focusing on computational models as the core design artifact this book presents the cutting edge results that have helped establish model based design and continue to expand its parameters The book is organized into three sections Real Time and Performance Analysis in Heterogeneous Embedded Systems Design Tools and Methodology for Multiprocessor System on Chip and Design Tools and Methodology for Multidomain Embedded Systems The respective contributors share their considerable expertise on the automation of design refinement and how to relate properties throughout this refinement while enabling analytic and synthetic qualities They focus on multi core methodological issues real time analysis and modeling and validation taking into account how optical electronic and mechanical components often interface Model based design is emerging as a solution to bridge the gap between the availability of computational capabilities and our inability to make full use of them yet This approach enables teams to start the design process using a high level model that is gradually refined through abstraction levels to ultimately yield a prototype When executed well model based design encourages enhanced performance and quicker time to market for a product Illustrating a broad and diverse spectrum of applications such as in the automotive aerospace health care consumer electronics this volume provides designers with practical readily adaptable modeling solutions for their own practice

Objects, Components, Models and Patterns Manuel Oriol, Bertrand Meyer, 2009-06-29 This book constitutes the thoroughly refereed proceedings of the 47th International Conference on Objects Components Models and Patterns TOOLS EUROPE 2009 held in Zurich Switzerland in June July 2009 TOOLS has played a major role in the spread of object oriented and component technologies It has now broadened its scope beyond the original topics of object technology and component based development to encompass all modern practical approaches to software development At the same time TOOLS has kept its traditional spirit of technical excellence its acclaimed focus on practicality its well proven combination of theory and applications and its reliance on the best experts from academia and industry The 17 regular papers and two short papers presented in this book together with two invited papers were carefully reviewed and selected from 67 submissions The topics covered in this volume are reflection and aspects models theory components monitoring and systems generation

Energy-Efficient Fault-Tolerant Systems Jimson Mathew, Rishad A. Shafik, Dhiraj K. Pradhan, 2013-09-07 This book describes the state of the art in energy efficient fault tolerant embedded systems It covers the entire product lifecycle of electronic systems design analysis and testing and

includes discussion of both circuit and system level approaches Readers will be enabled to meet the conflicting design objectives of energy efficiency and fault tolerance for reliability given the up to date techniques presented

EDA for IC System Design, Verification, and Testing Louis Scheffer,Luciano Lavagno,Grant Martin,2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set

Low-Power Variation-Tolerant Design in Nanometer Silicon Swarup Bhunia,Saibal Mukhopadhyay,2010-11-10 Design considerations for low power operations and robustness with respect to variations typically impose contradictory requirements Low power design techniques such as voltage scaling dual threshold assignment and gate sizing can have large negative impact on parametric yield under process variations This book focuses on circuit architectural design techniques for achieving low power operation under parameter variations We consider both logic and memory design aspects and cover modeling and analysis as well as design methodology to achieve simultaneously low power and variation tolerance while minimizing design overhead This book will discuss current industrial practices and emerging challenges at future technology nodes

Behavioral Modeling for Embedded Systems and Technologies: Applications for Design and Implementation Gomes, Luis,Fernandes, Jo?o M.,2009-07-31 This book provides innovative behavior models currently used for developing embedded systems accentuating on graphical and visual notations Provided by publisher

Job Scheduling Strategies for Parallel Processing Dalibor Klusáček,Julita Corbalán,Gonzalo P. Rodrigo,2024-12-20 This book constitutes the refereed proceedings of the 27th International Workshop on Job Scheduling Strategies for Parallel Processing JSSPP 2024 held in San Francisco CA USA on May 31 2024 The 10 full papers included in this book were carefully reviewed and selected from 15 submissions The JSSPP 2024 covers several interesting problems within the resource management and scheduling domains

Integrated Formal Methods Judi M.T. Romijn,Graeme P. Smith,Jaco C. van de Pol,2005-11-24 This book constitutes the refereed proceedings of the 5th International Conference on Integrated Formal Methods IFM 2005 held in Eindhoven The Netherlands in November December 2005 The 19 revised full papers presented together with 3 invited papers were carefully reviewed and selected from 40 submissions The papers are organized in topical sections on components state event based verification system development applications of B tool support non software domains semantics as well as UML and statecharts

Variation-Aware Design of Custom Integrated Circuits: A Hands-on Field Guide Trent McConaghy,Kristopher Breen,Jeffrey Dyck,Amit Gupta,2012-09-28 This book targets custom IC designers who are encountering variation issues in

their designs especially for modern process nodes at 45nm and below such as statistical process variations environmental variations and layout effects It teaches them the state of the art in Variation Aware Design tools which help the designer to analyze quickly the variation effects identify the problems and fix the problems Furthermore this book describes the algorithms and algorithm behavior performance limitations which is of use to designers considering these tools designers using these tools CAD researchers and CAD managers Design of Hardware/Software Embedded Systems Eugenio Villar Bonet,2001 Este libro presenta los desaf os planteados por las nuevas y sumamente poderosas tecnolog as de integraci n de sistemas electr nicos que est n en la base de los cambios sociales hacia lo que llaman la Sociedad de la Informaci n en la que los dispositivos electr nicos se har n una parte incorporada de la vida diaria encajados en casi cada producto Es necesario un conocimiento cuidadoso de los desaf os para aprovechar la amplia gama de ocasiones ofrecidas por tales capacidades de integraci n y las correspondientes posibilidades de dise o de sistemas electr nicos *Embedded Systems Handbook 2-Volume Set* Richard Zurawski,2018-10-08 During the past few years there has been an dramatic upsurge in research and development implementations of new technologies and deployments of actual solutions and technologies in the diverse application areas of embedded systems These areas include automotive electronics industrial automated systems and building automation and control Comprising 48 chapters and the contributions of 74 leading experts from industry and academia the Embedded Systems Handbook Second Edition presents a comprehensive view of embedded systems their design verification networking and applications The contributors directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews exploring new developments deployments and trends To accommodate the tremendous growth in the field the handbook is now divided into two volumes New in This Edition Processors for embedded systems Processor centric architecture description languages Networked embedded systems in the automotive and industrial automation fields Wireless embedded systems Embedded Systems Design and Verification Volume I of the handbook is divided into three sections It begins with a brief introduction to embedded systems design and verification The book then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Networked Embedded Systems Volume II focuses on selected application areas of networked embedded systems It covers automotive field industrial automation building automation and wireless sensor networks This volume highlights implementations in fast evolving areas which have not received proper coverage in other publications Reflecting the unique functional requirements of different application areas the contributors discuss inter node communication aspects in the context of specific applications of networked embedded systems **Languages for Embedded Systems and their Applications** Martin Radetzki,2009-05-24 Embedded systems take over complex control and data processing tasks in diverse application elds such

as automotive avionics consumer products and telecommunications They are the primary driver for improving overall system safety efficiency and comfort The demand for further improvement in these aspects can only be satisfied by designing embedded systems of increasing complexity which in turn necessitates the development of new system design methodologies based on specification design and verification languages The objective of the book at hand is to provide researchers and designers with an overview of current research trends results and application experiences in computer languages for embedded systems The book builds upon the most relevant contributions to the 2008 conference Forum on Design Languages FDL the premier international conference specializing in this field These contributions have been selected based on the results of reviews provided by leading experts from research and industry In many cases the authors have improved their original work by adding breadth depth or explanation

Information and Software Technologies Audrius Lopata, Daina Gudonienė, Rita Butkienė, 2021-10-08 This book constitutes the refereed proceedings of the 27th International Conference on Information and Software Technologies ICIST 2021 held in Kaunas Lithuania in October 2021 The 24 full papers and 6 short papers presented were carefully reviewed and selected from 79 submissions The papers discuss such topics as business intelligence for information and software systems intelligent methods for data analysis and computer aided software engineering information technology applications smart e learning technologies and applications language technologies

Designing Embedded Systems with the SIGNAL Programming Language Abdoulaye Gamatié, 2009-10-06 I am very pleased to play even a small part in the publication of this book on the SIGNAL language and its environment POLYCHRONY I am sure it will be a significant milestone in the development of the SIGNAL language of synchronous computing in general and of the data flow approach to computation In data flow the computation takes place in a producer consumer network of dependent processing stations Data travels in streams and is transformed as these streams pass through the processing stations often called lters Data flow is an attractive model for many reasons not least because it corresponds to the way production transportation and communication are typically organized in the real world outside cyberspace I myself stumbled into data flow almost against my will In the mid 1970s Ed Ashcroft and I set out to design a super structured programming language that we hoped would radically simplify proving assertions about programs In the end we decided that it had to be declarative However we also were determined that iterative algorithms could be expressed directly without circumlocutions such as the use of a tail recursive function The language that resulted which we named LUCID was much less traditional than we would have liked LUCID statements are equations in a kind of executable temporal logic that specify the time sequences of variables involved in an iteration

Perspectives of Systems Informatics Edmund Clarke, Irina Virbitskaite, Andrei Voronkov, 2012-05-13 This book contains thoroughly refereed and revised papers from the 8th International Andrei Ershov Memorial Conference on Perspectives of System Informatics PSI 2011 held in Akademgorodok Novosibirsk Russia in June July 2011 The 18 revised full papers and 10 revised short papers presented were carefully reviewed and selected from 60 submissions The volume also

contains 5 invited papers covering a range of hot topics in computer science and informatics. The papers are organized in topical sections on foundations of program and system development and analysis, partial evaluation, mixed computation, abstract interpretation, compiler construction, computer models and algorithms for bioinformatics, programming methodology and software engineering, information technologies, knowledge based systems and knowledge engineering. Smart Spaces and Next Generation Wired/Wireless Networking Sergey Balandin, Roman Dunaytsev, Yevgeni Koucheryavy, 2010-08-05. We welcome you to the joint proceedings of the anniversary 10 NEW2AN 2010 Next Generation Teletra c and Wired Wireless Advanced Networking and Third ruSMART 2010 Are You Smart conferences held in St Petersburg, Russia during August 23-25, 2010. Originally, the NEW2AN conference was launched by ITC International Teletra c Congress in St Petersburg in June 1993 as an ITC Sponsored Regional International Teletra c Seminar. The first implementation was entitled Tra c Management and Routing in SDH Networks and was hosted by R D LONIIS. In 2002, the event received its current name NEW2AN. In 2008, NEW2AN was joined by a new counterpart in smart spaces, ruSMART, hence boosting interaction between researchers, practitioners and engineers from different areas of ICT. This year, in its 10th implementation, NEW2AN ruSMART is an established conference with a unique cross disciplinary mix of telecommunications science in Russia. NEW2AN ruSMART has always featured outstanding keynotes from universities and companies from Europe, USA and Russia. The 10th NEW2AN technical program addressed various aspects of next generation network architectures. New and innovative developments, enhanced signaling protocols, QoS mechanisms, cross layer optimization, tra c characterization were also addressed within the program. In particular, issues of QoS in wireless and IP based multiservice networks were dealt with, as well as financial aspects of future networks. It is also worth mentioning that emphasis was placed on wireless networks, including but not limited to cellular networks, wireless local area networks, personal area networks, mobile ad hoc networks and sensor networks. **Synthesis of Embedded Software** Sandeep Kumar Shukla, Jean-Pierre Talpin, 2010-08-05. Embedded software is ubiquitous today. There are millions of lines of embedded code in smart phones and even more in systems responsible for automotive control, avionics control, weapons control and space missions. Some of these are safety critical systems whose correctness, timely response and reliability are of paramount importance. These requirements pose new challenges to system designers. This necessitates that a proper design science based on constructive correctness be developed. Correct by construction design and synthesis of embedded software is done in a way so that post development verification is minimized and correct operation of embedded systems is maximized. This book presents the state of the art in the design of safety critical embedded software. It introduced readers to three major approaches to specification driven embedded software synthesis: construction, synchronous programming based approaches, models of computation based approaches and an approach based on concurrent programming with a co design focused language. It is an invaluable reference for practitioners and researchers concerned with improving the product development life cycle. **Readings in Hardware/Software Co-Design** Giovanni De

Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s

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