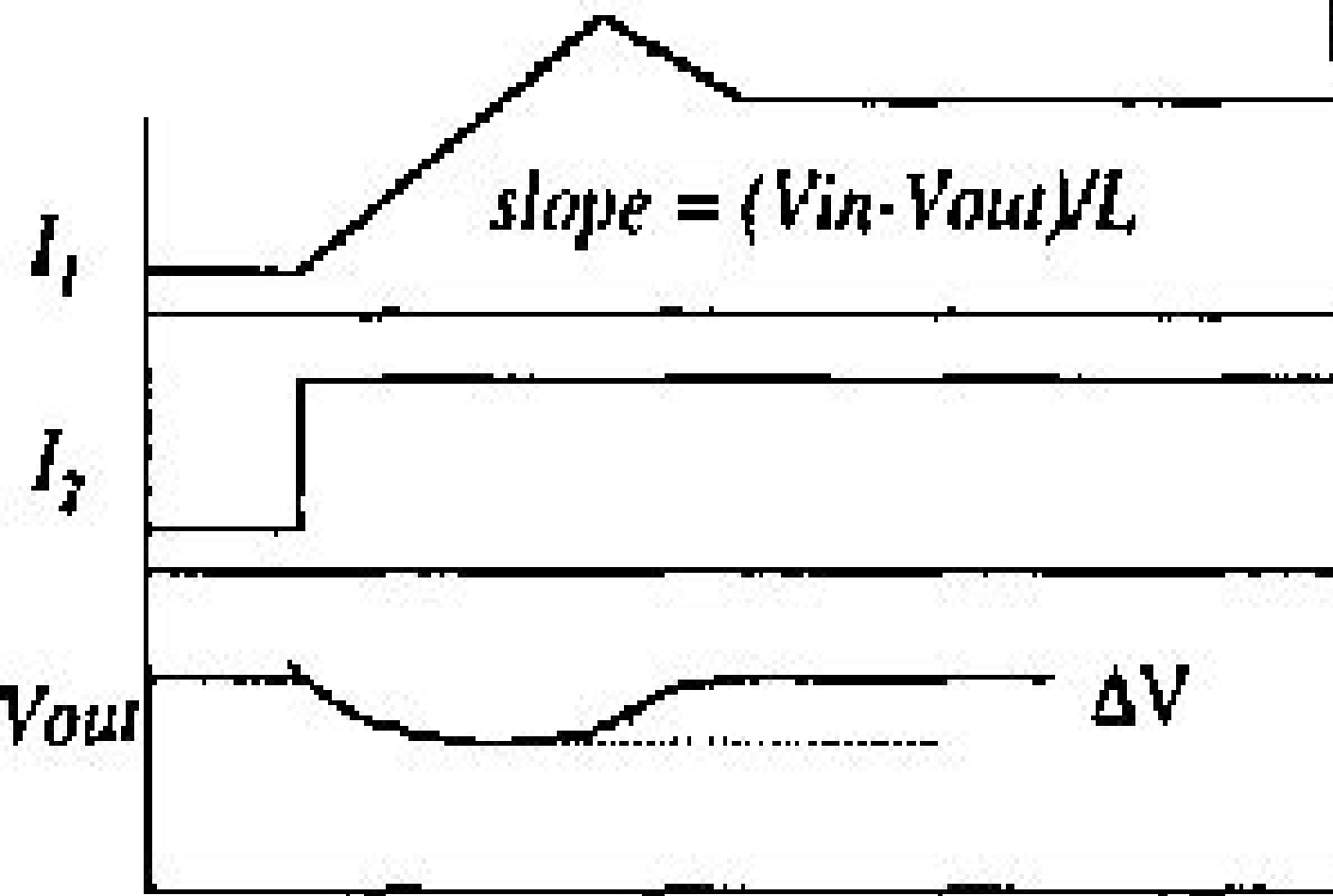
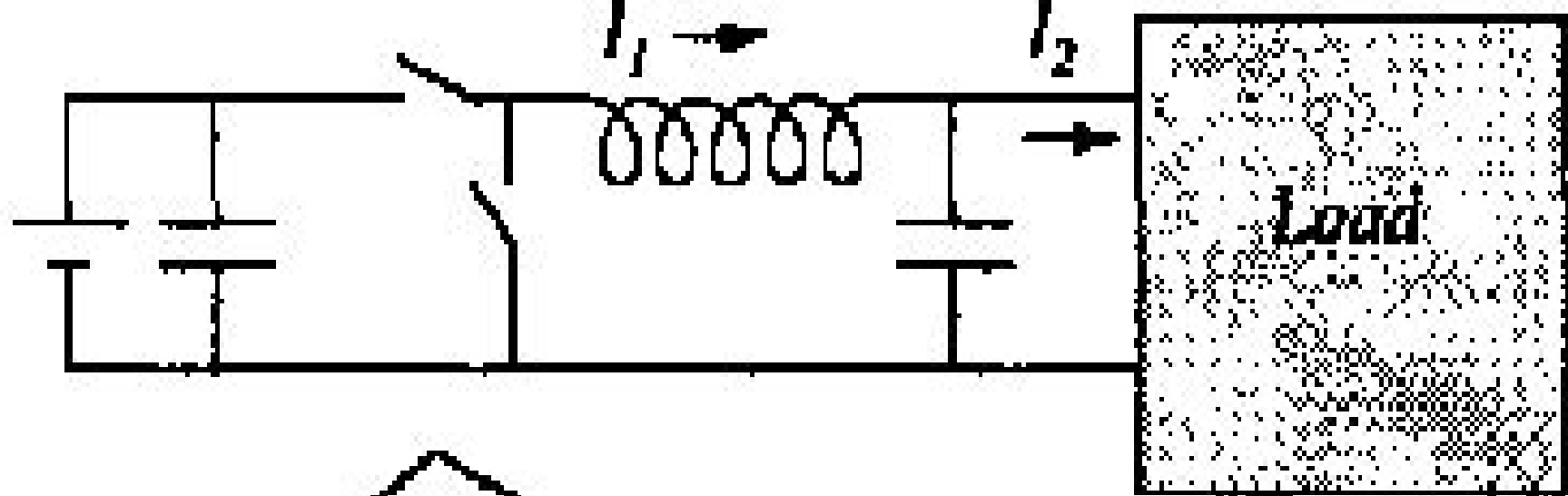




Design Of Microfabricated Inductors Power Electronics

Vijay K. Varadan, K. J. Vinoy, K. A. Jose



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Inductors and Transformers for Power Electronics Vencislav Cekov Valchev,Alex Van den Bossche,2018-10-03 Although they are some of the main components in the design of power electronic converters the design of inductors and transformers is often still a trial and error process due to a long working in time for these components Inductors and Transformers for Power Electronics takes the guesswork out of the design and testing of these systems and provides a broad overview of all aspects of design Inductors and Transformers for Power Electronics uses classical methods and numerical tools such as the finite element method to provide an overview of the basics and technological aspects of design The authors present a fast approximation method useful in the early design as well as a more detailed analysis They address design aspects such as the magnetic core and winding eddy currents insulation thermal design parasitic effects and measurements The text contains suggestions for improving designs in specific cases models of thermal behavior with various levels of complexity and several loss and thermal measurement techniques This book offers in a single reference a concise representation of the large body of literature on the subject and supplies tools that designers desperately need to improve the accuracy and performance of their designs by eliminating trial and error

Power Management Integrated Circuits Mona M. Hella,Patrick Mercier,2017-12-19 Power Management Integrated Circuits and Technologies delivers a modern treatise on mixed signal integrated circuit design for power management Comprised of chapters authored by leading researchers from industry and academia this definitive text Describes circuit and architectural level innovations that meet advanced power and speed capabilities Explores hybrid inductive capacitive converters for wide range dynamic voltage scaling Presents innovative control techniques for single inductor dual output SIDO and single inductor multiple output SIMO converters Discusses cutting edge design techniques including switching converters for analog RF loads Compares the use of GaAs pHEMTs to CMOS devices for efficient high frequency switching converters Thus Power Management Integrated Circuits and Technologies provides comprehensive state of the art coverage of this exciting and emerging field of engineering

Voltage Regulators for Next Generation Microprocessors Toni López,Reinhold Elferich,Eduard Alarcón,2010-12-01 This book deals with energy delivery challenges of the power processing unit of modern computer microprocessors It describes in detail the consequences of current trends in miniaturization and clock frequency increase upon the power delivery unit referred to as voltage regulator This is an invaluable reference for anybody needing to understand the key performance limitations and opportunities for improvement from both a circuit and systems perspective of state of the art power solutions for next generation CPUs

Metallic Films for Electronic, Optical and Magnetic Applications Katayun Barmak,Kevin Coffey,2014-02-13 Metallic films play an important role in modern technologies such as integrated circuits information storage displays sensors and coatings Metallic Films for Electronic Optical and Magnetic Applications reviews the structure processing and properties of metallic films Part one explores the structure of metallic films using characterization methods such as x ray diffraction and

transmission electron microscopy This part also encompasses the processing of metallic films including structure formation during deposition and post deposition reactions and phase transformations Chapters in part two focus on the properties of metallic films including mechanical electrical magnetic optical and thermal properties Metallic Films for Electronic Optical and Magnetic Applications is a technical resource for electronics components manufacturers scientists and engineers working in the semiconductor industry product developers of sensors displays and other optoelectronic devices and academics working in the field Explores the structure of metallic films using characterization methods such as x ray diffraction and transmission electron microscopy Discusses processing of metallic films including structure formation during deposition and post deposition reactions and phase transformations Focuses on the properties of metallic films including mechanical electrical magnetic optical and thermal properties

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Fred C. Lee, 1997 A sequel to Power Electronics Technology and Applications this text is targeted specifically towards the needs of practicing design engineers The focus is to provide the practicing engineer with up to date technology and emerging applications

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Design and Optimisation of Micro-fabricated Inductors for High-frequency Power Converters Ronan Thomas Meere, 2010 Trends in the miniaturisation of electronic products especially in the portable products area has sparked considerable interest in the miniaturisation of the energy processing electronics i e the power conversion circuits such as the switched mode power supply SMPS Unlike digital electronics which have benefited from miniaturisation and integration in microelectronics power conversion electronics have not significantly reduced in size This is directly due to the fact that power conversion requires energy storage components such as inductors and capacitors The value of the inductors and capacitors required can be reduced if the switching frequency of the power converter is increased In order to miniaturise the power converter the switching frequency must be increased so that passive components can be miniaturised and integrated Traditionally the inductive components have been difficult to integrate on chip This work focused on the design and fabrication of integrated inductors on silicon for very high frequency power conversion 20 u2013 100 MHz Initially an analytical model for micro inductors which was developed in

previous work was used to design inductors for operation up to 20 MHz. The designs selected for fabrication had a footprint area between 5 u2013 9 mm² and a predicted device efficiency of 90% and above. These models were validated by finite element analysis before fabrication. The fabricated prototypes displayed a low loss of inductance to 20 MHz and current handling ability to 0.5 A. The micro inductors were then interfaced with a high frequency dc/dc converter 20 u2013 100 MHz developed by NXP Semiconductor and achieved an inductor efficiency of 93% at 20 MHz. The maximum converter efficiency with the micro inductor was measured to be 78.5% which to date is highest quoted inductor on silicon device efficiency in a converter application at 20 MHz. Circuit equivalent lumped element models of the micro inductor for use in circuit simulation software were also developed. This equivalent circuit model includes elements such as capacitance which are not accounted for in the previously developed analytical model. The initial micro inductor devices performance was found to be comparable to commercial chip inductors for inductor efficiency when used in a converter. However if the micro inductor technology is to compete as a viable alternative to commercial devices it needed to reduce its footprint area dramatically. This was achieved by using an optimisation software engine to find the inductor designs with maximum efficiency for a given footprint area. The footprint of these optimised devices ranged from 0.5 u2013 2.5 mm² for a range of inductances to 200 nH. A range of optimised devices were fabricated and the measured optimised devices displayed a low loss of inductance to tens of MHz and good current handling capability. However measured dc resistance was found to be substantially higher than design due to issues in the fabrication process. The fabricated inductors also highlighted the trade offs that are introduced in micro inductor performance vs footprint area. This design trade off was also reflected in micro inductor performance in a converter. An optimised 2.5 mm² area device was tested in a dc/dc converter at 20 MHz which resulted in a slightly lower peak micro inductor efficiency of 90.5% than the previous larger devices. The fabricated optimised micro inductors achieve an inductance density inductance per unit area ranging from 66.110 nH/mm² and display current handling ability of 500mA for the 2.5 mm², 250mA for the 1.3 mm² and 150mA for the 0.5 mm² area device. For inductors aimed at power conversion applications this work shows a significant improvement to what is reported in literature in high frequency operation to tens of MHz inductance density and current handling.

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