

Digital Logic Design Principles



Norman Balabanian
Bradley Carlson

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Franklin P. Prosser, David E. Winkel



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Digital Design and Computer

Organisation D. Nasib S. Gill, J.B. Dixit, 2008-12 Digital Design and Computer Organization introduces digital design as it applies to the creation of computer systems. It summarizes the tools of logic design and their mathematical basis along with in depth coverage of combinational and sequential circuits. The book includes an accompanying CD that includes the majority of circuits highlighted in the text delivering you hands on experience in the simulation and observation of circuit functionality. These circuits were designed and tested with a user friendly Electronics Workbench package Multisim Textbook Edition that enables your progression from truth tables onward to more complex designs. This volume differs from traditional digital design texts by providing a complete design of an AC based CPU allowing you to apply digital design directly to computer architecture. The book makes minimal reference to electrical properties and is vendor independent allowing emphasis on the general design principles.

Digital Integrated Circuit Design Hubert Kaeslin, 2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry. Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more.

Digital Logic Design and Computer Organization with Computer Architecture for Security Nikrouz Faroughi, 2014-09-08 A COMPREHENSIVE GUIDE TO THE DESIGN ORGANIZATION OF MODERN COMPUTING SYSTEMS. Digital Logic Design and Computer Organization with Computer Architecture for Security provides practicing engineers and students with a clear understanding of computer hardware technologies. The fundamentals of digital logic design as well as the use of the Verilog hardware description language are discussed. The book covers computer organization and architecture, modern design concepts and computer security through hardware. Techniques for designing both small and large combinational and sequential circuits are thoroughly explained. This detailed reference addresses memory technologies, CPU design and techniques to increase performance, microcomputer architecture including plug and play device interface and memory hierarchy. A chapter on security engineering methodology as it applies to computer architecture concludes the book. Sample problems, design examples and detailed diagrams are provided throughout this practical resource. COVERAGE INCLUDES: Combinational circuits small designs, Combinational circuits large designs, Sequential circuits core modules, Sequential circuits small designs, Sequential circuits large designs, Memory Instruction set architecture, Computer architecture interconnection, Memory system, Computer architecture security.

VLSI Testing

Stanley Leonard Hurst, 1998. Hurst, an editor at the Microelectronics Journal, analyzes common problems that electronics engineers and circuit designers encounter while testing integrated circuits and the systems in which they are used and explains a variety of solutions available for overcoming them in both digital and mixed circuits. Among his topics are faults in digital circuits, generating a digital test pattern, signatures and self tests, structured design for testability, testing structured

digital circuits and microprocessors and financial aspects of testing The self contained reference is also suitable as a textbook in a formal course on the subject Annotation copyrighted by Book News Inc Portland OR The Digital Logic Handbook, 1966-67 Edition ,1966 *The Art of Digital Design* Franklin P. Prosser,David E. Winkel,1987 Hardware Logic Design The Digital Logic Handbook Digital Equipment Corporation,1966 **Digital System Test and Testable Design** Zainalabedin Navabi,2010-12-10 This book is about digital system testing and testable design The concepts of testing and testability are treated together with digital design practices and methodologies The book uses Verilog models and testbenches for implementing and explaining fault simulation and test generation algorithms Extensive use of Verilog and Verilog PLI for test applications is what distinguishes this book from other test and testability books Verilog eliminates ambiguities in test algorithms and BIST and DFT hardware architectures and it clearly describes the architecture of the testability hardware and its test sessions Describing many of the on chip decompression algorithms in Verilog helps to evaluate these algorithms in terms of hardware overhead and timing and thus feasibility of using them for System on Chip designs Extensive use of testbenches and testbench development techniques is another unique feature of this book Using PLI in developing testbenches and virtual testers provides a powerful programming tool interfaced with hardware described in Verilog This mixed hardware software environment facilitates description of complex test programs and test strategies

Foundations of Embedded Systems Alexander Barkalov,Larysa Titarenko,Małgorzata Mazurkiewicz,2019-02-04 This book is devoted to embedded systems ESs which can now be found in practically all fields of human activity Embedded systems are essentially a special class of computing systems designed for monitoring and controlling objects of the physical world The book begins by discussing the distinctive features of ESs above all their cybernetic physical character and how they can be designed to deliver the required performance with a minimum amount of hardware In turn it presents a range of design methodologies Considerable attention is paid to the hardware implementation of computational algorithms It is shown that different parts of complex ESs could be implemented using models of finite state machines FSMs Also field programmable gate arrays FPGAs are very often used to implement different hardware accelerators in ESs The book pays considerable attention to design methods for FPGA based FSMs before the closing section turns to programmable logic controllers widely used in industry This book will be interesting and useful for students and postgraduates in the area of Computer Science as well as for designers of embedded systems In addition it offers a good point of departure for creating embedded systems for various spheres of human activity **Design of Computers and Other Complex Digital Devices** Sunggu Lee,2000 Uniquely this advanced digital logic design textbook has as its design target an actual commercial 8 bit processor the Intel 8080 serving as an extended example of the effective use of VHDL a hardware description language EPG As field programmable gate arrays and the ASM Algorithmic State Machine method to achieve this end Part I provides a refresher course in basic digital logic design Part II examines the use of programmable logic devices hardware description

languages and the ASM method for implementation of general algorithms in hardware Part III details the microprocessor s design and implementation specifications Appends an overview of the Intel 8080 instruction set and suggested lab projects for junior and senior level students in electrical and computer engineering

Digital Design John F. Wakerly,1990 This popular volume provides a solid foundation in the elements of basic digital electronics and switching theory that are used in most practical digital design today and builds on that theory with discussions of real world digital components design methodologies and tools Covers a full range of topics number systems and codes digital circuits combinational logic design principles and practices combinational logic design with PLDs sequential logic design principles and practices sequential logic design with PLDs memory and additional real world topics e g computer aided engineering tools design for testability estimating digital system reliability and transmission lines reflections and termination This edition introduces PLDs as soon as possible emphasizes CMOS logic families and introduces digital circuits in a strongly technology independent fashion covers the latest Generic Array Logic GAL devices offers expanded coverage of ROM and RAM system level design and provides additional design examples For those needing a solid introduction or review of the principles and practices of modern digital design Previously announced in Oct 1992 PTR Catalogue

Digital Networks and Computer Systems Taylor L. Booth,1978 Introduction to digital systems Representation of information in digital form Representation of basic logic operations Operations on digital information Combinational logic circuit elements Switching algebra and logic network realization Minimization of combinational logic networks Flip flops registers and basic information transfers Introduction to the analysis and design of synchronous sequential networks Input output and memory elements Digital system representation and design Stored program information processors and computers The computing process and machine language programs Assembler languages and assemblers Programming languages and compilers Appendices Index

Modern Digital Systems Design John Y. Cheung,Jon G. Bredeson,1990

Microprocessors and Small Digital Computer Systems for Engineers and Scientists Granino Arthur Korn,1977

Technical Books in Print ,1964

Digital Transformation of Enterprise Architecture Vivek Kale,2019-07-08 In this book Vivek Kale makes an important contribution to the theory and practice of enterprise architecture this book captures the breadth and depth of information that a modern enterprise architecture must address to effectively support an agile enterprise This book should have a place in every practicing architect s library John D McDowall Author of Complex Enterprise Architecture Digital Transformation of Enterprise Architecture is the first book to propose Enterprise Architecture EA as the most important element after Business Models for digital transformation of enterprises This book makes digital transformation more tangible by showing the rationale and typical technologies associated with it and these technologies in turn reveal the essence of digital transformation This book would be useful for analysts designers and developers of future ready agile application systems This book proposes that it is the perennial quest for interoperability portability scalability availability etc that has directed and driven the evolution of the

IT IS industry in the past 50 years It is this very quest that has led to the emergence of technologies like service oriented cloud and big data computing In addition to the conventional attributes of EA like interoperability scalability and availability this book identifies additional attributes of mobility ubiquity security analyticity and usability This pragmatic book Identifies three parts effort for any digital transformation Business Models Enterprise Architectures and Enterprise Processes Describes eight attributes of EA interoperability scalability availability mobility ubiquity security analyticity and usability Explains the corresponding technologies of service oriented cloud big data context aware Internet of Things IoT blockchain soft and interactive computing Briefs on auxiliary technologies like integration virtualization replication spatio temporal databases embedded systems cryptography data mining and interactive interfaces that are essential for digital transformation of enterprise architecture Introduces interactive interfaces like voice gaze gesture and 3D interfaces Provides an overview of blockchain computing soft computing and customer interaction systems Digital Transformation of Enterprise Architecture proposes that to withstand the disruptive digital storms of the future enterprises must bring about digital transformation i e a transformation that affects an exponential change amplification or attenuation in any aspect of the constituent attributes of EA It proposes that each of these technologies service oriented cloud big data context aware IoT blockchain soft and interactive computing bring about digital transformation of the corresponding EA attribute viz interoperability scalability availability mobility ubiquity security analyticity and usability **Cumulative Book Index** ,1998 A world list of books in the English language Books in Print Supplement ,2002 **The British National Bibliography** Arthur James Wells,2003

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