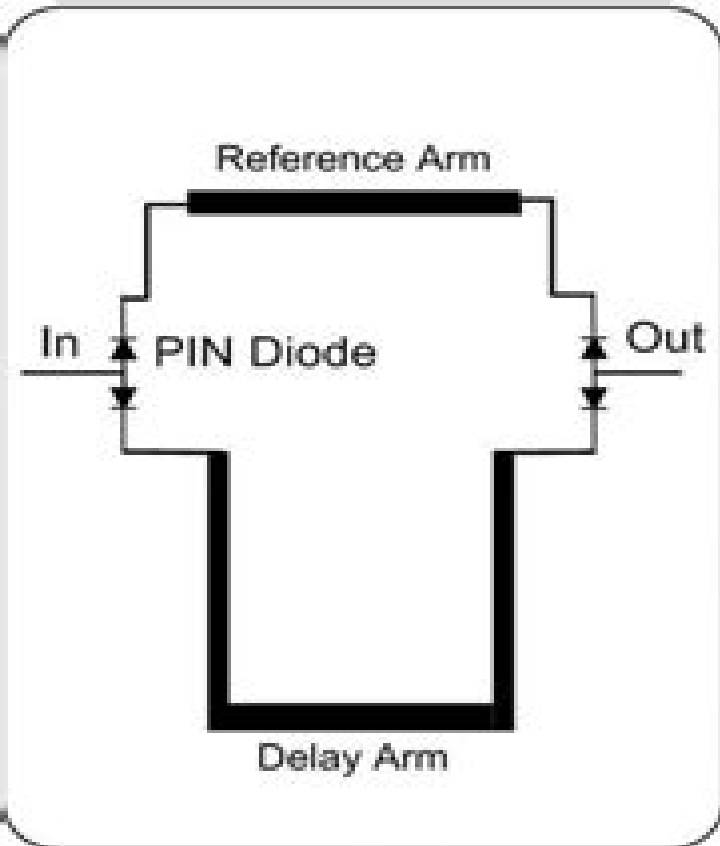
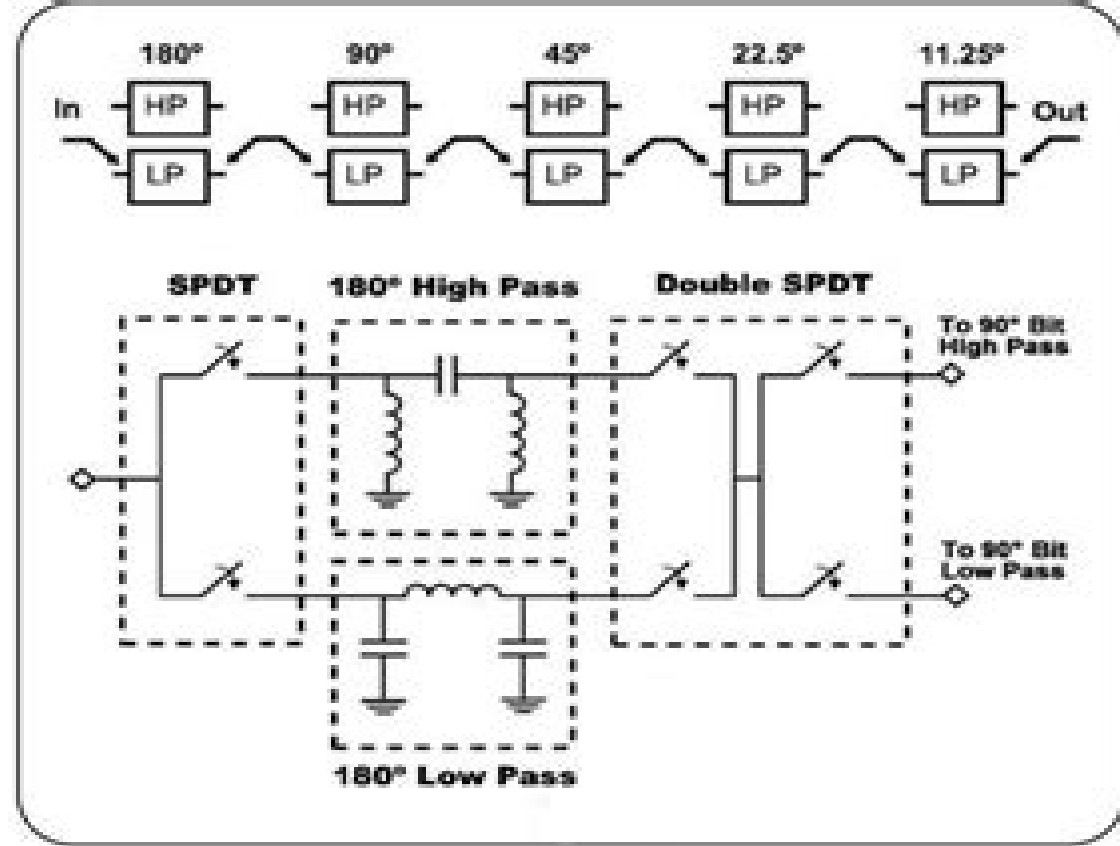
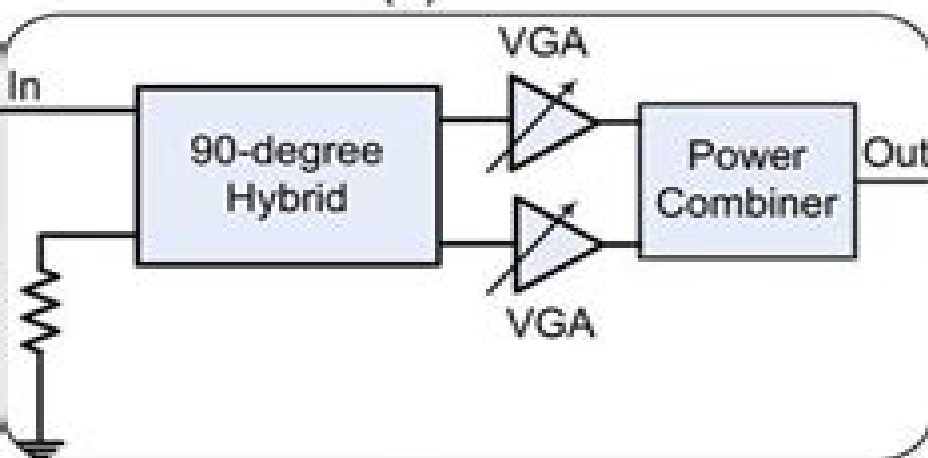




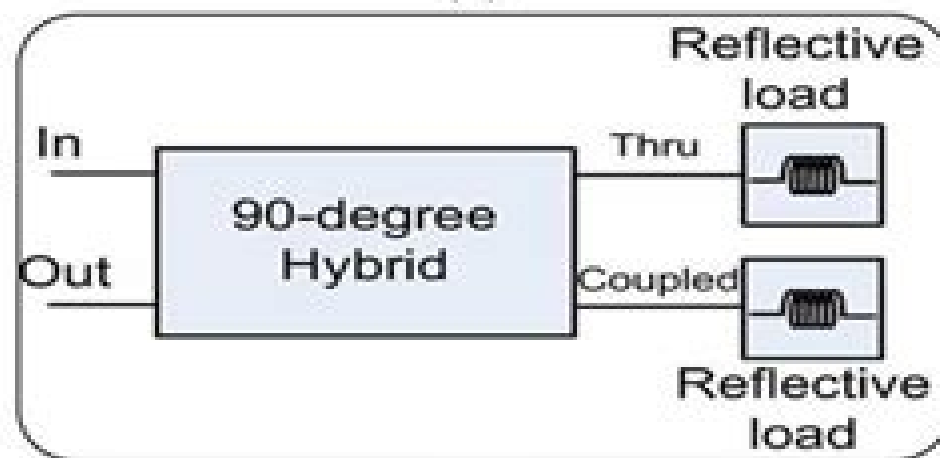
(a)



(b)



(c)



(d)

# Digital Phase Shifters Cernex

**L. J Lavedan (Jr), B. R. Savage, SPERRY  
MICROWAVE ELECTRONICS  
CLEARWATER FL., J Brown (Jr)**

## **Digital Phase Shifters Cernex:**

Microwave Journal ,2009      **Design of Digital Phase Shifters for Multipurpose Communication Systems** Binboga Siddik Yarman,2022-09-01 This book aims to cover a new emerging need in designing digital phase shifter for modern communication systems With the advancement of new generation mobile communication systems directed beams of antenna arrays save a substantial amount of power as well as improve the communication quality In this regard beam forming circuits such as digital phase shifters DPS constitute essential parts of the antenna array systems Therefore this book is devoted to the design of digital phase shifters for various communications systems Nowadays phase array systems demand compact phase shifters suitable for chip implementation with wide phase range and broad frequency band Each chapter of this book is organized as stand alone in such a way that the reader requires no specific background acquired from the other chapters For each phase shifter topology introduced in this book the reader is furnished with explicit design equations to construct the circuit under consideration Furthermore design equations are programmed using MATLAB to assess the electrical performance of the phase shifters with ideal and lossy components MATLAB design programs are given at the end of each chapter as appendices and provided as soft copy on the web page of the book In chapters 12 and 14 MMIC layouts for the lattice and T section based DPS are provided for the readers It is hoped that an interested reader can immediately identifies the optimum phase shifter topology for the need under consideration with its estimated electric performance      Digitally Controlled Distributed Phase Shifter ,1993 A digitally controlled distributed phase shifter is comprised of N phase shifters Digital control is achieved by using N binary length weighted electrodes located on the top surface of a waveguide A control terminal is attached to each electrode thereby allowing the application of a control signal The control signal is either one or two discrete bias voltages The application of the discrete bias voltages changes the modal index of a portion of the waveguide that corresponds to a length of the electrode to which the bias voltage is applied thereby causing the phase to change through the underlying portion of the waveguide The digitally controlled distributed phase shift network has a total phase shift comprised of the sum of the individual phase shifters      The Omega Digital Phase Shifter, Model 2 J. J. Wilson,NAVAL ELECTRONICS LAB CENTER SAN DIEGO CA.,1968 A digital phase shifter was designed and built for use at transmitting stations of the Omega navigation system The equipment uses integrated logic circuitry throughout and is intended to maintain proper phase of Omega signals Author      A High Speed Digital, X-band Phase Shifter Douglas H. Cortelyou,1965

**THE OMEGA DIGITAL PHASE SHIFTER.** John J. Wilson,NAVAL ELECTRONICS LAB CENTER FOR COMMAND CONTROL AND COMMUNICATIONS SAN DIEGO CALIF.,1967 A Digital Phase Shifter was developed for use with the Omega long range navigation system When used in conjunction with an error signal A D Converter it maintains Omega carrier frequency phase to within 0.225 of a centicycle Further development using integrated logic circuits is recommended Author      **Development of a Fast Switching, High Power Digital Phase-shifter at X-band** Robert

Addington, WESTERN MICROWAVE LABS INC SANTA CLARA CALIF., 1965 The development of a four bit ferrite phase shifter is reported A method of designing a fast switching latchable ferrite switch of stripline construction is described This method incorporates several new design techniques which are a direct product of this development program The final data is presented for a high power latchable digital phase shifter and reference is made to the characteristics of its low power counterpart Author L-BAND PHASE-SHIFTING TECHNIQUES. HUGHES AIRCRAFT CO CULVER CITY CALIF., 1962 A discussion of techniques applicable to obtain digital phase shift without continuously applied current is given 2 60 Hughes Aircraft Co Culver City Calif L BAND PHASE SHIFTING TECHNIQUES Final engineering rept 1 July 61 16 Mar 62 16 Mar 62 15p incl illus table 2 refs Contract NObsr 81378 Proj SR 0080302 Unclassified report DESCRIPTORS Phase shifters Digital systems Ferrites Magnetic fields Measurement L band A discussion of techniques applicable to obtain digital phase shift without continuously applied current is given Also a phase shifter developed during the program to which the digital techniques are applicable is described The construction and method of operation of a prototype digital phase shifter is described and the performance is given Work was performed to determine the cause of variation of insertion loss between the prototype phase shifters Author Real Time Digital Phase Shifters Utilizing 4-port Single-junction Latching Circulators James W. McManus, Bruce K. Horne, SPERRY MICROWAVE ELECTRONICS CLEARWATER FL., 1966 The devices under development are real time delay digital phase shifters utilizing 4 port single junction waveguide latching circulators A preliminary theoretical cavity investigation based on a cavity mode excitation model for the circulator is described Several mode excitation models are discussed and empirical data presented The role of ferrimagnetic material parameters and a method for rapid measurement of rf characteristics of the circulator are discussed Data are presented on circulator operation in X band 8 2 12 4 GHz **Insertion Phase Correction of Phase Shifters by Presetting Binary Counters** George A. Hrivnak, Vincent J. Organic, OFFICE OF THE SECRETARY OF THE ARMY WASHINGTON D C., 1972 The patent relates to a method of inserting into a phase shifter the necessary amount of insertion phase shift to correct for departure of phase shift from an acceptable value for that phase shifter resulting from normal inadequate production tolerances The method involves using in the logic driver circuit for the phase shifter a counter whose digital output determines the magnitude of the phase shift command applied to the phase shifter The input to the counter is preset in accordance with the amount of phase shift correction determined from the phase shift measuring test to be necessary to correct for the undesired phase deviation in the manufacturing process **Novel Methods of Digital Phase Shifting to Achieve Arbitrary Values of Time Delay** A. Mohammed, Defence Research Establishment Atlantic, Canada. Department of National Defence. Research and Development Branch, 1985 In many signal processing applications such as in underwater acoustic array beamforming the need arises to implement digital phase shifters Conventional methods of implementation make use of digital interpolation and decimation to derive FIR Finite duration Impulse Response realizations Such filters however are capable of providing delays that are only

rational fractions of the unit delay To obtain delays that are arbitrary factors of the unit delay two novel methods are presented the first method makes use of a windowing technique and the second method makes use of a frequency sampling approach In both methods the constraint of exactly linear phase is relaxed and the departures from linear phase are kept very small To ensure that the new phase shifters attain a high level of performance comprehensive error measures have been developed and applied these performance measures consists of a normalized rms error the phase delay error and the group delay error Moreover these error measures are applicable to any method of designing digital phase shifters In addition for the frequency sampling designs the concept of an effective filter length is introduced this concept takes into account the wraparound error that arises in fast convolution signal processing operations Aside from the presentation of the design procedures and error measures examples are included to illustrate the salient features of the two new methods Keywords include Digital Phase Shifters Windowing Frequency Sampling Time Delay Realization and Beamforming Time Delay

A *Broadband Digital Diode Phase Shifter Using a Matrix Interconnection of Quadrature Hybrid Couplers* C. W Young (Jr), NAVAL RESEARCH LAB WASHINGTON D C., 1975 An analysis of 2 bit digital diode phase shifter using four PIN diodes and a matrix arrangement of four 3 dB quadrature hybrids has been performed The 0 degrees 90 degrees 180 degrees and 270 degrees differential phase states were achieved by using the 90 degrees phase relationship of the hybrid coupler outputs and proper combinations of open circuits and short circuits provided by the diodes with the appropriate bias The phase state determined at which of two ports of the hybrid matrix the output occurred thus an additional SPDT or transfer switch was needed to complete the phase shifter Broad bandwidths can be achieved since the bandwidth of the phaser is limited by the hybrid bandwidth and the band over which the diode open and short can be made to track plus the bandwidth of the single transfer switch With the addition of a second transfer switch in conjunction with a 45 degrees Schiffman constant phase shift section a 3 bit or 45 degrees resolution phase shifter can be obtained The effects of certain component errors and imperfections on the performance of the phaser have been presented a generalized method for an exact error analysis has been derived

**Broadband Digital Diode Phase Shifter Using a Matrix Interconnection of Quadrature Hybrid Couplers**, 1975

**Experimental Results for a Digital Ferrite Phase Shifter Operating in the Circular TE<sub>01</sub> Mode** George Herbert Day, 1968

**A High Linearity X-band SOI CMOS Digitally-controlled Phase Shifter**, 2015 High Power C Band Phase Shifters L. J Lavedan (Jr), B. R. Savage, SPERRY MICROWAVE ELECTRONICS CLEARWATER FL., J Brown (Jr), 1968 The report outlines all tasks performed under this program and includes techniques performed in developing high power phase shifters and a description of the hardware delivered with test results Structural studies have been carried out to assure compatibility of designs with phased array requirements Cross sections have been reduced to a minimum by careful location of cooling structures Remote driving techniques were developed with emphasis on switching efficiency A scheme of triggering a single bit phase shifter is outlined in detail Studies of flux drive led to a better understanding of

practical and theoretical limitations these limitations being discussed in detail A direct comparison of single and multibit phase shifters is given with emphasis on microwave performance and relative cost Author **Study of Phase Shifters** G. J. House, Electrical Engineering and Electronics, 1977 **Direct Digital Phase Shift by DDS Rf Source**, 1993

Development of X-band Resonance Phase Shifters E. Stern, A. Christopher, MASSACHUSETTS INST OF TECH LEXINGTON LINCOLN LAB., 1964 A successful technique was devised for the design and construction of composite magnetic loop structures Microwave circuit designs were found which provide good performance and at the same time match into composite loop structures One square loop material was developed with properties suitable for temperature stabilization of phase shifters The first attempt to assemble a phase shifter was not successful Author *Electronic phase shifter* Jarvis Duane Gantt, 1961

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