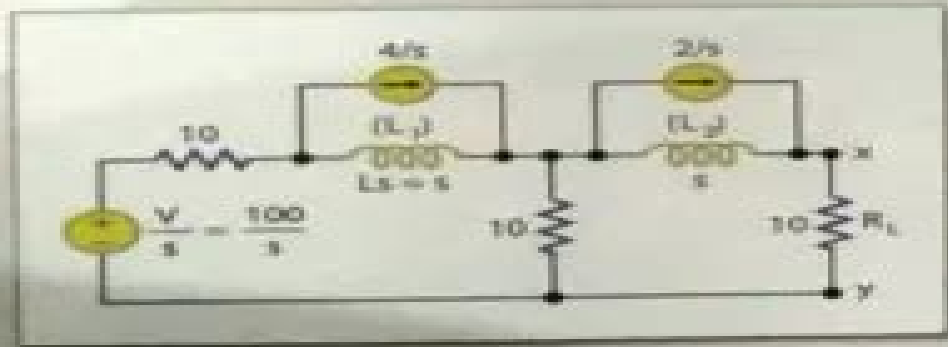
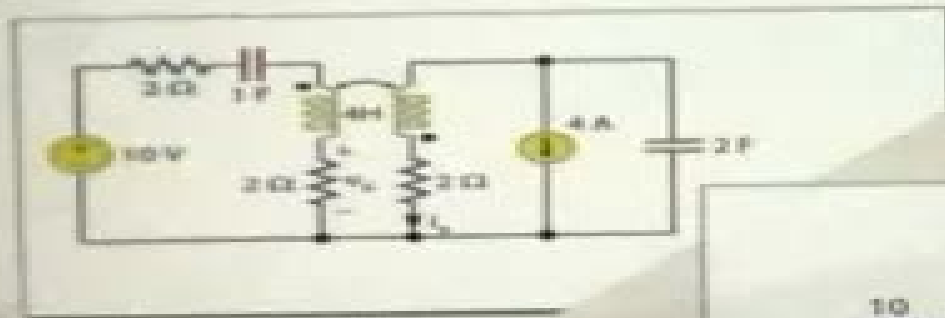


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ANALYSIS AND SYNTHESIS

(REVISED NEW EDITION)

Abhijit Chakrabarti



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requirements and the data dominated characteristics of this domain we have adopted a target architecture style that is compatible with modern embedded processors and we have developed a systematic step wise methodology to make the exploration and optimization of such applications feasible in a source to source precompilation approach

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