ia-64 linux kernel

design and implementation

david mosberger • stéphane eranian



Authorisative and complete description of the Linux kernell for IA-64/IPT (Barrium Processor Family)

Provides an in-depth discussion of the ideas and concepts behind the Linux hordware abstraction interface, the IA-64 design space, and their actual realizations

From the lead architects and developers of the IA-64 Linux kernel.

Foreword by Bruce Perens



Ia 64 Linux Kernel Design And Implementation

Robert Love

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explanations with clear complete example programs You ll find descriptions of over 500 system calls and library functions and more than 200 example programs 88 tables and 115 diagrams You ll learn how to Read and write files efficiently Use signals clocks and timers Create processes and execute programs Write secure programs Write multithreaded programs using POSIX threads Build and use shared libraries Perform interprocess communication using pipes message queues shared memory and semaphores Write network applications with the sockets API While The Linux Programming Interface covers a wealth of Linux specific features including epoll inotify and the proc file system its emphasis on UNIX standards POSIX 1 2001 SUSv3 and POSIX 1 2008 SUSv4 makes it equally valuable to programmers working on other UNIX platforms The Linux Programming Interface is the most comprehensive single volume work on the Linux and UNIX programming interface and a book that s destined to become a new classic TCP/IP Architecture, Design, and Implementation in Linux Sameer Seth, M. Ajaykumar Venkatesulu, 2009-01-23 This book provides thorough knowledge of Linux TCP IP stack and kernel framework for its network stack including complete knowledge of design and implementation Starting with simple client server socket programs and progressing to complex design and implementation of TCP IP protocol in linux this book provides different aspects of socket programming and major TCP IP related algorithms In addition the text features netfilter hook framework a complete explanation of routing sub system IP QOS implementation and Network Soft IRQ This book further contains elements on TCP state machine implementation TCP timer implementation on Linux TCP memory management on Linux and debugging TCP IP stack using lcrash <u>Design and Implementation of the MTX Operating System K. C.</u> Wang, 2015-06-29 This course tested textbook describes the design and implementation of operating systems and applies it to the MTX operating system a Unix like system designed for Intel x86 based PCs Written in an evolutional style theoretical and practical aspects of operating systems are presented as the design and implementation of a complete operating system is demonstrated Throughout the text complete source code and working sample systems are used to exhibit the techniques discussed The book contains many new materials on the design and use of parallel algorithms in SMP Complete coverage on booting an operating system is included as well as extending the process model to implement threads support in the MTX kernel an init program for system startup and a sh program for executing user commands Intended for technically oriented operating systems courses that emphasize both theory and practice the book is also suitable for self study Itanium **Architecture for Programmers** James S. Evans, Gregory L. Trimper, 2003 Step by step guide to assembly language for the 64 bit Itanium processors with extensive examples Details of Explicitly Parallel Instruction Computing EPIC Instruction set addressing register stack engine predication I O procedure calls floating point operations and more Learn how to comprehend and optimize open source Intel and HP UX compiler output Understand the full power of 64 bit Itanium EPIC processors Itanium reg Architecture for Programmers is a comprehensive introduction to the breakthrough capabilities of the new 64 bit Itanium architecture Using standard command line tools and extensive examples the authors illuminate the

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18 2018 and of the 13th International Parallel Tools Workshop held in Dresden Germany during September 2 3 2019 The workshops are a forum to discuss the latest advances in parallel tools for high performance computing High performance computing plays an increasingly important role for numerical simulation and modeling in academic and industrial research At the same time using large scale parallel systems efficiently is becoming more difficult A number of tools addressing parallel program development and analysis has emerged from the high performance computing community over the last decade and what may have started as a collection of a small helper scripts has now matured into production grade frameworks Powerful user interfaces and an extensive body of documentation together create a user friendly environment for parallel tools

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